ORDER NO. AVS87072040S8

Service Manual

Colour CCTV Cameras

WV-CD130/WV-CD132/WV-CD134

White Balance Controller

WV-RC130

CONTENTS

LIST OF MODIFICATION 1

CAMERA ADJUSTMENT 2

LOCATION OF TEST POINTS AND ADJUSTING CONTROLS 14

POWER BOARD 16

HEAD BOARD 17

GEN-LOCK/SYNC BOARD 19

PROCESS BOARD 21

MICROPROCESSOR BOARD 23

MOTHER BOARD 25

REPLACEMENT PARTS LIST 27

National/Panasonic

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LIST OF MODIFICATION

◆Power Board (on page 16)

- Power board has been corrected as shown on page 16.

◆Head Board (on page 17,18)

- Printing of Head board has been changed since April products,1987.
- VR1,VR2,VR3,VR4,VR5 have been changed to prevent making a short circuit by contacting these VR each other since June products,1987.
- The rest of part numbers have been changed as shown in the parts list.

◆CCD Board (on page 17,18)

- Printing of CCD board has been changed since April products,1987.
- R13 has been changed to prevent horizontal line noise.
- The rest of part numbers have been changed as shown in the parts list.

◆Gen-lock/Sync Board (on page 19,20)

- Printing of Gen-Lock/Sync board has been changed since April products,1987.
- C27 has been changed to prevent interference on the monitor when power is turned on since April products,1987.
- The rest of part numbers have been changed as shown in the parts list.

◆Process Board (on page 21,22)

- Printing of Process board has been changed since April products,1987.
- R53 has been changed to change aperture gain since April products,1987.
- R27 has been changed to change chroma clip level.
- The rest of part numbers have been changed as shown in the parts list.

◆Process VIC-1 Board (on page 21,22)

- Printing of Process VIC-1 board has been changed since April products,1987
- The part numbers have been changed as shown in the parts list.

◆Process VIC-2 Board (on page 21,22)

- Printing of Process VIC-2 board has been changed since April products,1987.
- R3 has been changed to change vertical aperture gain.
- The rest of part numbers have been changed as shown in the parts list.

◆Process VIC-3 Board (on page 21,22)

- Printing of Process VIC-3 board has been changed since April products,1987.
- CT1 has been changed to adjust chroma phase more easily since April products,1987.
- The rest of part numbers have been changed as shown in the parts list.

◆Microprocessor Board (on page 23,24)

- Printing of Microprocessor board has been changed since April products,1987.
- R200 has been changed to chip type resistor ,which will be applied August products,1987.
- The rest of part numbers have been changed as shown in the parts list.

◆Mother Board (on page 25,26)

- Printing of Mother board has been changed since April products,1987.
- The part numbers have been changed as shown in the parts list.

◆Control Board (on page 25,26)

- R1,R2 have been changed to change adjustment range of manual white balance.
- R3 has been changed to prevent that SES mode control does not work, which has been applied since March products,1987.
- The rest of part numbers have been changed as shown in the parts list.

4-2 Camera Adjustment

■ This camera adjustment has been revised.

Note : Before adjusting the camera unit , adjust the camera flangeback briefly while referring to (28) Frangeback adjustment on page 48.

(1) + 12V adjustment
(Power board for WV-CD130/WV-CD134)

Test point :	Pin 2 of CN401	Power board
Adjust :	R408 (+ 12V ADJ)	Power board

- o Connect the digital voltmeter (DVM) probe between pin 2 of CN401 on the Power board and chassis.
- o Adjust R408 (+ 12V ADJ) on the Power board for + 12V.

(2) + 4.9V Adjustment

Test point:	TP4 (+ 4.9V)	Mother board
Adjust:	VR1 (+ 4.9V ADJ)	Mother board

- o Connect the digital voltmeter (DVM) to TP4 (+ 4.9V).
- o Adjust VR1 (+ 4.9V ADJ) for 4.9V ±0.05V.

(3) White balance control adjustment

Test points :	TP2 (+ 1.8V)	Mother board
	TP9 (R/B)	Mother board
Adjust :	VR1 (White balance control)	Control board

- o Connect the digital voltmeter to TP2 (+ 1.8V) and note the voltage of TP2.
- o Connect the digital voltmeter to TP9 (R/B).
- o Adjust VR1 (white balance control) on the control board so that the voltage of TP9 becomes same voltage as TP2.

(4) SW OSC adjustment

Test point :	Cathode of D1 (68KHz)	Mother board
Adjust :	VR2 (SW OSC ADJ)	Mother board

- o Connect the frequency counter to cathode of D1 (68KHz).
- o Adjust VR2 (SW OSC ADJ) for 68KHz ± 0.5KHz.

(5) Subcarrier adjustment

Test point:	Pin 8 of CN5	Mother board
Adjust:	VR2 (SC ADJ)	Gen-lock/sync board

- o Connect the frequency counter to Pin 8 of CN5 on the Mother board.
- o Adjust VR2 (SC ADJ) for 4.433618 ± 10 Hz.

(6) EXT SC VCO bias adjustment

Test point :	Pin 8 of CN5	Mother board
Adjust :	VR1 (EXT SC VCO BIAS)	Gen-lock/sync board

- o Supply the black and white signal from the signal generator to the Gen-lock input connector on the rear of camera.
- o Connect the frequency counter to Pin 8 of CN5 on the Mother board.
- o Adjust VR1 (EXT SC VCO BIAS) on the Gen-lock/sync board for 4.433618 ± 10Hz.

(7) Gen-lock VCO adjustment

Test point:	TP1 (EXT SC VCO)	Gen-lock/sync board
Adjust:	L6 (GEN-LOCK VCO)	Gen-lock/sync board

- o Keep the color bar or black burst signal from the special effects generator (SEG) or the signal generator to the Gen-lock input connector on the rear of the camera.
- o Connect the digital voltmeter (DVM) to TP1.
- o Adjust L6 (GEN-LOCK VCO) for 4V ± 0.1V.
- o Disconnect the color bar or black burst signal from Gen-lock input connector on the rear of the camera.

(8) INT SYNC VCO adjustment

Test point :	TP2 (INT SYNC VCO)	Gen-lock/sync board
Adjust :	L7 (INT SYNC VCO)	Gen-lock/sync board

- o Connect the digital voltmeter to TP2 (INT SYNC VCO).
- o Adjust L7 (INT SYNC VCO) for 4.0 ± 0.1V.

(9) V. transfer pulse adjustment

Test points:	TP3 (øVM2)	Head board
	TP4 (øVM1)	Head board
	VR1 (øVM1 ADJ)	Head board
Adjusts:	VR4 (øVH ADJ)	Head board
	VR5 (øVM2 ADJ)	Head board

- o Cap on the lens.
- o Connect the oscilloscope to TP3 (øVM2) and set the oscilloscope to V. rate.
- o Set the input of the oscilloscope to the DC-coupled.
- o Adjust VR4 (øV_H ADJ) so that the high level of the V. transfer pulse is 13V. (Fig. 4-2-1)

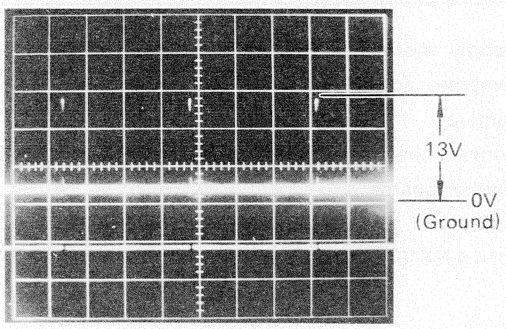


Fig. 4-2-1

- o Confirm that the black horizontal lines are not observed in the Zone I on the color video monitor. (Fig. 4-2-2 and Fig. 4-2-3)

  Note : Adjust the contrast brightness of the color video monitor for better viewing.

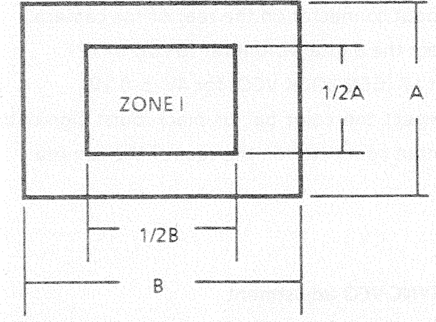
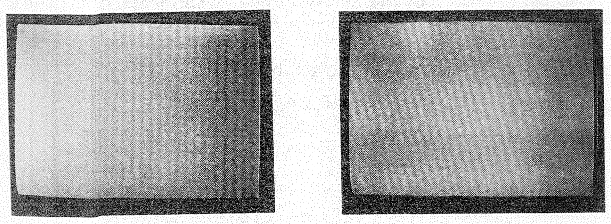


Fig. 4-2-2



NO GOOD GOOD

Fig. 4-2-3

- o If the black horizontal lines are observed in the Zone I, readjust VR4 (øV_H ADJ) so that the black horizontal lines disappear from the Zone I. After adjustment, confirm that the high level of the V. transfer pulse is 13V ± 0.3V. (Fig. 4-2-4)

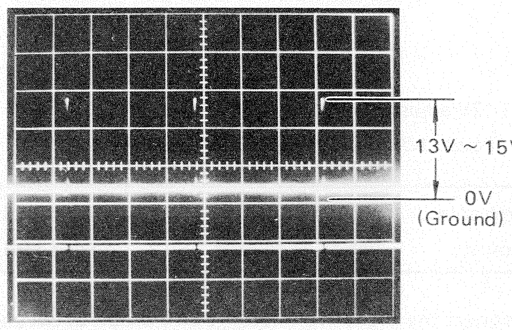


Fig. 4-2-4

- o Set the oscilloscope to H. rate.
- o Adjust VR5 (øVM2 ADJ) so that the middle level of the V. transfer pulse at TP3 (øVM2) is 3V ± 0.1V. (Fig. 4-2-5)

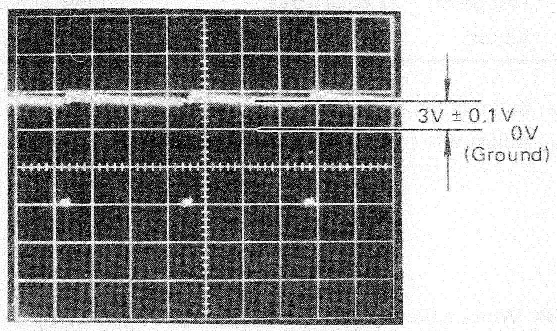


Fig. 4-2-5

- o Set the oscilloscope to H.rate.
- o Connect the oscilloscope to TP4 (øVM1).
- o Adjust VR1 (øVM1 ADJ) so that the middle level of the V.transfer pulse at TP4 (øVM1) is 3V ± 0.1V. (Fig. 4-2-6)

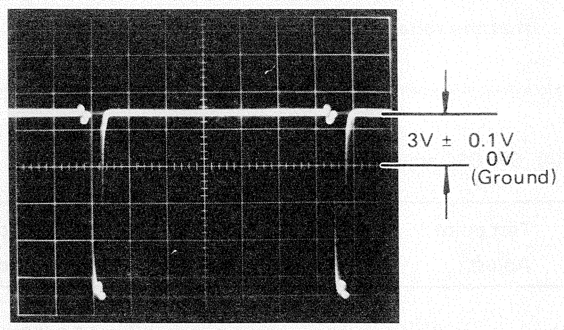


Fig. 4-2-6

(10) f_H PLL adjustment

Test point:	TP1 (f_H VCO)	Head board
Adjust:	L3 (f_H VCO)	Head board

- o The extension board YFVKZD130E2A is required for this adjustment.
- o Disconnect the CN2 on the Head board.
- o Connect the digital voltmeter (DVM) to TP1 (f_H VCO).
- o Adjust L3 (fH VCO) for 3 ± 0.1V.
- o Connect the CN2 on the Head board.

(11) CCD bias coarse adjustment

Adjust:	VR2 (CCD BIAS)	Head board
Observe:	Color video monitor	

- o Turn SW1 (POWER SWITCH) to OFF position.
- o Disconnect the automatic iris control (AIC) connector from the camera.
- o Change AIC lens to manual iris lens (f1.4 16mm C-mount).
- o Turn SW1 (POWER SWITCH) to ON position.
- o Aim the camera at a 40-watt lamp placed in front of the dark background. (Fig. 4-2-7)

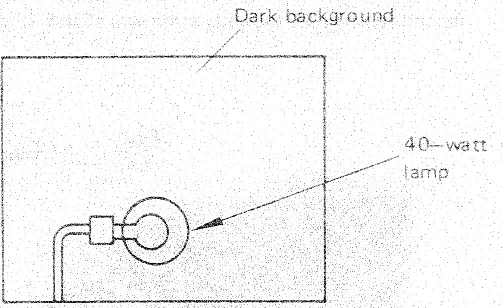
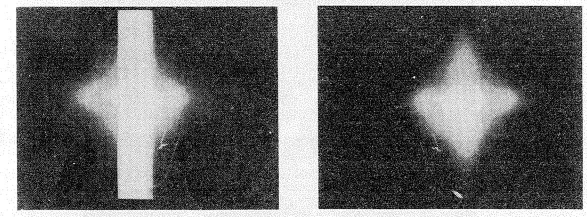


Fig. 4-2-7

- o Set the lens iris to OPEN position (f1.4).
- o While observing the picture on the color video monitor, turn VR2 (CCD BIAS) clockwise or counterclockwise and stop it at a point where the blooming and the noise are minimized. (Fig. 4-2-8)



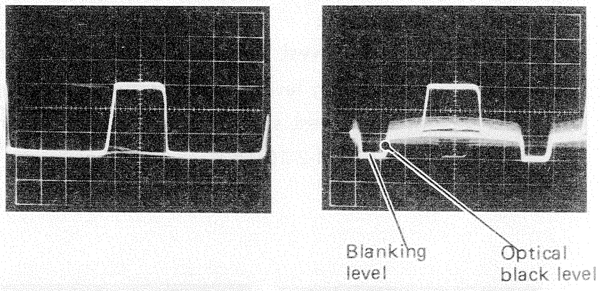
NO GOOD GOOD

Fig. 4-2-8

(12) Output gate adjustment

Test point:	TP8 (TRACKING)	Head board
Adjust:	VR3 (OUTPUT GATE)	Head board
Observe :	Color video monitor	

- o Keep the lens iris to OPEN position.
- o Aim the camera at the white set chart so that the chart fully occupies the color video monitor.
- o Connect the oscilloscope to TP8 (TRACKING) and set the oscilloscope to H rate.
- o Adjust VR3 (OUTPUT GATE) so that the black level following to the white level at TP8 is maximum and the difference between the blanking level and the optical black level is minimum. (Fig. 4-2-9)



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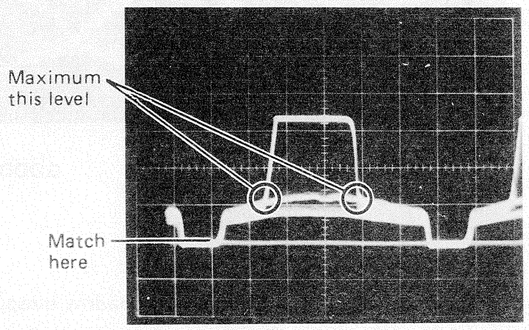


GOOD

Fig. 4-2-9

- o Turn SW1 (POWER SWITCH) to OFF position.
- o Change the manual iris lens to AIC lens.
- o Connect the automatic iris control (AIC) connector to the camera.
- o Turn SW1 (POWER SWITCH) to ON position.
- o Cap on the lens and the SES/Normal Selection Switch SW2 on the Control board to the right (SES) position.
- o Confirm that there is no white-shadow at the higher portion of picture. If it is observed, carefully readjust VR3 (OUTPUT GATE) so that the shadow just disappears from the screen. (Fig. 4-2-10-1)

Note : Adjust the contrast and brightness of the color video monitor for better viewing.

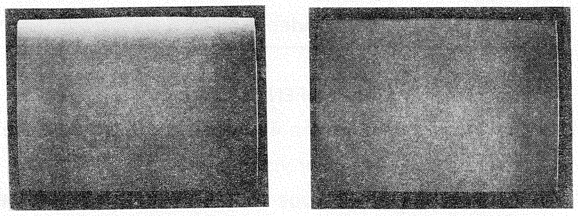


Fig. 4-2-10-1

- o Remove the lens cap.
- o Aim the camera at the gray scale chart.
- o Connect the oscilloscope to TP8 (TRACKING) and set the oscilloscope to H rate.
- o Confirm that the lowest step (black chip) of gray scale is higher than blanking level of waveform. If not, readjust VR3 so that the lowest step of gray scale is higher than blanking level. (Fig. 4-2-10-2)

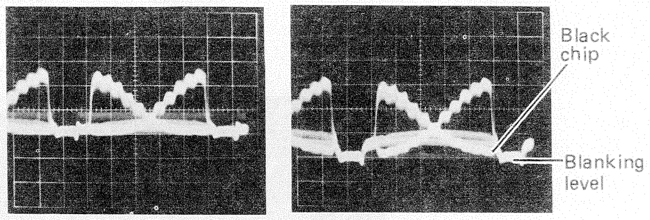


Fig. 4-2-10-2

- o Repeat above steps until the white shadow disappears and the lowest step of gray scale is higher than blanking level.
- o Set the SES/Normal Selection switch SW2 on the control board to left (NOR) position.

(13) Y Pedestal adjustment

Test point:	TP9 (Y_H)	Head board
Adjust:	VR7 (Y.PED)	Head board

- o Cap on the lens.
- o Connect the oscilloscope to TP9 (Y_H).
- o Set the oscilloscope to H. rate.
- o Adjust VR7 (Y.PED) so that the pedestal level at TP9 (Y_H) becomes 0mV ± 5mV. (Fig. 4-2-11)
- o Remove the lens cap.

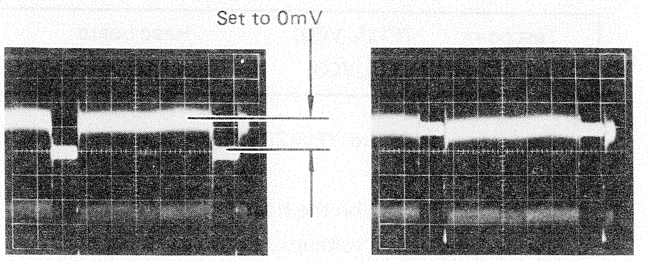


Fig. 4-2-11

(14) Lens iris adjustment

Test point:	TP8 (TRACKING)	Head board
Adjust:	VR2 (LEVEL CONTROL)	Lens board

- o Aim the camera at the logarithmic gray scale chart so that the chart fully occupies the raster of the color video monitor.
- o Connect the oscilloscope to TP8 (TRACKING).
- o Set the oscilloscope to H. rate.
- o Adjust VR2 (LEVEL CONTROL) on the lens (Fig. 4-2-12) so that the waveform is 0.5V ± 0.02V from the blanking level to the top step of the gray scale waveform. (Fig. 4-2-13)

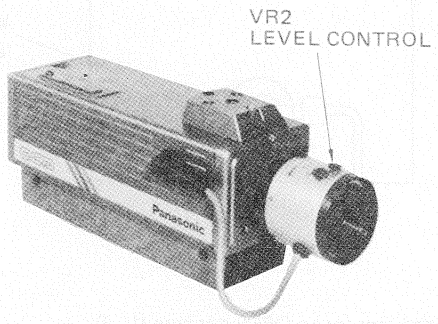


Fig. 4-2-12

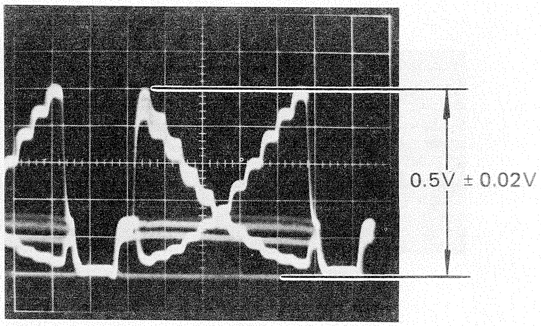


Fig. 4-2-13

(15) AGC adjustment

Test point:	TP9 (Y_H)	Head board
Adjusts:	VR8 (AGC GAIN)	Head board
	VR9 (MANUAL GAIN)	Head board

- Aim the camera at the logarithmic gray scale chart.
- Connect the oscilloscope to TP9 (Y_H).
- Set the oscilloscope to H. rate.
- Set the AGC ON/OFF Switch SW4 on the Control board to the right (ON) position.
- Adjust VR8 (AGC GAIN) on the Head board for 0.4V ± 0.02V from the blanking level to the top step of the gray scale waveform. (Fig. 4-2-14)

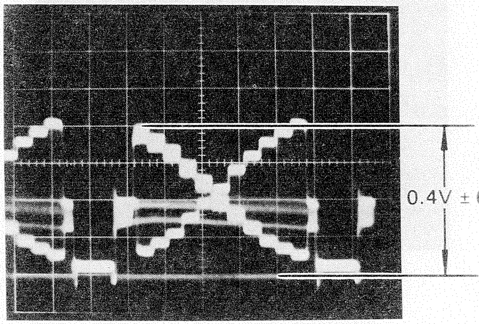


Fig. 4-2-14

- Set the AGC ON/OFF Switch SW2 to the left (OFF) position.
- Adjust VR9 (MANUAL GAIN) on the Head board for 0.4V ± 0.02V from the blanking level to the top step of the gray scale waveform. (Fig. 4-2-14)
- Reset the AGC ON/OFF switch SW4 to the right (ON) position.

(16) Y_H 1H Bias adjustment

Test points:	TP1 (Y_H (1H) BIAS)	VIC1/Process board
	Pin 9 of E1	VIC1/Process board
Adjusts:	VR1 (Y_H (1H) BIAS)	VIC1/Process board
	VR2 (Y_H (1H) GAIN)	VIC1/Process board

- Aim the camera at the logarithmic gray scale chart.
- Connect the oscilloscope to TP1 (Y_H (1H) BIAS) on the VIC1 of the Process board.
- Set the oscilloscope to H. rate.
- Set the input of the oscilloscope to the DC- coupled.
- Turn VR1 (Y_H (1H) BIAS) on the VIC1/Process board fully counterclockwise to minimize the DC level.
- Adjust the vertical position control of the oscilloscope so that the DC waveform coincides with the voltage scale of the oscilloscope. (Fig. 4-2-15)

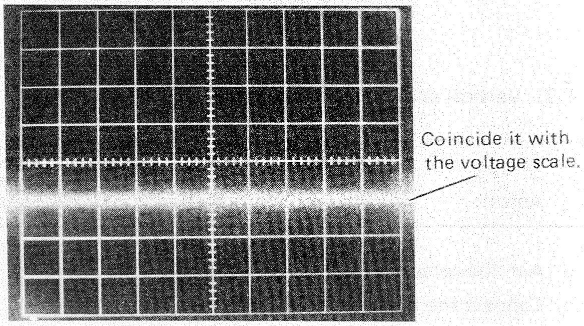


Fig. 4-2-15

- Adjust VR1 (Y_H (1H) BIAS) so that the blanking level increases 700mV ± 20mV. (Fig. 4-2-16)

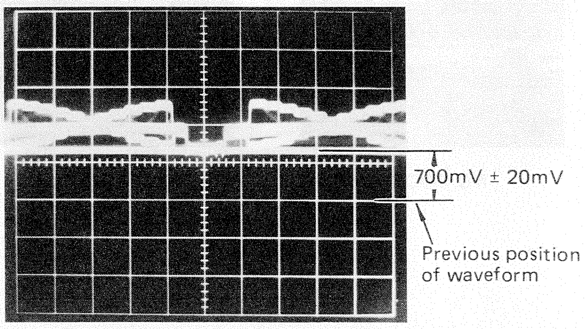


Fig. 4-2-16

- o Change the probe of the oscilloscope from TP1 (Y_H (1H) BIAS) to pin 9 of E1 on the VIC1/Process board.
- o Set the input of the oscilloscope to the AC- coupled.
- o Adjust VR2 (YH (1H) GAIN) for 0.4V ± 0.02V from the blanking level to the top step of the gray scale waveform. (Fig. 4-2-17)

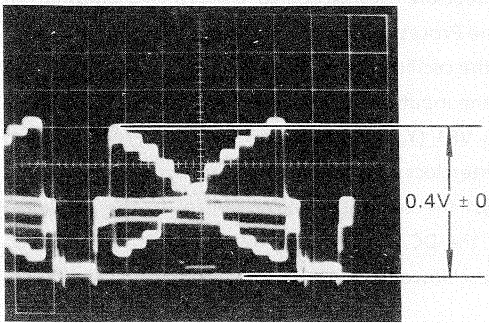
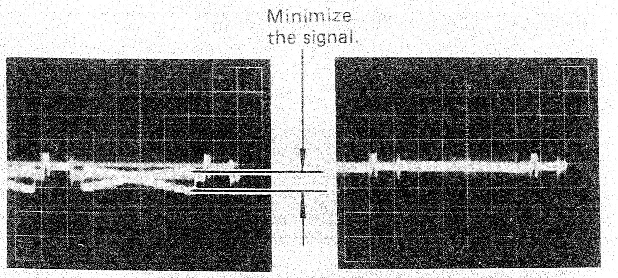


Fig. 4-2-17

(17) Vertical edge balance adjustment

Test point:	TP1 (V.EDGE)	VIC2/Process board
Adjust:	VR1 (V.EDGE BALANCE)	VIC2/Process board

- o Aim the camera at the logarithmic gray scale chart.
- o Connect the oscilloscope to TP1 (V.EDGE).
- o Set the oscilloscope to H. rate.
- o Adjust VR1 (V.EDGE BALANCE) so that the gray scale waveform becomes minimum. (Fig. 4-2-18)



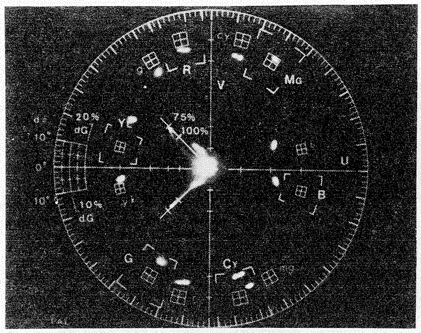
NO GOOD GOOD

Fig. 4-2-18

(18) Chroma phase detection and gain adjustment

Test point:	Video output connector	
	Plus pole of C25	Process board
Adjust:	VR1 (R/B GAIN)	Process board
	VR2 (CHROMA PHASE DETECT)	Processboard

- o Connect the Video Output Connector to the vectorscope.
- o Aim the camera at the color chart so that the chart fully occupies the raster of the color video monitor.
- o Adjust VR2 (CHROMA PHASE DETECT) so that the color vector is maximum on the vectorscope. (Fig. 4-2-19)



Maximize the vectors.

Fig. 4-2-19

- o Connect the oscilloscope to plus pole of C25 and set the oscilloscope to H. rate.
- o Adjust VR1 (R/B GAIN) so that the signal level at plus pole of C25 is 0.6 ± 0.01V. (Fig. 4-2-20)

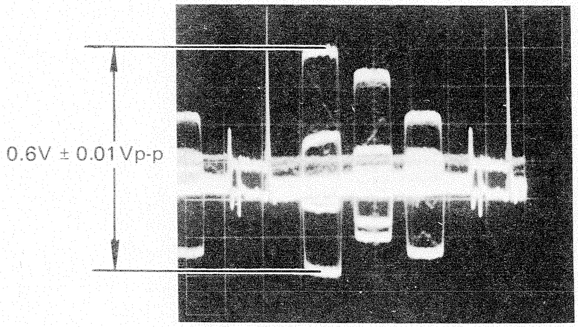
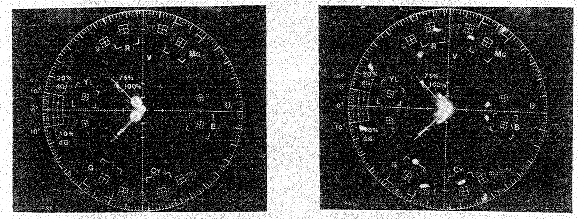


Fig. 4-2-20

(19) Spot correction offset adjustment

Test point:	Video output connector	
Adjust:	VR6 (CORRECTION OFFSET)	Head board

- o This adjustment is only for the cameras which has the IC5 on the Head board.
- o Connect the Video Output Connector to the vectorscope.
- o Cap on the lens.
- o While observing the vectorscope, adjust VR6 (CORRECTION OFFSET) so that the vector positions at the center of the vectorscope. (Fig. 4-2-21)



NO GOOD GOOD

Fig. 4-2-21

(20) CCD bias fine adjustment

Test point:	TP3 (VIDEO)	Process board
Adjust:	VR2 (CCD BIAS)	Head board
Observe:	Color video monitor	

- o Turn SW1 (POWER SWITCH) to OFF position.
- o Disconnect the automatic iris control (AIC) connector from camera.
- o Change AIC lens to manual iris lens.
- o Set SW1 (POWER SWITCH) to ON position.
- o Aim the camera at a 40-watt lamp placed in front of dark background. (Fig. 4-2-22)

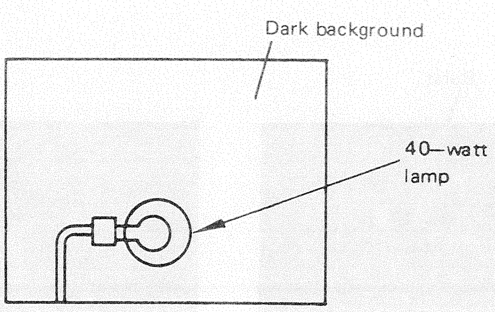
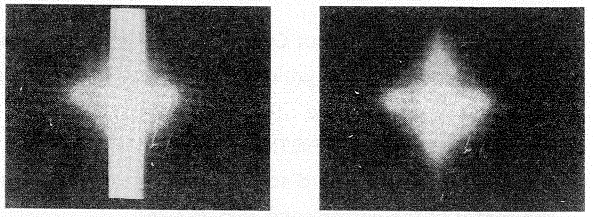


Fig. 4-2-22

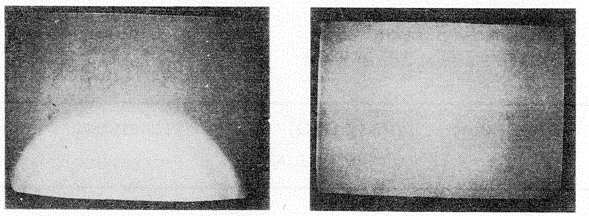
- o Set the lens iris to the OPEN position.
- o Adjust VR2 (CCD BIAS) so that the blooming and the noise are minimized. (Fig. 4-2-23)



NO GOOD GOOD

Fig. 4-2-23

- o Set the SES/Normal Selection Switch SW2 on the Control board to the right (SES) position.
- o Aim the camera at the white chart/paper.
- o Confirm that there is no shadow (caused by the insufficient transfer of charge) at the lower portion of picture. If it is observed, carefully adjust VR2 (CCD BIAS) so that the shadow just disappears from the screen. (Fig. 4-2-24)



NO GOOD GOOD

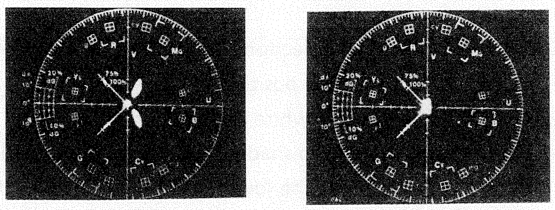
Fig. 4-2-24

- o Set the SES/Normal Selection Switch SW2 to the left (NOR) position.
- o Aim the camera at the gray scale chart.
- o Place the 1/8 ND filter in front of lens and confirm that the gray scale waveform at TP3 (VIDEO) on the Process board is not suppressed.
- o Turn SW1 (POWER SWITCH) to OFF position.
- o Change the manual iris lens to AIC lens.
- o Connect the automatic iris control (AIC) connector to the camera.
- o Turn SW1 (POWER SWITCH) to ON position.

(21) White balance preset course adjustment

Test point:	Video output connector	
Adjusts:	VR4 (R ATW PRESET)	Microprocessor board
	VR5 (B ATW PRESET)	Microprocessor board

- o Connect the Video Output Connector to the vectorscope.
- o Aim the camera at the white chart so that the chart fully occupies the raster of the color video monitor.
- o Adjust VR4 (R ATW PRESET) and VR5 (B ATW PRESET) on the Microprocessor board so that the vector positions at the center of the vectorscope. (Fig. 4-2-25)



NO GOOD **GOOD**

Fig. 4-2-25

(22) Video level adjustment

Test point:	TP3 (VIDEO)	Process board
Adjust:	VR5 (YH GAIN)	Process board

- o Aim the camera at the gray scale chart.
- o Connect the oscilloscope to TP3 (VIDEO).
- o Set the oscilloscope to H. rate.
- o Adjust VR5 (YH GAIN) for 0.7 ± 0.05V (100IRE ± 7IRE) from the blanking level to the top step of the gray scale waveform. (Fig. 4-2-26)

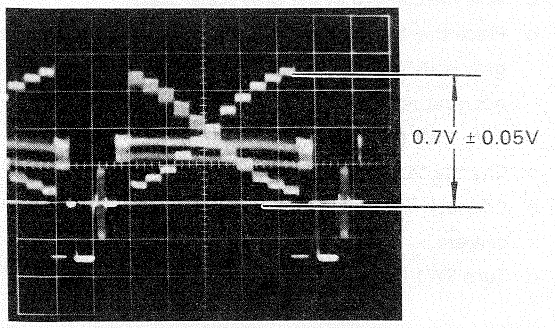


Fig. 4-2-26

(23) Y pedestal adjustment

Test point :	TP9 (Y_H)	Head board
Adjust :	VR7 (Y.PED)	Head board

- o Cap on the lens.
- o Connect the oscilloscope to TP9 (Y_H).
- o Make sure that pedestal level at TP9 (Y_H) is 0 ± 5mV.
- o If not, readjust VR7 (Y.PED) so that the pedestal level at TP9 (Y_H) becomes 0 ± 5mV. (Fig. 4-2-27)

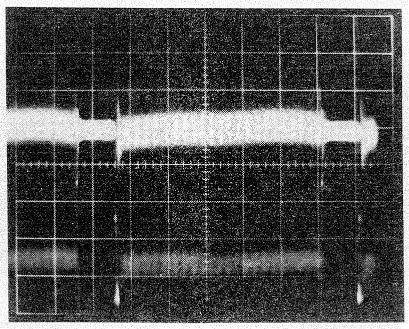


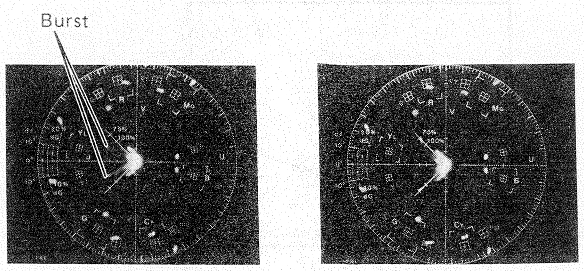
Fig. 4-2-27

- o Remove the lens cap.

(24) Burst phase adjustment

Test point:	Video output connector	
Adjust:	VR2 (BURST PHASE)	VIC3/Process board

- o Connect the Video Output connector to the vectorscope.
- o Aim the camera at the logarithmic gray scale chart.
- o Adjust VR2 (BURST PHASE) so that the burst vectors position (90° ± 2°) on the burst axis of the vectorscope. (Fig. 4-2-28)



NO GOOD **GOOD**

Fig. 4-2-28

(25) Chroma gain, B-Y_L gain, chroma phase, R matrix, chroma clip and burst gain adjustment

Test point:	Video output connector	
Adjusts:	VR4 (CHROMA GAIN)	VIC3/Process board
	VR3 (BURST GAIN)	VIC3/Process board
	CT1 (CHROMA PHASE)	VIC3/Process board
	VR3 (B-Y_L GAIN)	Process board
	VR1 (R MATRIX)	VIC3/Process board
	VR4 (CHROMA CLIP)	Process board

- o Connect the Video Output Connector to the vectorscope.
- o Aim the camera at the color chart so that the chart fully occupies the raster of the color video monitor.
- o Adjust the Phase Control of the vectorscope so that the burst vector positions on the burst axis on the vectorscope.
- o Adjust VR3 (BURST GAIN) on the VIC3/Process board so that the burst level is 75% on the vectorscope. (Fig. 4-2-29)

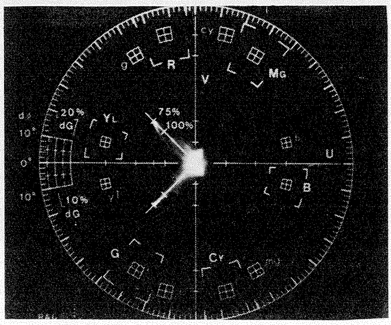

cy
R
V
MG
YL
20%
dG
75%
100%
U
B
10%
dG
G
CY
mg
PAL

Fig. 4-2-29

- o Adjust VR4 (CHROMA GAIN) on the VIC3/Process board so that the red vector is 180% ± 25% as compares to the burst vector on the vectorscope. (Fig. 4-2-30)

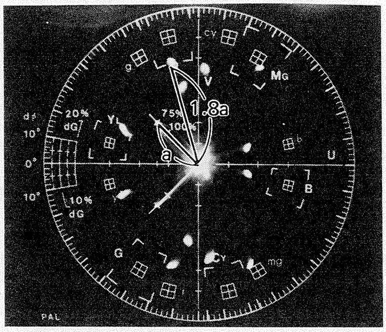

cy
V
MG
1.8a
75%
100%
a
20%
dG
U
B
10%
dG
G
CY
mg
PAL

Fig. 4-2-30

- o Adjust VR3 (B-Y_L GAIN) so that the yellow vector is 135% ± 25% as compares to the burst vector. (Fig. 4-2-31)

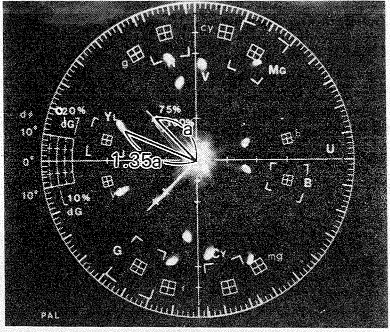

cy
V
MG
20%
dG
75%
a
1.35a
U
B
10%
dG
G
CY
mg
PAL

Fig. 4-2-31

- o Set the vectorscope to the NTSC mode and adjust the Phase Control of vectorscope so that burst vectors coincide each other.
- o Adjust CT1 (CHROMA PHASE) on the VIC3/process board so that the red vectors for odd and even field coincide each other.
- o Adjust VR1 (R MATRIX) on the VIC3/process board so that the phase of red vector is 102° ± 7°. (Fig. 4-2-32)

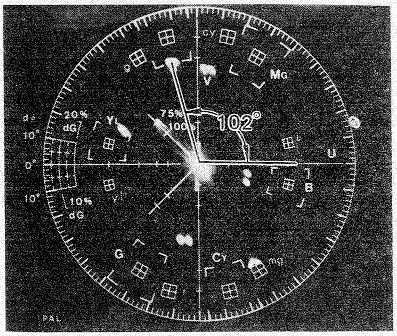

cy
V
MG
20%
dG
75%
100%
102°
U
B
10%
dG
G
CY
mg
PAL

Fig. 4-2-32

- o Repeat adjusting VR3 (B-YL GAIN), VR4 (CHROMA GAIN) and VR1 (R MATRIX) so that the yellow vector and red vector are 135% ± 25% and 180% ± 25% respectively as compare to burst vector and the phase of red vector is 102° ± 7°. (Fig. 4-2-33)

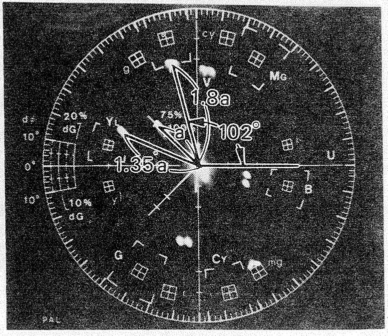

cy
V
MG
1.8a
75%
a
102°
1.35a
20%
dG
U
B
10%
dG
G
CY
mg
PAL

Fig. 4-2-33

- o Set the vector scope to the PAL mode.
- o Turn VR1 (WHITE BALANCE CONTROL) on the rear panel fully clockwise to maximum the red gain.
- o Adjust VR4 (CHROMA CLIP) on the process board so that the red vector is clipped at 233 ± 5% as compare to the burst vector. (Fig. 4-2-34)
- o After adjusting VR4, readjust VR1 while referring to (2) White Balance Control adjustment on page.

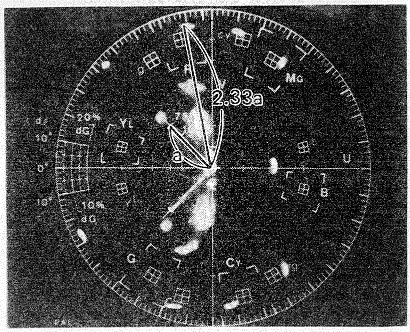


Fig. 4-2-34

(26) White balance preset fine-adjustment

Test point:	Video output connector	
Adjusts:	VR4 (R ATW PRESET)	Microprocessor board
	VR5 (B ATW PRESET)	Microprocessor board

- o Connect the Video Output Connector to the vectorscope.
- o Set the White Balance Selection Switch to the MANU position and turn SW1 (POWER SWITCH) to OFF position once to reset the white balance.
- o Aim the camera at the white chart so that the chart fully occupies the raster of the color video monitor.
- o Confirm that the vector positions at the center of the vectorscope. If not, adjust VR4 (R ATW PRESET) and VR5 (B ATW PRESET) on the Microprocessor board so that the vector positions at the center of the vectorscope. (Fig. 4-2-35)

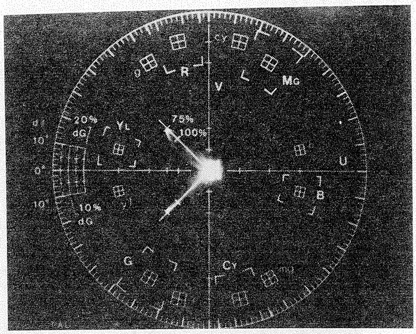


Fig. 4-2-35

(27) ATW gain adjustment

Test point :	Video output connector	
Adjusts :	VR2 (R ATW GAIN)	Microprocessor board
	VR3 (B ATW GAIN)	Microprocessor board

- o Set the White Balance Selection switch to ATW position.
- o Aim the camera at the white chart so that the carefully occupies the raster of the color video monitor.
- o Set the color temperature conversion filter which converts 3200°K to 5200°K in front of the lens.

  Note : If there is no color temperature conversion filter, the ATW gain can roughly be adjusted by using the outdoor light.
- o Adjust VR2 (R ATW GAIN) and VR3 (B ATW GAIN) so that the vector positions at center of vectorscope. (Fig. 4-2-36)

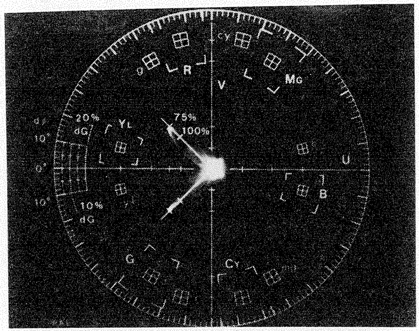


Fig. 4-2-36

- o Remove the color temperature conversion filter from the lens.
- o Confirm that the vector on the vectorscope positions at the center. If not, repeat adjustment of (26) and (27).

(28) Flangeback adjustment

Adjust:	Flangeback adjusting ring
Observe:	Color video monitor

- o Aim the camera at an object which is illuminated approx. 10 footcandles (100 lux) and further than 10 meters.
- o Set the lens focus ring to ∞ position.
- o Loosen the two hex screws on the flange adjusting ring by using supplied hex-wrench. (Fig. 4-2-37)
- o Turn the flange adjusting ring by using supplied hex-wrench to the best focus position.
- o Tighten the two hex screws on the flange adjusting ring by using supplied hex-wrench.

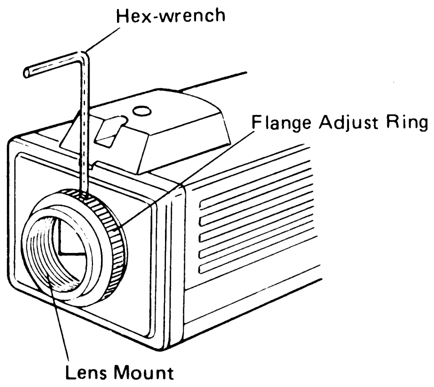


Fig. 4-2-37

(29) Character size adjustment

Test point :	Video output connector	
Adjust :	VR1 (CHARACTER SIZE)	Microprocessor board

- o Cap on the lens.
- o Set SW4 (CAMERA NUMBER ON/OFF SWITCH) on the Microprocessor board to ON position and SW1, 2, 3 (CAMERA NUMBER SETTING SWITCH) on Microprocessor board to O position.
- o Connect the oscilloscope to Video Output connector and trigger the oscilloscope at H.rate.
- o Expand the horizontal period on the oscilloscope.
- o Adjust VR1 (CHARACTER SIZE) on the Microprocessor board so that the blanking lags by 8 μsec ± 0.5 μsec the character signal. (Fig. 4-2-38)

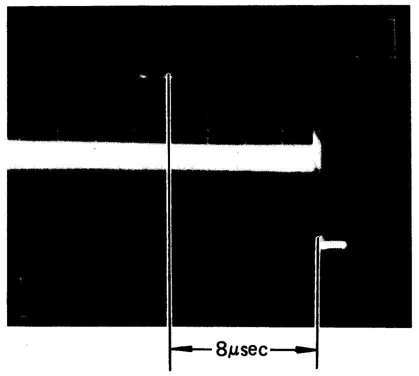


Fig. 4-2-38

(30) Horizontal phase adjustment

Test points :	TP3 (VIDEO)	Process board
	TP3 (GEN-LOCK)	Gen-lock/sync board
Adjust :	VR4 (H.PHASE)	Gen-lock/sync board

- o Supply the black burst signal from the special effect generator (SEG) or signal generator to the GEN-LOCK INPUT connector on the rear of the camera.
- o Connect the oscilloscope to TP3 (VIDEO) on the process board and TP3 (GEN-LOCK) on the Gen-lock/sync board and trigger the oscilloscope at H.rate.
- o Expand the H sync period on the oscilloscope.
- o Confirm that the sync of gen-lock signal at TP3 (GEN-LOCK) leads by more than 0.5 μsec and lags more than 0.5 μsec sync signal at TP3 (VIDEO) as VR4 (H.PHASE) on the Gen-lock/sync board is turned through its range.
- o After confirmed the above step, adjust VR4 so that the leading edge of both signal are coincident.

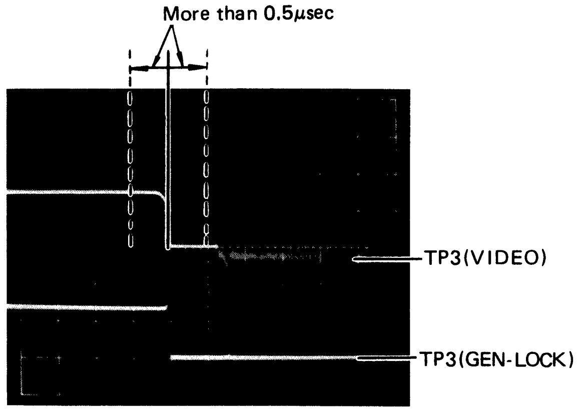


Fig. 4-2-39

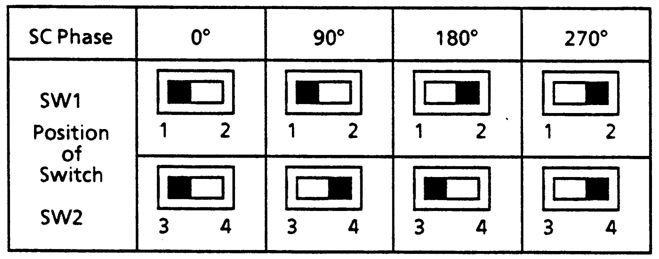
(31) Confirmation of subcarrier phase

Test point:	Video output connector	
Adjusts:	SW1	Gen-lock/sync board
	(SC PHASE COARSE SW1)	
	SW2	Gen-lock/sync board
	(SC PHASE COARSE SW2)	
	VR3	Gen-lock/sync board
	(SC PHASE FINE)	
Observe:	Vectorscope	

- o Keep the black burst signal from the special effect generator (SEG) or signal generator to the GEN-LOCK connector on the rear of the camera.
- o Aim the camera at the color chart.
- o Keep the subcarrier (SC) signal from the special effect generator (SEG) or signal generator to the external subcarrier reference (EXT CW ø REF) connector on the rear of the vectorscope and set the burst mode (ø REF) of vectorscope to EXT position.
- o Set SW1 (SC PHASE COARSE SW1) and SW2 (SC PHASE COARSE SW2) 0° position and turn VR3 (SC PHASE FINE) fully counterclockwise.
- o Confirm that the burst phase changes more than 90°, 180° and 270° position. (Fig. 4-2-40)

- o Confirm that the burst phase changes more than 90°, when VR3 is turned back. (The burst phase changes more than 360° by setting SW1, SW2 and VR3.)
- o Reset SW1 and SW2 to the 0° position and return VR3 fully counterclockwise.
- o Disconnect the black burst signal from the GEN-LOCK connector, and the external subcarrier signal from the vectorscope.

SC Phase Coarse Switch Selection

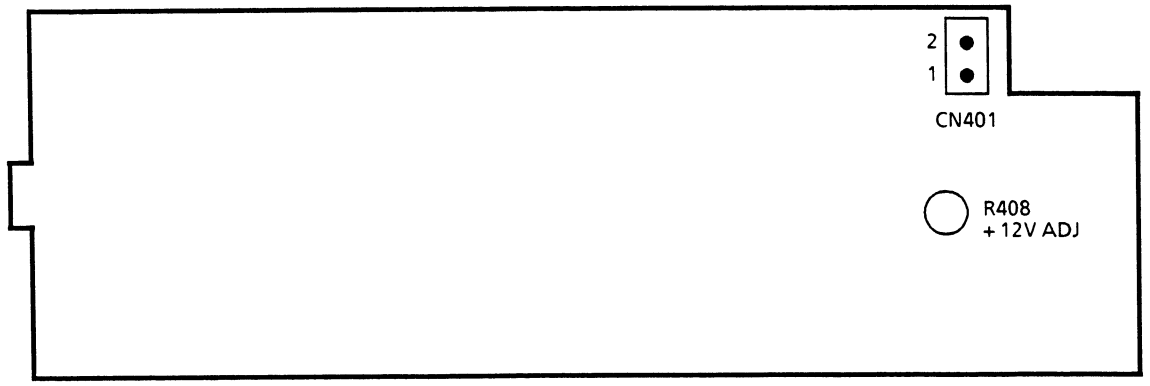


SC Phase	0°	90°	180°	270°
SW1	■□ 1 2	■□ 1 2	□■ 1 2	□■ 1 2
Position of Switch				
SW2	■□ 3 4	□■ 3 4	■□ 3 4	□■ 3 4

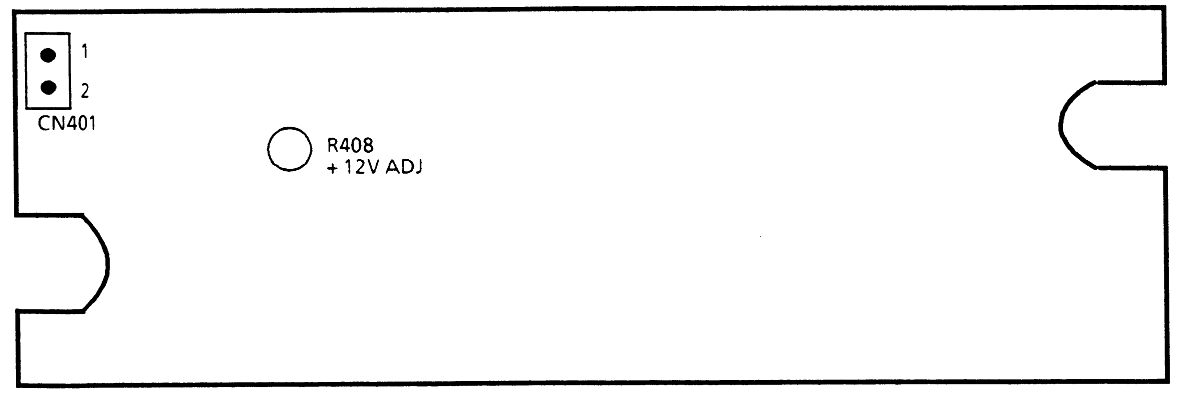
Fig. 4-2-40

LOCATION OF TEST POINTS AND ADJUSTING CONTROLS

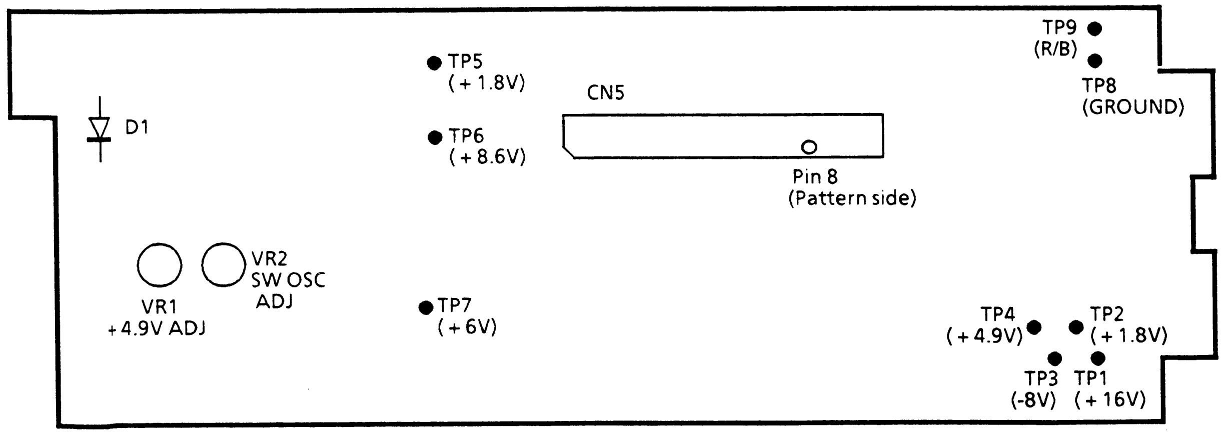
POWER BOARD (WV-CD130)


2
1
CN401
R408
+ 12V ADJ

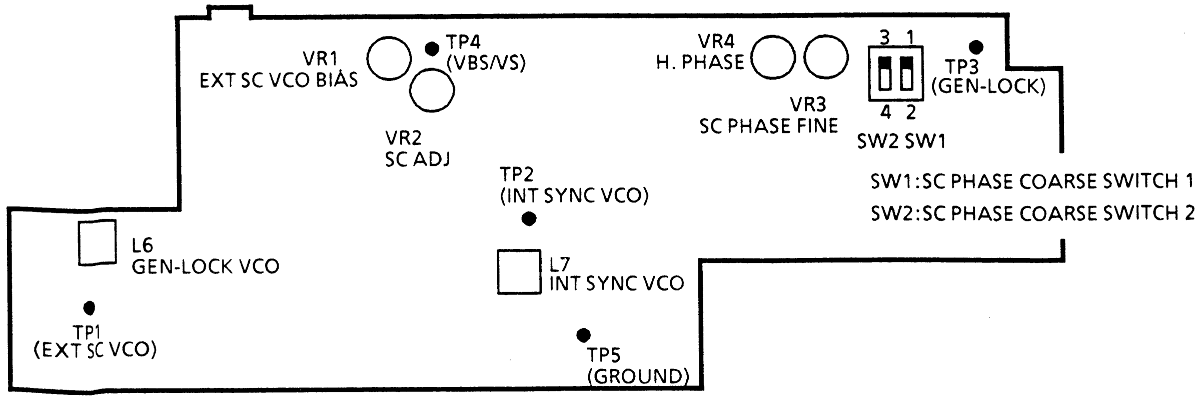
POWER BOARD (WV-CD134)


1
2
CN401
R408
+ 12V ADJ

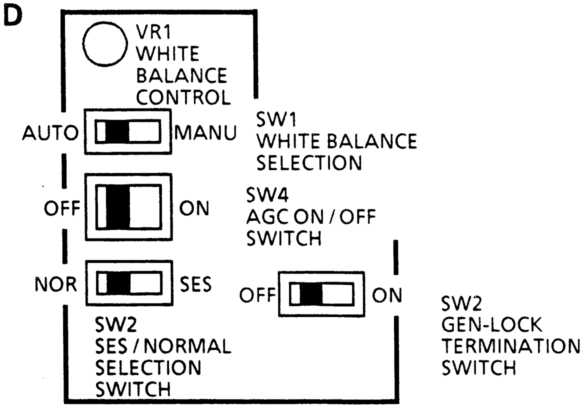
MOTHER BOARD


TP9 (R/B)
TP5 (+ 1.8V)
CN5
TP8 (GROUND)
D1
TP6 (+ 8.6V)
Pin 8 (Pattern side)
VR2 SW OSC ADJ
VR1 +4.9V ADJ
TP7 (+ 6V)
TP4 (+ 4.9V)
TP2 (+ 1.8V)
TP3 (-8V)
TP1 (+ 16V)

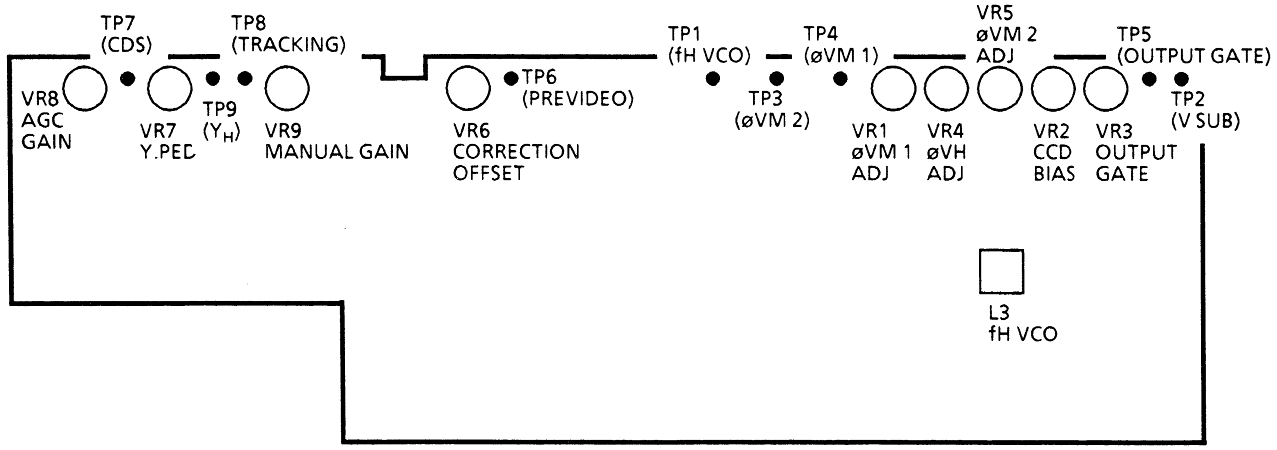
GEN-LOCK / SYNC BOARD


VR1 EXT SC VCO BIAS
TP4 (VBS/VS)
VR2 SC ADJ
VR4 H. PHASE
VR3 SC PHASE FINE
3 1
4 2
SW2 SW1
TP3 (GEN-LOCK)
TP2 (INT SYNC VCO)
SW1:SC PHASE COARSE SWITCH 1
SW2:SC PHASE COARSE SWITCH 2
L6 GEN-LOCK VCO
L7 INT SYNC VCO
TP1 (EXT SC VCO)
TP5 (GROUND)

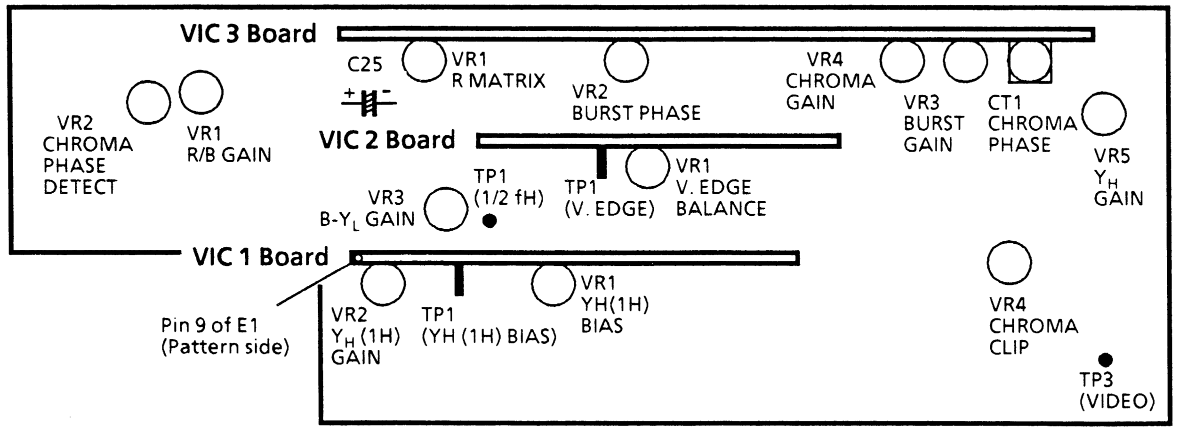
CONTROL BOARD


VR1 WHITE BALANCE CONTROL
AUTO
MANU
SW1 WHITE BALANCE SELECTION
OFF
ON
SW4 AGC ON / OFF SWITCH
NOR
SES
SW2 SES / NORMAL SELECTION SWITCH
OFF
ON
SW2 GEN-LOCK TERMINATION SWITCH

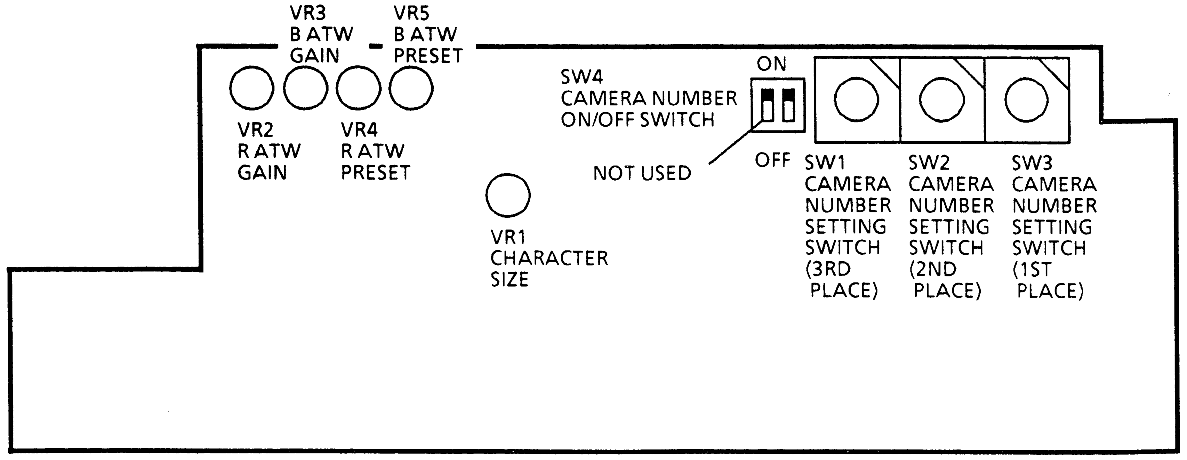
HEAD BOARD


TP7 (CDS)
TP8 (TRACKING)
TP1 (fH VCO)
TP4 (øVM 1)
VR5 øVM 2 ADJ
TP5 (OUTPUT GATE)
VR8 AGC GAIN
VR7 Y.PED
TP9 (Y_H)
VR9 MANUAL GAIN
TP6 (PREVIDEO)
VR6 CORRECTION OFFSET
TP3 (øVM 2)
VR1 øVM 1 ADJ
VR4 øVH ADJ
VR2 CCD BIAS
VR3 OUTPUT GATE
TP2 (V SUB)
L3 fH VCO

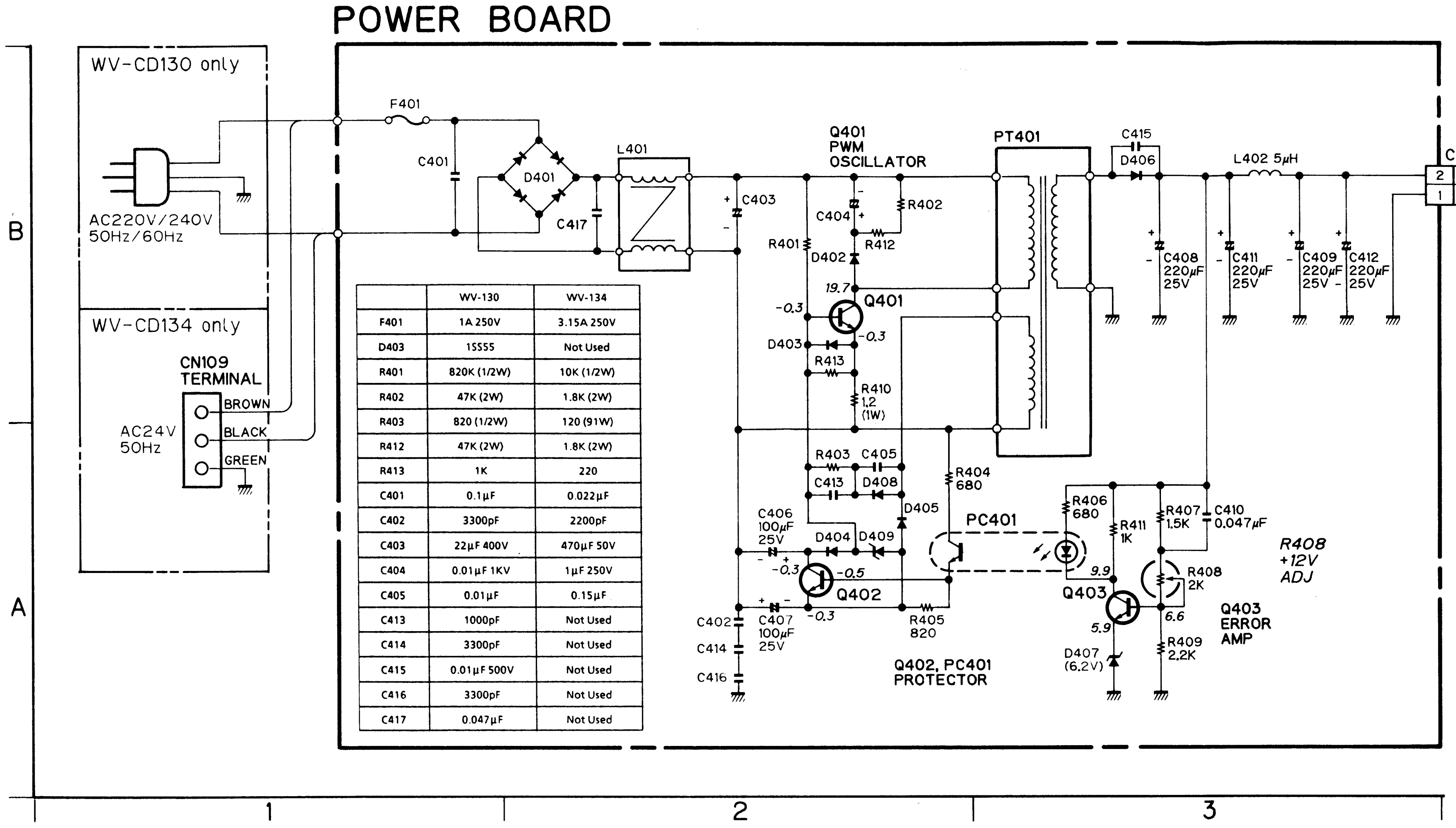
PROCESS BOARD


VIC 3 Board
C25
VR1 R MATRIX
VR2 BURST PHASE
VR4 CHROMA GAIN
VR3 BURST GAIN
CT1 CHROMA PHASE
VR5 Y_H GAIN
VR2 CHROMA PHASE DETECT
VR1 R/B GAIN
VIC 2 Board
TP1 (1/2 fH)
TP1 (V. EDGE)
VR1 V. EDGE BALANCE
VR3 B-Y_L GAIN
VIC 1 Board
Pin 9 of E1 (Pattern side)
VR2 Y_H (1H) GAIN
TP1 (YH (1H) BIAS)
VR1 YH(1H) BIAS
VR4 CHROMA CLIP
TP3 (VIDEO)

MICROPROCESSOR BOARD


VR3 B ATW GAIN
VR5 B ATW PRESET
VR2 R ATW GAIN
VR4 R ATW PRESET
SW4 CAMERA NUMBER ON/OFF SWITCH
ON
OFF
NOT USED
VR1 CHARACTER SIZE
SW1 CAMERA NUMBER SETTING SWITCH (3RD PLACE)
SW2 CAMERA NUMBER SETTING SWITCH (2ND PLACE)
SW3 CAMERA NUMBER SETTING SWITCH (1ST PLACE)

SCHEMATIC DIAGRAM OF POWER BOARD


POWER BOARD
WV-CD130 only
AC220V/240V
50Hz/60Hz
WV-CD134 only
CN109
TERMINAL
AC24V
50Hz
BROWN
BLACK
GREEN
F401
C401
D401
C417
L401
C403
Q401
PWM
OSCILLATOR
C404
R402
R401
R412
D402
19.7
-0.3
Q401
D403
R413
R410
1.2
(1W)
PT401
C415
D406
L402 5μH
C408
220μF
25V
C411
220μF
25V
C409
220μF
25V
C412
220μF
25V
CN401
+12V OUT
GROUND
TO
MOTHER
BOARD
... CN9
R403
C405
C413
D408
R404
680
D405
C406
100μF
25V
D404
D409
-0.5
Q402
C402
C414
C416
C407
100μF
25V
R405
820
PC401
Q402, PC401
PROTECTOR
R406
680
R411
1K
R407
1.5K
C410
0.047μF
R408
+12V
ADJ
R408
2K
9.9
Q403
5.9
6.6
D407
(6.2V)
R409
2.2K
Q403
ERROR
AMP
A
B
1
2
3

	WV-130	WV-134
F401	1A 250V	3.15A 250V
D403	1SS55	Not Used
R401	820K (1/2W)	10K (1/2W)
R402	47K (2W)	1.8K (2W)
R403	820 (1/2W)	120 (91W)
R412	47K (2W)	1.8K (2W)
R413	1K	220
C401	0.1μF	0.022μF
C402	3300pF	2200pF
C403	22μF 400V	470μF 50V
C404	0.01μF 1KV	1μF 250V
C405	0.01μF	0.15μF
C413	1000pF	Not Used
C414	3300pF	Not Used
C415	0.01μF 500V	Not Used
C416	3300pF	Not Used
C417	0.047μF	Not Used

Power Board for WV-CD130

Q401	2SC3549	B2
Q402	2SC1815	A2
Q403	2SC1815	A3
D401	S1VB60	B2
D402	RU1P	B2
D403	1SS55	B2
D404	1SS55	A2
D405	EM1Z	A2
D406	ERB84-009	B3
D407	RD6.2EB1	A3
D408	ERB44-02	A2
D409	RD7.5EB3	A2

Power Board for WV-CD134

Q401	2SC2334-K	B2
Q402	2SD471	A2
Q403	2SC1815	A3
D401	D2SB40	B2
D402	ES1A	B2
D404	ERB44-02	A2
D405	1SS55	A2
D406	ERB84-009	B3
D407	RD6.2EB1	A3
D408	ERB44-02	A2
D409	RD5.6EB1	A2

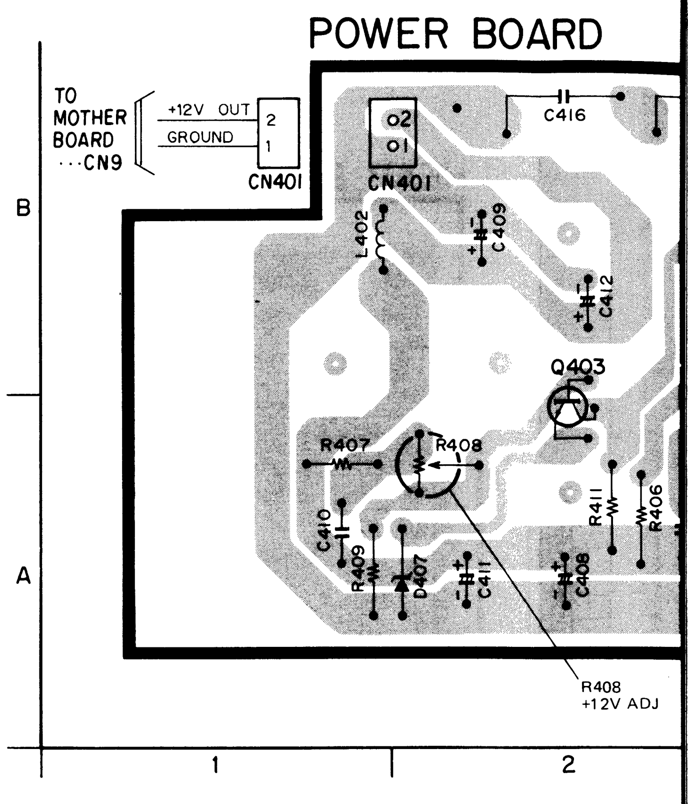
Power Board for WV-CD130

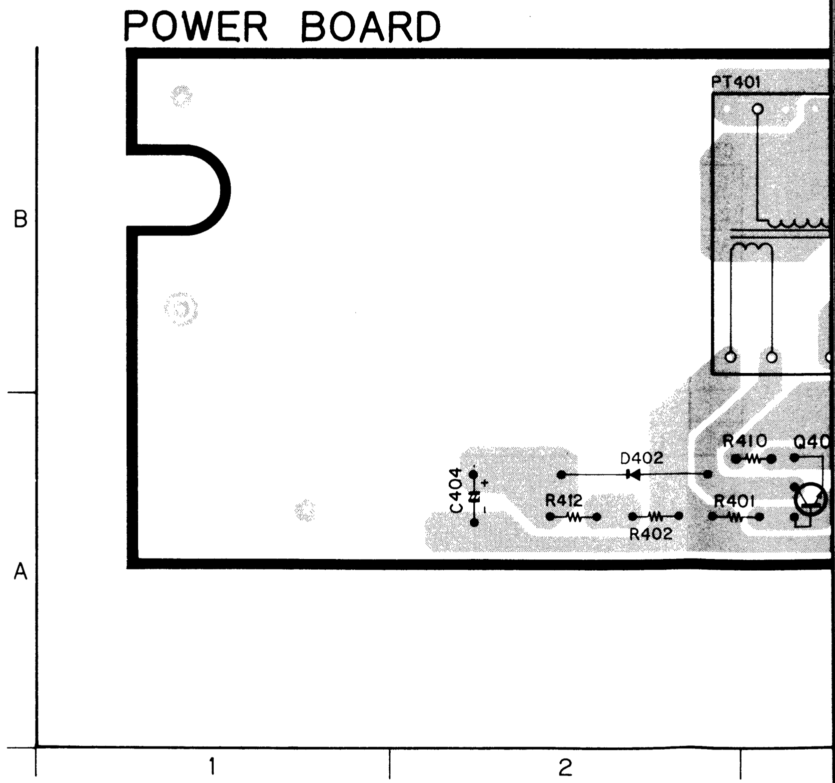
Q401	2SC3549	B4
Q402	2SC1815	B3
Q403	2SC1815	A2
D401	S1VB60	A5
D402	RU1P	A4
D403	1SS55	A4
D404	1SS55	B3
D405	EM1Z	B3
D406	ERB84-009	A2
D407	RD6.2EB1	A1
D408	ERB44-02	B3
D409	RD7.5EB3	B3

Power Board for WV-CD134

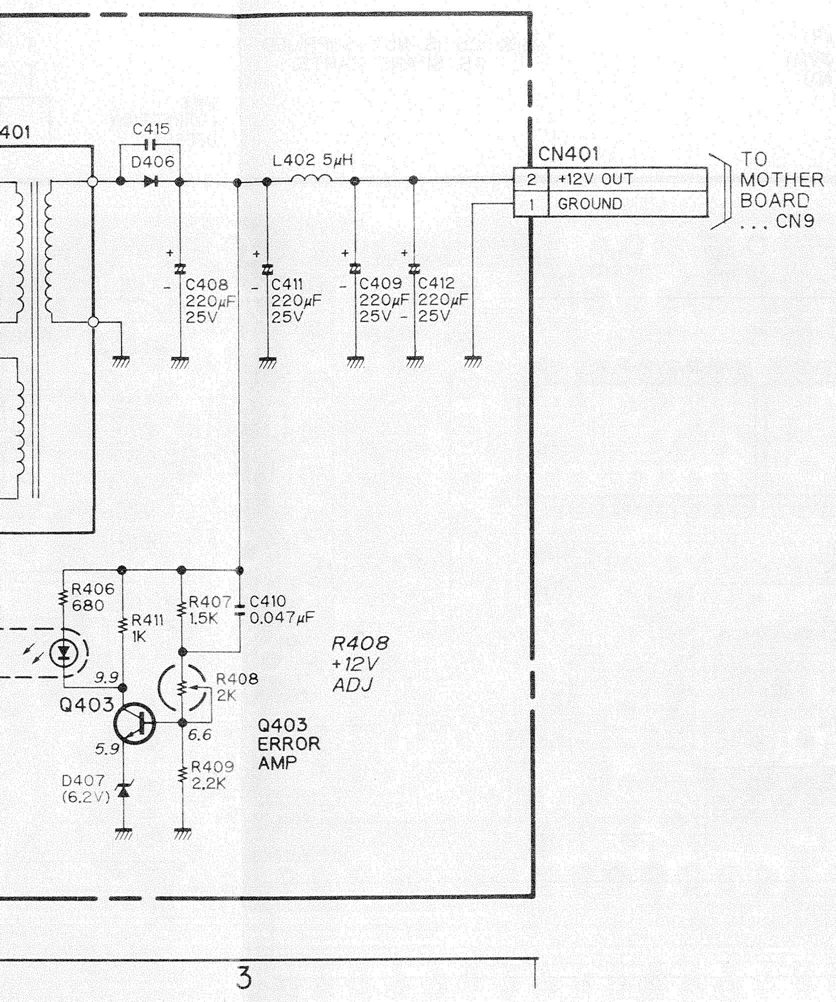
Q401	2SC2334-K	A3
Q402	2SD471	B3
Q403	2SC1815	B4
D401	D2SB40	B4, B5
D402	ES1A	B3
D404	ERB44-02	B3
D405	1SS55	B4
D406	ERB84-009	B3
D407	RD6.2EB1	B4
D408	ERB44-02	B3
D409	RD5.6EB1	A3

CONDUCTO

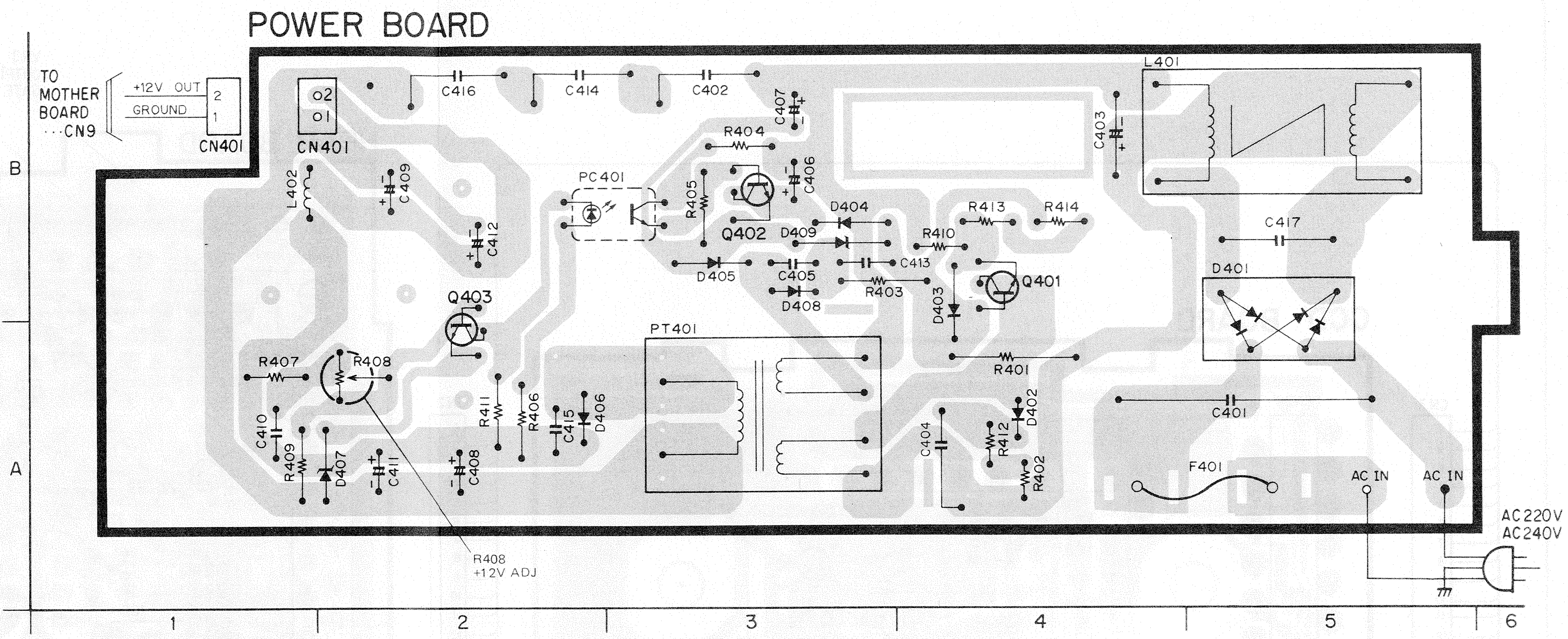

POWER BOARD
TO
MOTHER
BOARD
...CN9
+12V OUT
GROUND
CN401
C416
L402
C409
C412
Q403
R407
R408
R411
R406
C410
R409
D407
C411
C408
R408
+12V ADJ
A
B
1
2

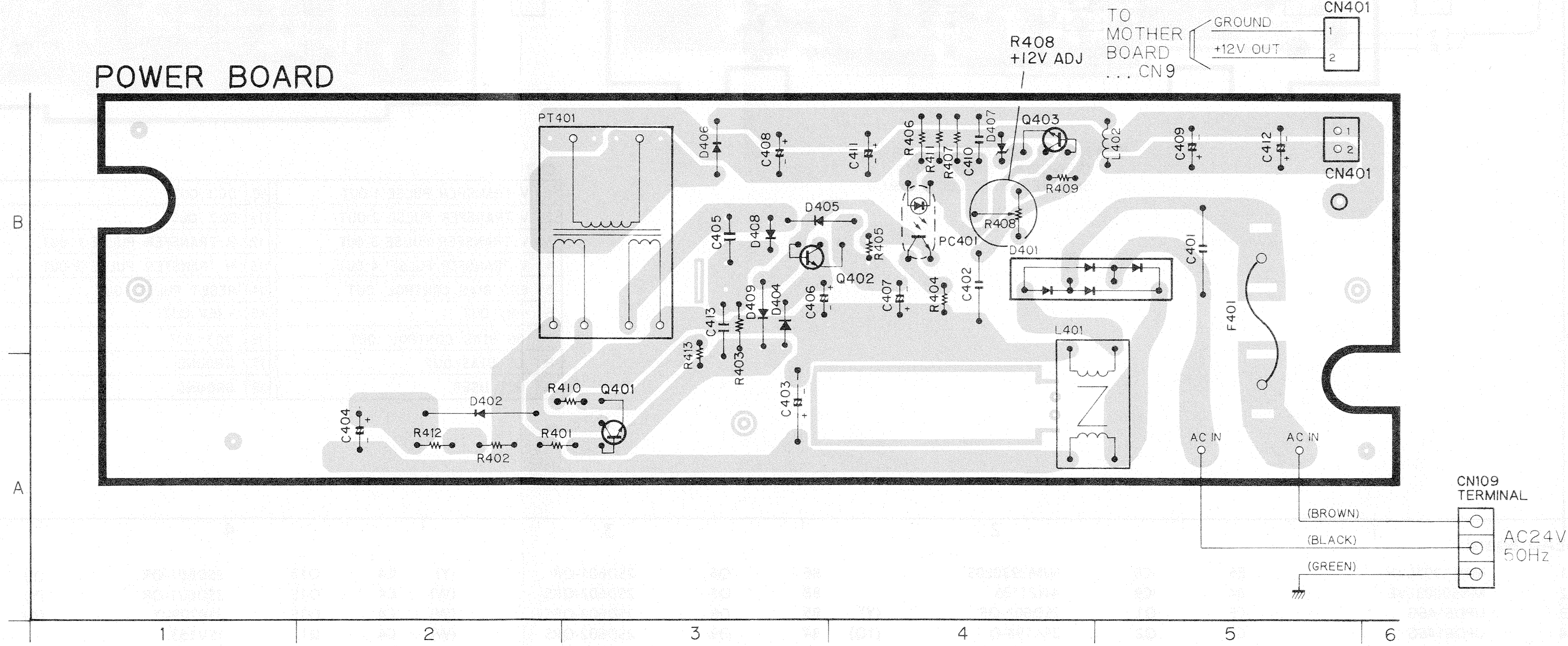

POWER BOARD
PT401
D402
R410
C404
R412
R402
R401
A
B
1
2

R BOARD


401
C415
D406
L402 5μH
CN401
2 +12V OUT
1 GROUND
TO
MOTHER
BOARD
... CN9
C408 220μF 25V
C411 220μF 25V
C409 220μF 25V
C412 220μF 25V
R406 680
R411 1K
R407 1.5K
C410 0.047μF
R408 +12V ADJ
9.9
R408 2K
Q403
6.6
5.9
Q403 ERROR AMP
D407 (6.2V)
R409 2.2K
3

CONDUCTOR VIEW OF POWER BOARD


POWER BOARD
TO MOTHER BOARD ···CN9
+12V OUT
GROUND
CN401
C416
C414
C402
C407
R404
C403
L401
C409
PC401
R405
C406
L402
C412
Q402
D404
D409
R413
R414
C417
R410
D405
C405
C413
D408
R403
Q401
D401
Q403
PT401
D403
R407
R408
R401
R411
R406
C415
D406
D402
C401
C410
R409
D407
C411
C408
C404
R412
R402
F401
AC IN
AC IN
AC220V
AC240V
R408
+12V ADJ
B
A
1
2
3
4
5
6


POWER BOARD
R408
+12V ADJ
TO
MOTHER
BOARD
... CN9
GROUND
+12V OUT
CN401
PT401
D406
C408
C411
R406
R411
R407
C410
D407
Q403
L402
C409
C412
R409
D405
R408
C405
D408
R405
PC401
D401
C401
Q402
C407
R404
C402
C413
D409
D404
C406
F401
R413
R403
L401
R410
Q401
D402
C403
C404
R412
R401
R402
AC IN
AC IN
CN109
TERMINAL
(BROWN)
(BLACK)
(GREEN)
AC24V
50Hz
B
A
1
2
3
4
5
6

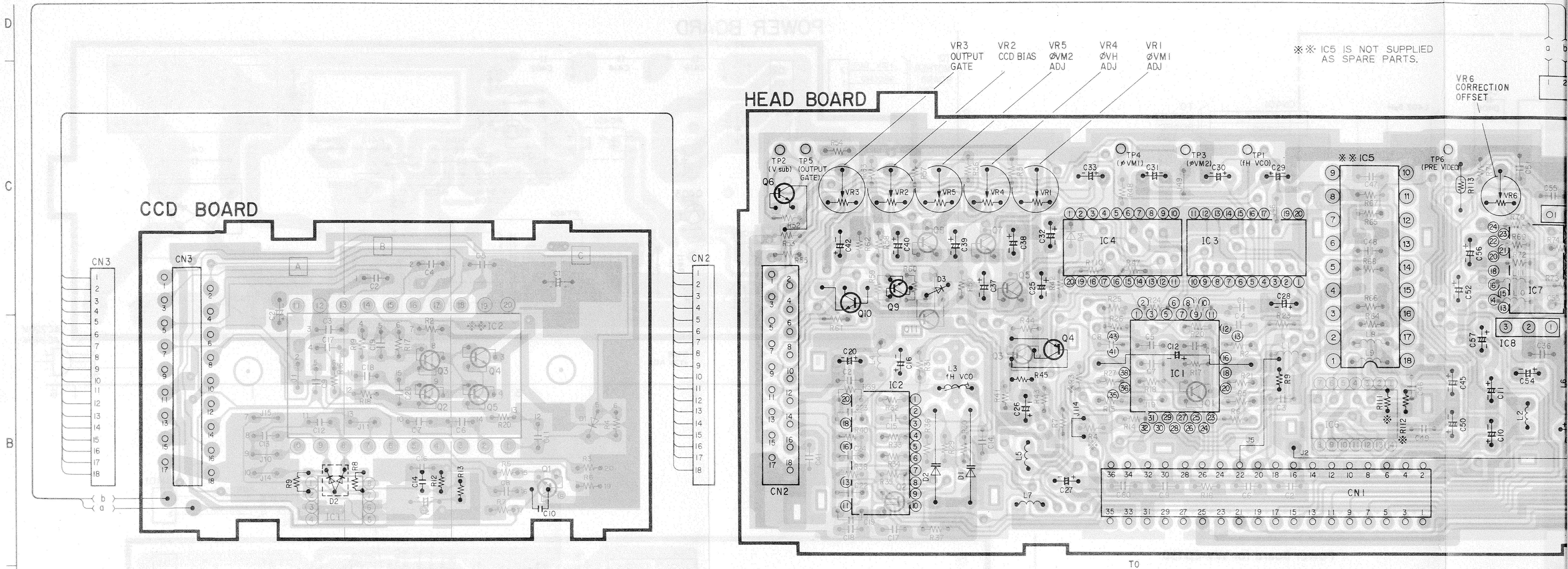
Power Board for WV-CD130

Q401	2SC3549	B4
Q402	2SC1815	B3
Q403	2SC1815	A2
D401	S1VB60	A5
D402	RU1P	A4
D403	1SS55	A4
D404	1SS55	B3
D405	EM1Z	B3
D406	ERB84-009	A2
D407	RD6.2EB1	A1
D408	ERB44-02	B3
D409	RD7.5EB3	B3

Power Board for WV-CD134

Q401	2SC2334-K	A3
Q402	2SD471	B3
Q403	2SC1815	B4
D401	D2SB40	B4, B5
D402	ES1A	B3
D404	ERB44-02	B3
D405	1SS55	B4
D406	ERB84-009	B3
D407	RD6.2EB1	B4
D408	ERB44-02	B3
D409	RD5.6EB1	A3

CONDUCTOR VIEW OF HEAD BOARD


D
C
B
A
CCD BOARD
HEAD BOARD
CN3
CN2
IC1
IC2
Q1
Q2
Q3
Q4
Q5
D2
VR3 OUTPUT GATE
VR2 CCD BIAS
VR5 ØVM2 ADJ
VR4 ØVH ADJ
VR1 ØVM1 ADJ
VR6 CORRECTION OFFSET
TP2 (V sub)
TP5 (OUTPUT GATE)
TP4 (ØVM1)
TP3 (ØVM2)
TP1 (fH VCO)
TP6 (PRE VIDEO)
IC3
IC4
IC5
IC6
IC7
IC8
L3 fH VCO
CN1
b
a

※※ IC5 IS NOT SUPPLIED AS SPARE PARTS.

❖❖ IC2 IS NOT SUPPLIED AS INDIVIDUAL SPARE PASTS, SUPPLIED WITH CCD KIT(M17)

CN2

1	V.TRANSFER PULSE 1 OUT	10	DC 1 OUT
2	V.TRANSFER PULSE 2 OUT	11	DC2 OUT
3	V.TRANSFER PULSE 3 OUT	12	H.TRANSFER PULSE 1 OUT
4	V.TRANSFER PULSE 4 OUT	13	H.TRANSFER PULSE 3 OUT
5	CCD BIAS CONTROL OUT	14	RESET PULSE OUT
6	-8V OUT	15	+16V OUT
7	OG BIAS CONTROL OUT	16	DC3 OUT
8	LG BIAS OUT	17	GROUND
9	NOT USED	18	GROUND

TO
MOTHER BOARD

CN1

36	CP3 OUT	24	Vp IN	12	GROUND
35	+16V IN	23	+8.6V IN	11	GAIN CONTR
34	BL IN	22	1/2fH PULSE OUT	10	GROUND
33	-8V IN	21	PREVIDEO OUT	9	AIC VIDEO
32	CP5 OUT	20	SES CONTROL IN	8	GROUND
31	GROUND	19	AGC CONTROL OUT	7	WHD/WBL
30	GROUND	18	APERTURE SIGNAL IN	6	+4.9V IN
29	NOT USED	17	MANUAL CONTROL OUT	5	LOW LIGHT
28	R SC OUT	16	GROUND	4	+8.6V IN
27	NOT USED	15	YH OUT	3	AGC DC OUT
26	WHD IN	14	GROUND	2	+1.8V IN
25	GROUND	13	NOT USED	1	+4.9V IN

1 2 3 4 5 6

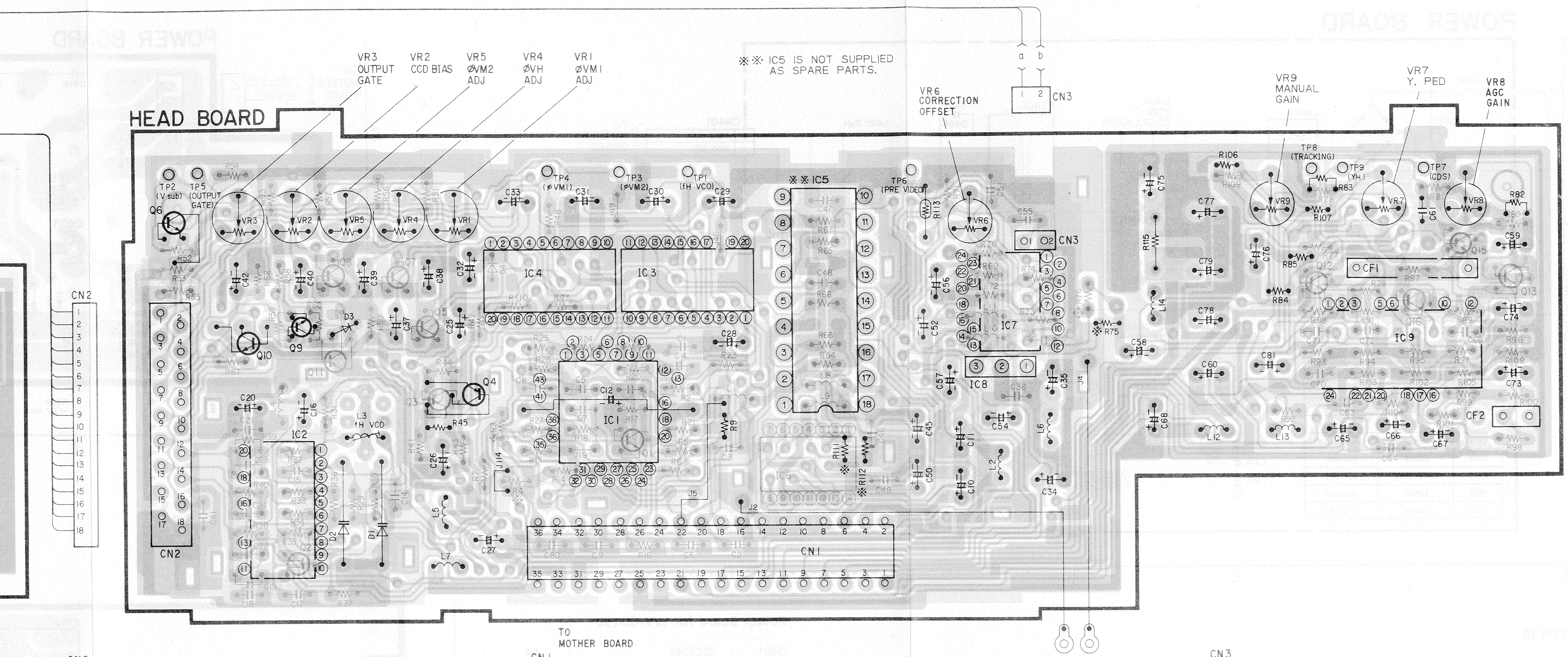
HEAD Board

IC1	MN51007LVH		B5	IC8	NJM2930L05		B6	Q6	2SD601-QR	(Y)	C4	Q13	2SD601-QR	(Y)	C8
IC2	MN50003LVE		B4	IC9	AN2110S		B8	Q7	2SD602-QRS	(W)	C4	Q15	2SD601-QR	(Y)	C8
IC3	UPD6146G		C5	Q1	2SD601-QR	(Y)	B5	Q8	2SD602-QRS	(W)	C4	Q16	2SB709-Q	(A)	C8
IC4	UPD6146G		C5	Q2	2SK198-Q	(1Q)	B4	Q9	2SD602-QRS	(W)	C4	D1	1SV153		B4
IC5	YFVAKA0497AN		C6	Q3	2SA1022-C	(E)	B4	Q10	2SD973A-RS		B4	D2	1SV153		B4
IC6	MN74HC08S		B6	Q4	2SA1022-C	(E)	B5	Q11	2SB710-Q	(C)	B4	D3	MA151K	(MH)	C4
IC7	M51266FP		C7	Q5	2SD602-QRS	(W)	C4	Q12	2SC2404-CDTW	(U)	C8	D4	MA151K	(MH)	C5

CCD Board

IC1	CXB0026M		B2	Q5	2SA1022-C
Q1	2SC2404-CDTW	(U)	B2	D1	MA151K
Q2	2SA1022-C	(E)	B3	D2	MA151WA
Q3	2SC2404-CDTW	(U)	B2		
Q4	2SC2404-CDTW	(U)	B2		

CONDUCTOR VIEW OF HEAD BOARD


HEAD BOARD
VR3 OUTPUT GATE
VR2 CCD BIAS
VR5 ØVM2 ADJ
VR4 ØVH ADJ
VR1 ØVM1 ADJ
※※ IC5 IS NOT SUPPLIED AS SPARE PARTS.
VR6 CORRECTION OFFSET
CN3
a
b
VR9 MANUAL GAIN
VR7 Y. PED
VR8 AGC GAIN
TP2 (V sub)
TP5 (OUTPUT GATE)
TP4 (ØVM1)
TP3 (ØVM2)
TP1 (fH VCO)
TP6 (PRE VIDEO)
TP8 (TRACKING)
TP9 (YH)
TP7 (CDS)
※※IC5
IC4
IC3
IC1
IC2
IC6
IC7
IC8
IC9
CF1
CF2
L3 fH VCO
CN2
CN1
TO MOTHER BOARD

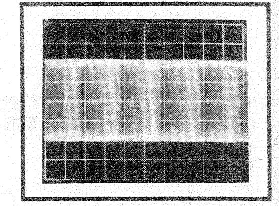
CN2			
1	V.TRANSFER PULSE 1 OUT	10	DC1 OUT
2	V.TRANSFER PULSE 2 OUT	11	DC2 OUT
3	V.TRANSFER PULSE 3 OUT	12	H.TRANSFER PULSE 1 OUT
4	V.TRANSFER PULSE 4 OUT	13	H.TRANSFER PULSE 3 OUT
5	CCD BIAS CONTROL OUT	14	RESET PULSE OUT
6	-8V OUT	15	+16V OUT
7	OG BIAS CONTROL OUT	16	DC3 OUT
8	LG BIAS OUT	17	GROUND
9	NOT USED	18	GROUND

CN1					
36	CP3 OUT	24	Vp IN	12	GROUND
35	+16V IN	23	+8.6V IN	11	GAIN CONTROL IN
34	BL IN	22	1/2 fH PULSE OUT	10	GROUND
33	-8V IN	21	PREVIDEO OUT	9	AIC VIDEO OUT
32	CP5 OUT	20	SES CONTROL IN	8	GROUND
31	GROUND	19	AGC CONTROL OUT	7	WHD/WBL IN
30	GROUND	18	APERTURE SIGNAL IN	6	+4.9V IN
29	NOT USED	17	MANUAL CONTROL OUT	5	LOW LIGHT INF OUT
28	R SC OUT	16	GROUND	4	+8.6V IN
27	NOT USED	15	YH OUT	3	AGC DC OUT
26	WHD IN	14	GROUND	2	+1.8V IN
25	GROUND	13	NOT USED	1	+4.9V IN

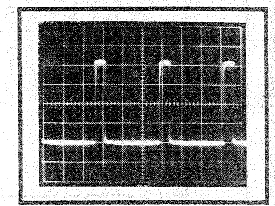
CN3	
1	PREVIDEO IN
2	GROUND

—W— COMPONENT SIDE
—W— PATTERN SIDE

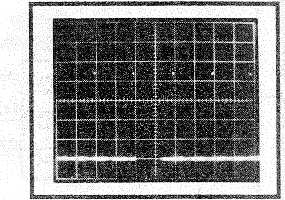
1. 0.2V/DIV, 5ms/DIV



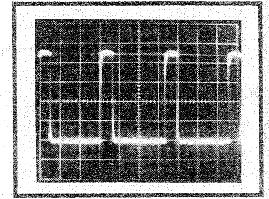
2. 0.1V/DIV, 20μs/DIV



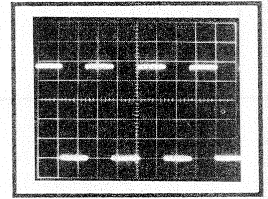
3. 0.1V/DIV, 10μs/DIV



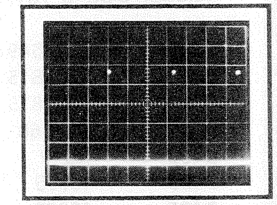
4. 0.1/DIV, 20μs/DIV



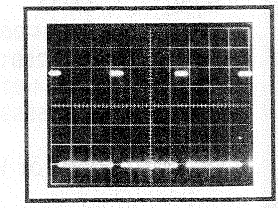
5. 0.1V/DIV, 20μs/DIV



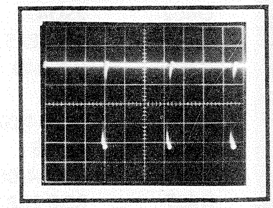
6. 0.1V/DIV, 20μs/DIV



7. 0.1V/DIV, 20μs/DIV



8. 0.2V/DIV, 20μs/DIV



3 4 5 6 7 8

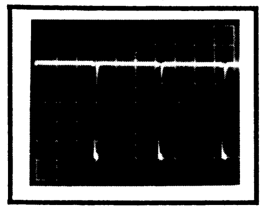
2SD601-QR	(Y)	C4	Q13	2SD601-QR	(Y)	C8
2SD602-QRS	(W)	C4	Q15	2SD601-QR	(Y)	C8
2SD602-QRS	(W)	C4	Q16	2SB709-Q	(A)	C8
2SD602-QRS	(W)	C4	D1	1SV153		B4
2SD973A-RS		B4	D2	1SV153		B4
2SB710-Q	(C)	B4	D3	MA151K	(MH)	C4
2SC2404-CDTW	(U)	C8	D4	MA151K	(MH)	C5

CCD Board

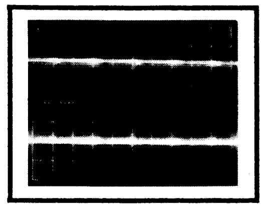
IC1	CXB0026M		B2	Q5	2SA1022-C	(E)	B2
Q1	2SC2404-CDTW	(U)	B2	D1	MA151K	(MH)	B3
Q2	2SA1022-C	(E)	B3	D2	MA151WA	(MN)	B2
Q3	2SC2404-CDTW	(U)	B2				
Q4	2SC2404-CDTW	(U)	B2				

	CCD image sensor without IC5 on Head board	CCD image sensor with IC5 on Head board
R3	Not Used	1K ohms
R4	Not Used	1K ohms
R75	0 ohm	Not Used
R76	Not Used	0 ohm
R111	1K ohms	Not Used
R112	1K ohms	Not Used

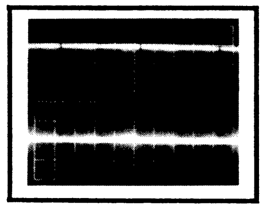
9. 0.2V/DIV, 20μs/DIV



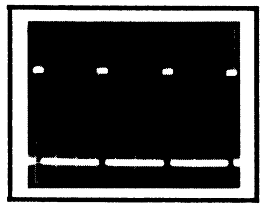
10. 0.2V/DIV, 5ms/DIV



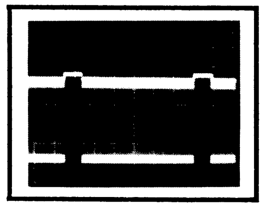
11. 0.2V/DIV, 5ms/DIV



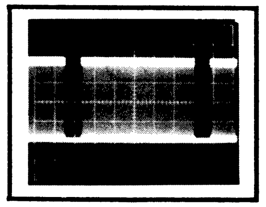
12. 0.1V/DIV, 20μs/DIV



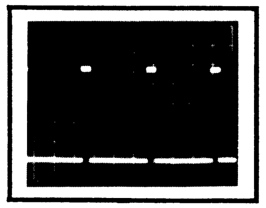
13. 0.1V/DIV, 10μs/DIV



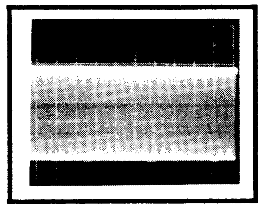
14. 0.1V/DIV, 10μs/DIV



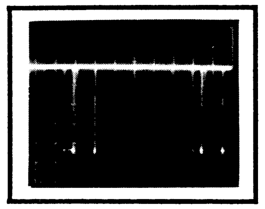
15. 0.1V/DIV, 20μs/DIV



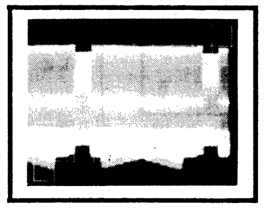
16. 0.1V/DIV, 20μs/DIV



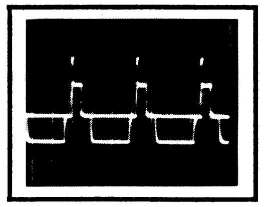
17. 0.1V/DIV, 10μs/DIV



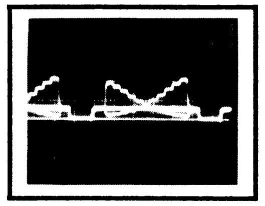
18. 20mV/DIV, 10μs/DIV



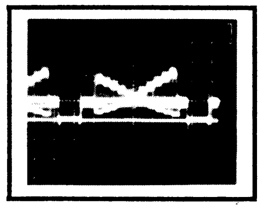
19. 0.1V/DIV, 20μs/DIV



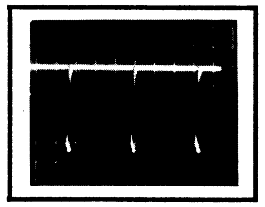
20. 20mV/DIV, 10μs/DIV



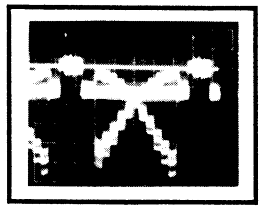
21. 20mV/DIV, 10μs/DIV



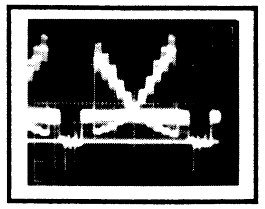
22. 2V/DIV, 20μs/DIV



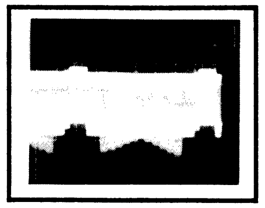
23. 50mV/DIV, 10μs/DIV



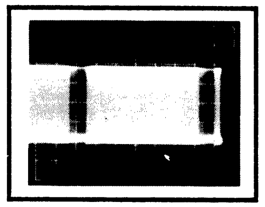
24. 50mV/DIV, 10μs/DIV



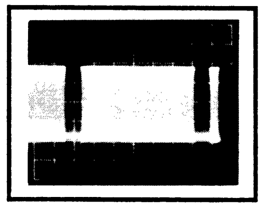
25. 0.2V/DIV, 10μs/DIV



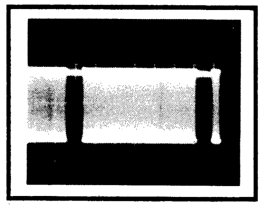
26. 2V/DIV, 10μs/DIV



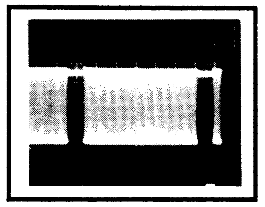
27. 2V/DIV, 10μs/DIV



28. 2V/DIV, 10μs/DIV



29. 2V/DIV, 10μs/DIV



	IC1	IC2	IC3	IC4	IC6	IC7	IC8	IC9
Pin 1	0.2	-0.9	0	-6.5	-9.1	0	8.6	1.9
2	0.3	2.2	-5.0	2.4	4.7	2.5	0	1.8
3	4.2	2.2	3.1	3.1	-94.0	0	5.0	1.8
4	4.3	-20.3	0	0	-37.4	-9.8		1.8
5	4.7	-56.3	2.7	-6.5	4.6	4.7		1.8
6	4.6	-58.0	4.7	4.7	-104.1	-7.1		2.3
7	0	-22.0	0	0	0	2.4		0
8	0	1.1	0.2	0	-97.0	2.5		0
9	0.1	1.2	4.7	0	4.6	4.7		1.9
10	0.1	0	0	0.3	-103.5	2.7		1.9
11	0	0	0	4.3	-96.8	0		0
12	4.6	0	4.6	0.3	4.6	2.5		4.7
13	0	0	0	0.3	0	4.7		1.9
14	0	-2.0	0	0	4.6			2.6
15	0.3	1.6	0	0		—		3.1
16	4.7	0	13.3	13.3		0		1.7
17	4.7	2.1	0	0		0		1.9
18	4.7	0.6	-4.3	-6.5		-111		2.6
19	2.3	4.7	3.1	3.1		-111		2.0
20	4.7	4.7	-5.0	-5.9		4.7		2.0
21	0					4.9		0.1
22	4.6					0		1.3
23	0.7					0		0
24	0					2.4		1.8
25	1.2							
26	2.3							
27	2.3							
28	2.3							
29	2.3							
30	-11.6							
31	-11.1							
32	-11.1							
33	-10.6							
34	0							
35	0							
36	0.2							
37	0							
38	0.7							
39	0							
40	0							
41	2.2							
42	0.6							
43	0.7							
44	0.7							

	Q1	Q2	Q3	Q4	Q5	Q6	Q7	Q8	Q9	Q10	Q11	Q12	Q13	Q15	Q16
B	2.0	G 0.7	-5.7	-7.3	3.6	11.4	13.9	3.7	0	8.4	7.7	1.5	3.8	3.7	3.1
C	4.9	D 4.6	-8.0	-7.4	16.0	15.6	16.0	0	15.5	8.6	0	4.0	8.6	8.6	0
E	1.5	S 3.0	-5.0	-6.5	3.0	10.9	13.2	3.1	11.7	7.7	8.3	0.9	3.2	3.1	3.8

CCD Board

	IC1	IC2
Pin 1	0	---
2	0.8	16.0
3	0	3.1
4	0.6	9.1
5	4.6	16.0
6	7.7	6.9
7	4.8	4.8
8	0	10.1
9		2.3
10		4.6
11		-8.0
12		0
13		10.9
14		16.0
15		-5.9
16		-4.3
17		2.4
18		2.7
19		0
20		0

	Q1	Q2	Q3	Q4	Q5
B	9.1	11.9	0.6	---	---
C	16.0	---	---	11.7	0
E	8.5	11.7	0	---	---

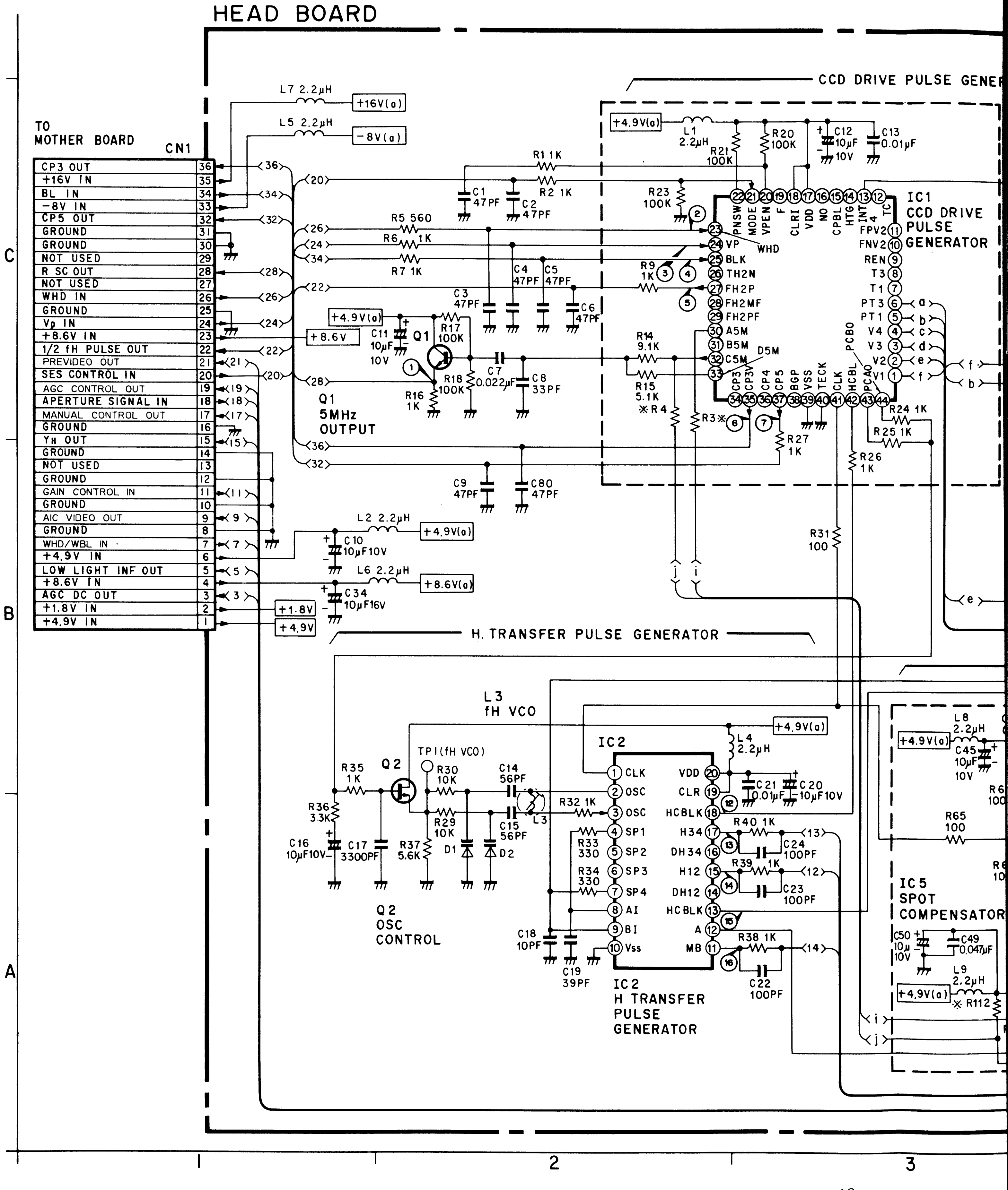
	CCD image sensor without IC5 on Head board	CCD image sensor with IC5 on Head board
R3	Not Used	1K ohms
R4	Not Used	1K ohms
R75	0 ohm	Not Used
R76	Not Used	0 ohm
R111	1K ohms	Not Used
R112	1K ohms	Not Used

Head Board

IC1	MN51007LVH		C3	Q6	2SD601-QR	(Y)	C5
IC2	MN50003LVE		A2	Q7	2SD602-QRS	(W)	C5
IC3	UPD6146G		C4	Q8	2SD602-QRS	(W)	C5
IC4	UPD6146G		B4	Q9	2SD602-QRS	(W)	B5
IC5	YFVAKA0497AN		A4	Q10	2SD973A-RS		B5
IC6	MN74HC08S		A4	Q11	2SB710-Q	(C)	B5
IC7	M51266FP		A5	Q12	2SC2404-CDTW	(U)	A6
IC8	NJM2930L05		A5	Q13	2SD601-QR	(Y)	A7
IC9	AN2110S		B7	Q15	2SD601-QR	(Y)	A7
Q1	2SD601-QR	(Y)	C2	Q16	2SB709-Q	(A)	A7
Q2	2SK198-Q	(1Q)	B2	D1	1SV153		A2
Q3	2SA1022-C	(E)	C4	D2	1SV153		A2
Q4	2SA1022-C	(E)	C4	D3	MA151K	(MH)	C5
Q5	2SD602-QRS	(W)	C5	D4	MA151K	(MH)	C5

CCD Board

IC1	CXB0026M		C8
IC2	YFVAKA0497AN		C7
Q1	2SC2404-CDTW	(U)	C7
Q2	2SA1022-C	(E)	C8
Q3	2SC2404-CDTW	(U)	C8
Q4	2SC2404-CDTW	(U)	C8
Q5	2SA1022-C	(E)	C8
D1	MA151K	(MH)	C8
D2	MA151WA	(MN)	C7


HEAD BOARD
TO MOTHER BOARD
CN1
CP3 OUT 36
+16V IN 35
BL IN 34
−8V IN 33
CP5 OUT 32
GROUND 31
GROUND 30
NOT USED 29
R SC OUT 28
NOT USED 27
WHD IN 26
GROUND 25
Vp IN 24
+8.6V IN 23
1/2 fH PULSE OUT 22
PREVIDEO OUT 21
SES CONTROL IN 20
AGC CONTROL OUT 19
APERTURE SIGNAL IN 18
MANUAL CONTROL OUT 17
GROUND 16
YH OUT 15
GROUND 14
NOT USED 13
GROUND 12
GAIN CONTROL IN 11
GROUND 10
AIC VIDEO OUT 9
GROUND 8
WHD/WBL IN 7
+4.9V IN 6
LOW LIGHT INF OUT 5
+8.6V IN 4
AGC DC OUT 3
+1.8V IN 2
+4.9V IN 1
L7 2.2μH
+16V(a)
L5 2.2μH
−8V(a)
CCD DRIVE PULSE GENER
+4.9V(a)
L1 2.2μH
R21 100K
R20 100K
C12 10μF 10V
C13 0.01μF
R1 1K
R2 1K
C1 47PF
C2 47PF
R5 560
R6 1K
R7 1K
R23 100K
IC1 CCD DRIVE PULSE GENERATOR
PNSW MODE VPEN F CLRI VDD NO CPBL HTG INT 4 TC
VP WHD BLK TH2N FH2P FH2MF FH2PF A5M B5M C5M D5M
FPV2 FNV2 REN T3 T1 PT3 PT1 V4 V3 V2 V1
CP3 CP3V CP4 CP5 BGP VSS TECK CLK HCBL PCAO PCBO
R9 1K
C3 47PF
C4 47PF
C5 47PF
C6 47PF
Q1
Q1 5MHz OUTPUT
+8.6V
C11 10μF 10V
R17 100K
R18 100K
R16 1K
C7 0.022μF
C8 33PF
R14 9.1K
R15 5.1K
※R4
R3※
R24 1K
R25 1K
R26 1K
R27 1K
C9 47PF
C80 47PF
L2 2.2μH
C10 10μF10V
L6 2.2μH
+8.6V(a)
C34 10μF16V
+1.8V
+4.9V
R31 100
H. TRANSFER PULSE GENERATOR
L3 fH VCO
IC2
L4 2.2μH
TP1(fH VCO)
Q2
R35 1K
R36 3.3K
R30 10K
C14 56PF
C15 56PF
R29 10K
R32 1K
C16 10μF10V
C17 3300PF
R37 5.6K
D1
D2
R33 330
R34 330
CLK OSC OSC SP1 SP2 SP3 SP4 AI BI Vss
VDD CLR HCBLK H34 DH34 H12 DH12 HCBLK A MB
C21 0.01μF
C20 10μF10V
R40 1K
C24 100PF
R39 1K
C23 100PF
R38 1K
C22 100PF
C18 10PF
C19 39PF
Q2 OSC CONTROL
IC2 H TRANSFER PULSE GENERATOR
L8 2.2μH
C45 10μF 10V
R65 100
IC5 SPOT COMPENSATOR
C50 10μ 10V
C49 0.047μF
L9 2.2μH
※R112
A
B
C
1
2
3

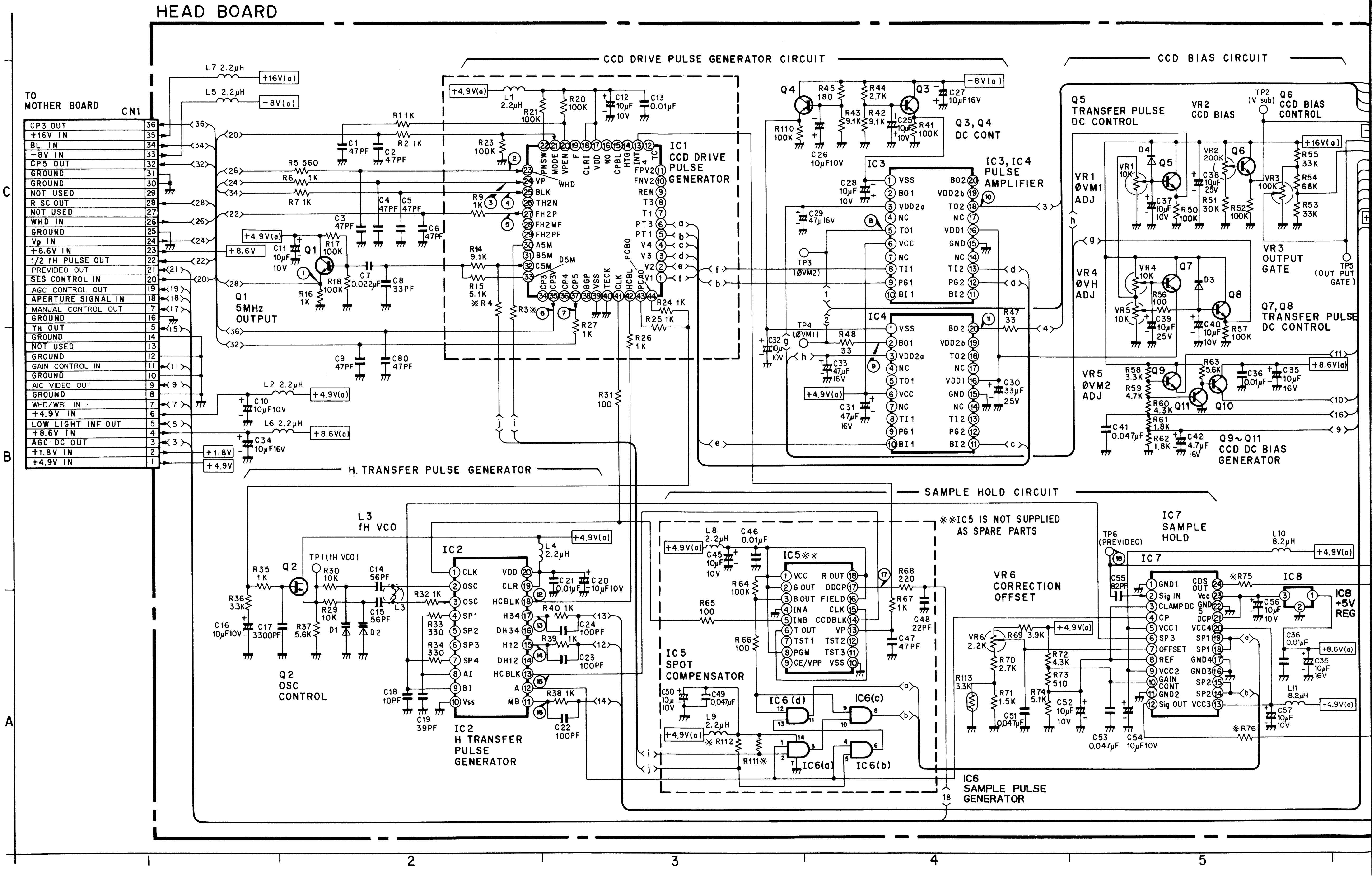
IC9
1.9
1.8
1.8
1.8
1.8
2.3
0
0
1.9
1.9
0
4.7
1.9
2.6
3.1
1.7
1.9
2.6
2.0
2.0
0.1
1.3
0
1.8

Q10	Q11	Q12	Q13	Q15	Q16
8.4	7.7	1.5	3.8	3.7	3.1
8.6	0	4.0	8.6	8.6	0
7.7	8.3	0.9	3.2	3.1	3.8

Q4	Q5
---	---
11.7	0
---	---

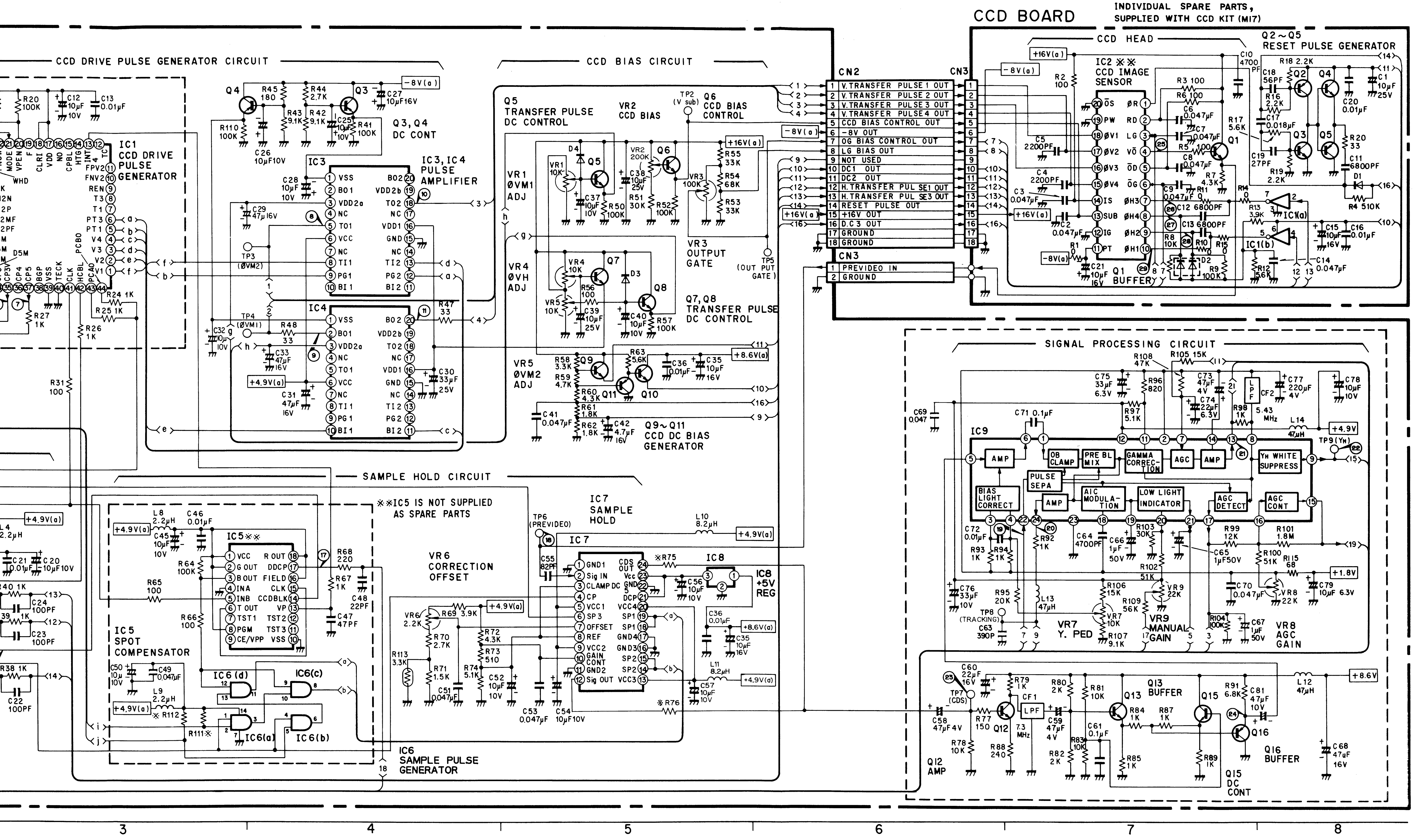
or n	CCD image sensor with IC5 on Head board
	1K ohms
	1K ohms
	Not Used
	0 ohm
	Not Used
	Not Used

SCHEMATIC DIAGRAM OF HEAD BOARD

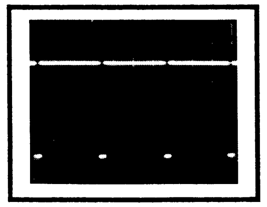

HEAD BOARD
TO MOTHER BOARD
CN1
CP3 OUT 36
+16V IN 35
BL IN 34
−8V IN 33
CP5 OUT 32
GROUND 31
GROUND 30
NOT USED 29
R SC OUT 28
NOT USED 27
WHD IN 26
GROUND 25
Vp IN 24
+8.6V IN 23
1/2 fH PULSE OUT 22
PREVIDEO OUT 21
SES CONTROL IN 20
AGC CONTROL OUT 19
APERTURE SIGNAL IN 18
MANUAL CONTROL OUT 17
GROUND 16
YH OUT 15
GROUND 14
NOT USED 13
GROUND 12
GAIN CONTROL IN 11
GROUND 10
AIC VIDEO OUT 9
GROUND 8
WHD/WBL IN 7
+4.9V IN 6
LOW LIGHT INF OUT 5
+8.6V IN 4
AGC DC OUT 3
+1.8V IN 2
+4.9V IN 1
CCD DRIVE PULSE GENERATOR CIRCUIT
IC1 CCD DRIVE PULSE GENERATOR
Q1 5MHz OUTPUT
CCD BIAS CIRCUIT
Q3, Q4 DC CONT
IC3, IC4 PULSE AMPLIFIER
Q5 TRANSFER PULSE DC CONTROL
VR2 CCD BIAS
Q6 CCD BIAS CONTROL
VR1 ØVM1 ADJ
VR4 ØVH ADJ
VR3 OUTPUT GATE
TP5 (OUT PUT GATE)
TP2 (V sub)
Q7, Q8 TRANSFER PULSE DC CONTROL
VR5 ØVM2 ADJ
Q9~Q11 CCD DC BIAS GENERATOR
TP3 (ØVM2)
TP4 (ØVM1)
H. TRANSFER PULSE GENERATOR
L3 fH VCO
TP1 (fH VCO)
Q2 OSC CONTROL
IC2 H TRANSFER PULSE GENERATOR
SAMPLE HOLD CIRCUIT
※※IC5 IS NOT SUPPLIED AS SPARE PARTS
IC5 SPOT COMPENSATOR
IC6 SAMPLE PULSE GENERATOR
VR6 CORRECTION OFFSET
IC7 SAMPLE HOLD
TP6 (PREVIDEO)
IC8 +5V REG
+16V(a)
−8V(a)
+4.9V(a)
+8.6V
+8.6V(a)
+1.8V
+4.9V
A
B
C
1
2
3
4
5

SCHEMATIC DIAGRAM OF HEAD BOARD

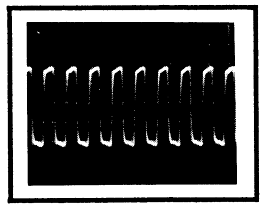
※※IC2 IS NOT SUPPLIED AS INDIVIDUAL SPARE PARTS, SUPPLIED WITH CCD KIT (MI7)


CCD BOARD
CCD DRIVE PULSE GENERATOR CIRCUIT
IC1 CCD DRIVE PULSE GENERATOR
Q3, Q4 DC CONT
IC3, IC4 PULSE AMPLIFIER
IC3
IC4
TP3 (ØVM2)
TP4 (ØVM1)
CCD BIAS CIRCUIT
Q5 TRANSFER PULSE DC CONTROL
VR2 CCD BIAS
TP2 (V sub)
Q6 CCD BIAS CONTROL
VR1 ØVM1 ADJ
VR4 ØVH ADJ
VR3 OUTPUT GATE
TP5 (OUT PUT GATE)
Q7, Q8 TRANSFER PULSE DC CONTROL
VR5 ØVM2 ADJ
Q9~Q11 CCD DC BIAS GENERATOR
CN2
CN3
1 V.TRANSFER PULSE 1 OUT
2 V.TRANSFER PULSE 2 OUT
3 V.TRANSFER PULSE 3 OUT
4 V.TRANSFER PULSE 4 OUT
5 CCD BIAS CONTROL OUT
6 -8V OUT
7 OG BIAS CONTROL OUT
8 LG BIAS OUT
9 NOT USED
10 DC1 OUT
11 DC2 OUT
12 H.TRANSFER PUL SE1 OUT
13 H.TRANSFER PUL SE3 OUT
14 RESET PULSE OUT
15 +16V OUT
16 D.C 3 OUT
17 GROUND
18 GROUND
CN3
1 PREVIDEO IN
2 GROUND
CCD HEAD
IC2 ※※ CCD IMAGE SENSOR
Q1 BUFFER
Q2~Q5 RESET PULSE GENERATOR
IC1(a)
IC1(b)
SIGNAL PROCESSING CIRCUIT
IC9
AMP
OB CLAMP
PRE BL MIX
GAMMA CORRECTION
AGC
AMP
YH WHITE SUPPRESS
PULSE SEPA
BIAS LIGHT CORRECT
AMP
AIC MODULATION
LOW LIGHT INDICATOR
AGC DETECT
AGC CONT
TP9 (YH)
TP8 (TRACKING)
VR7 Y. PED
VR9 MANUAL GAIN
VR8 AGC GAIN
TP7 (CDS)
Q12 AMP
LPF
Q13 BUFFER
Q15 DC CONT
Q16 BUFFER
SAMPLE HOLD CIRCUIT
※※IC5 IS NOT SUPPLIED AS SPARE PARTS
IC5※※
IC5 SPOT COMPENSATOR
IC6(a)
IC6(b)
IC6(c)
IC6(d)
IC6 SAMPLE PULSE GENERATOR
VR6 CORRECTION OFFSET
TP6 (PREVIDEO)
IC7 SAMPLE HOLD
IC7
IC8 +5V REG
3
4
5
6
7
8

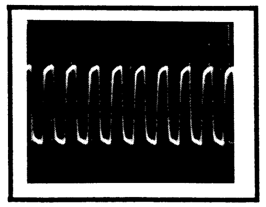
1. 1V/DIV, 20µs/DIV



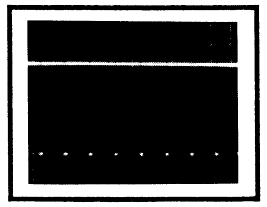
2. 1V/DIV, 20µs/DIV



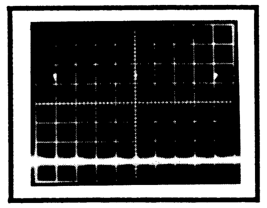
3. 1V/DIV, 20µs/DIV



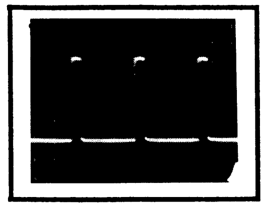
4. 1V/DIV, 5ms/DIV



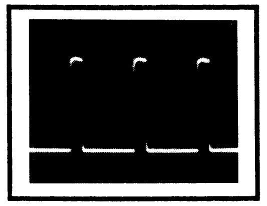
5. 1V/DIV, 5ms/DIV



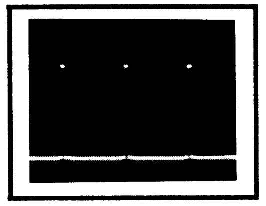
6. 1V/DIV, 20µs/DIV



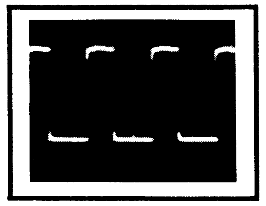
7. 1V/DIV, 20µs/DIV



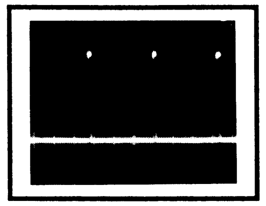
8. 1V/DIV, 20µs/DIV



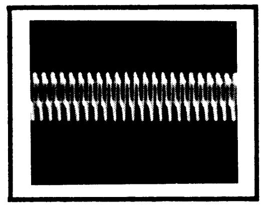
9. 1V/DIV, 20µs/DIV



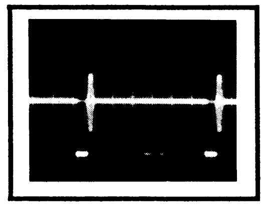
10. 1V/DIV, 20µs/DIV



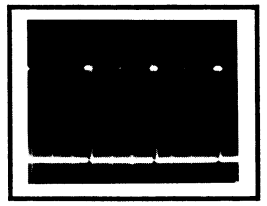
11. 20mV/DIV, 0.5µs/DIV



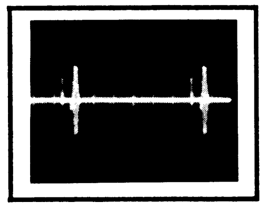
12. 0.1V/DIV, 10µs/DIV



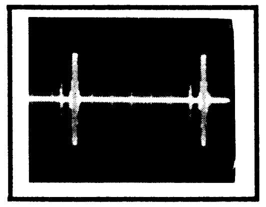
13. 1V/DIV, 20µs/DIV



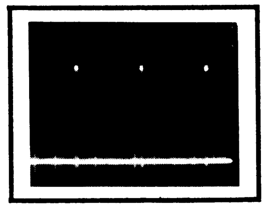
14. 0.2V/DIV, 10µs/DIV



15. 0.2V/DIV, 10µs/DIV



16. 1V/DIv, 20µs/DIV



Note : Voltage shows with ( ) are measured by Gen-lock mode.

	IC1	IC2	IC3	IC4	IC5	IC6	IC7	IC8
Pin 1	2.5	(0) 0	(0) 0	(4.6) 4.6	(4.6) 4.6	(4.6) 4.7	4.7	(0) 0
2	1.8	(1.7) 1.7	(0) 0	(0.3) 0	(4.6) 4.6	(0) 0	4.7	(4.4) 4.7
3	1.8	(0.1) 0	(2.1) 0.5	(4.2) 4.6	(0.7) 0.7	(3.6) 0	0	(4.6) 4.7
4	2.5	(4.3) 4.6	(2.1) 1.8	(0)0	(0) 0	(0) 2.4	0	(0) 4.7
5	0.9	(0) 0	(1.8) 1.8	(0) 0	(0.1) 0	(4.6) 2.3	4.6	(0.2) 0
6	7.1	(0) 0	(0) 0	(4.7) 4.7	(0.2) 0	(2.2) 2.2	4.6	(4.4) 4.7
7	0	(4.5) 0	(0) 0	(0) 0	(0) 0	(2.2) 2.1	0	(0) 0
8	3.9	(0) 0	(0) 0	(0.3) 0	(4.6) 4.6	(0) 0	4.6	(4.7) 4.7
9	0	(0) 0	(4.7) 0	(4.3) 4.7	(4.2) 4.7	(0) 0	4.6	(0) 0
10	3.9	(0) 0	(4.7) 0	(4.3) 4.7	(4.6) 4.6	(4.6) 0	0	(4.7) 0
11	0	(0.6) 0	(4.7) 0	(4.3) 4.7	(0.5) 0.5	(2.3) 0	0	(0) 4.7
12	7.1	(4.4) 4.7	(0) 0	(0.1) 0	(2.4) 0	(2.3) 2.3	4.6	(0) 0
13	0	(0.2) 0	(0) 0	(0) 0	(2.2) 1.7	(4.6) 4.7	4.6	(4.7) 4.7
14	0	(0) 0	(0) 0	(4.7) 4.7	(2.3) 4.3	(2.3) 0	4.6	(4.7) 4.7
15		(4.6) 4.6	(0) 0		(2.3) 0	(3.9) 2.3		
16		(4.7) 4.7	(4.7) 4.7		(4.6) 4.7	(2.6) 2.3		
17					(2.3) 2.3	(4.6) 0.8		
18					(2.3) 0	(3.9) 3.9		
19					(4.6) 0	(0.2) 0.2		
20					(0.6) 0	(3.0) 3.0		
21					(4.6) 4.6	(0.2) 0.2		
22					(0) 0	(0) 0		
23					(4.6) 4.7	(1.2) 1.2		
24					(2.2) 2.3	(0.7) 0.7		
25					(1.7) 0	(0) 0		
26					(2.4) 0	(4.2) 4.2		
27					(2.4) 4.7	(0) 0		
28					(3.6) 0	(4.7) 4.7		

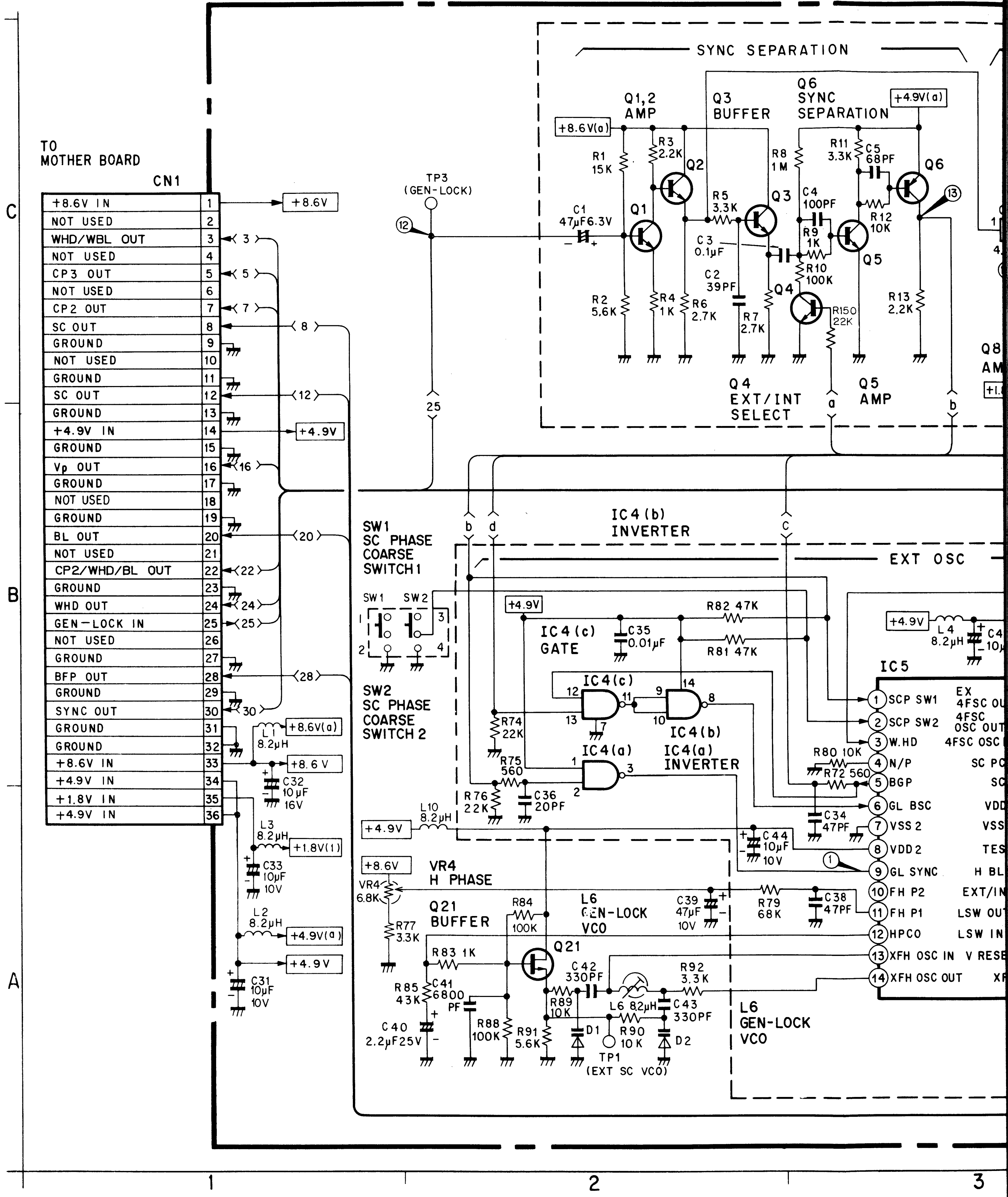
	Q1	Q2	Q3	Q4	Q5	Q6	Q7	Q8	Q9	Q10	Q11	Q12	Q13	Q14	Q15	Q16	Q17
B	2.2	4.7	4.1	0.6	0.4	4.6	1.7	0	0	4.7	1.6	1.0	1.7	1.6	1.7	1.7	2.9
C	4.7	8.2	8.2	0	4.6	0	3.6	0	4.6	4.7	3.7	0	2.9	3.7	4.7	2.9	4.7
E	1.5	4.1	3.5	0	0	4.7	0	1.0	0	3.9	1.0	1.6	1.0	1.0	1.0	1.0	2.2

	Q18	Q19	Q20	Q21	Q22	Q24	Q25
B	(0.6) 0	(0) 0.3	(1.2) 1.2	(2.5) G 0	(1.7) G 0	2.2	(0) G 0.9
C	(0) 4.5	(0) 0	(0) 0	(4.7) D 4.7	(4.7) D 4.7	4.7	(4.7) D 4.7
E	(0) 0	(0.9) 1.0	(1.9) 1.9	(2.9) S 0.6	(2.1) S 0.5	1.5	(0.6) S 1.4

Gen-Lock/Sync Board

IC1	NJM1496M		C5
IC2	MN74HC221S		C7
IC3	MN4053BS		B4
IC4	MN74HC00S		B2
IC5	MN6761S		A3
IC6	MN67602PS		A5
IC7	MN74HC00S		A7
IC8	MN74HC74S		A7
Q1	2SC2404-CDTW	(U)	C2
Q2	2SD601-QR	(Y)	C2
Q3	2SD601-QR	(Y)	C2
Q4	2SD601-QR	(Y)	C3
Q5	2SC2404-CDTW	(U)	C3
Q6	2SB709-Q	(A)	C3
Q7	2SC2404-CDTW	(U)	C3
Q8	2SC2404-CDTW	(U)	C3
Q9	2SC2404-CDTW	(U)	C4
Q10	2SC2404-CDTW	(U)	C4
Q11	2SC2404-CDTW	(U)	C4
Q12	2SA1022-C	(E)	C4
Q13	2SC2404-CDTW	(U)	C6
Q14	2SC2404-CDTW	(U)	C6
Q15	2SC2404-CDTW	(U)	C6
Q16	2SC2404-CDTW	(U)	C6
Q17	2SC2404-CDTW	(U)	C6
Q18	2SD601-QR	(Y)	B7
Q19	2SB709-Q	(A)	B6
Q20	2SB709-Q	(A)	B7
Q21	2SK198-Q	(1Q)	A2
Q22	2SK198-Q	(1Q)	B4
Q23	2SC2404-CDTW	(U)	A4
Q24	2SC2404-CDTW	(U)	A4
Q25	2SK198-PQRTX	(1Q)	A6
D1	1SV153		A2
D2	1SV153		A2
D3	1SV153		A4
D4	1SV153		A4
D5	1SV153		A5
D6	1SV153		A6
D7	1SV153		A6

GEN LOCK/SYNC BOARD

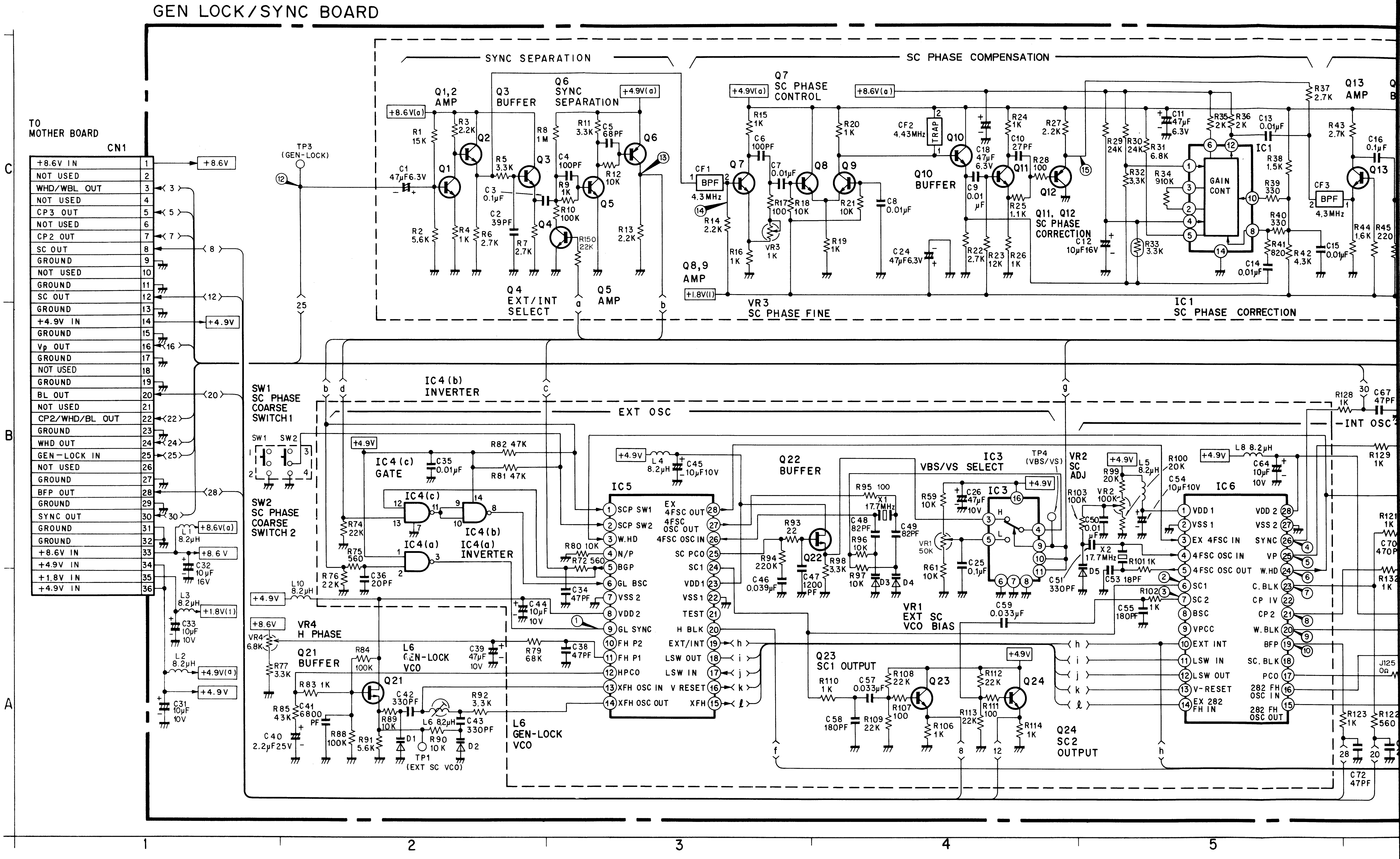

TO MOTHER BOARD
CN1
+8.6V IN
NOT USED
WHD/WBL OUT
CP3 OUT
CP2 OUT
SC OUT
GROUND
+4.9V IN
Vp OUT
BL OUT
CP2/WHD/BL OUT
WHD OUT
GEN-LOCK IN
BFP OUT
SYNC OUT
+1.8V IN
SYNC SEPARATION
Q1,2 AMP
Q3 BUFFER
Q6 SYNC SEPARATION
Q4 EXT/INT SELECT
Q5 AMP
TP3 (GEN-LOCK)
IC4(b) INVERTER
SW1 SC PHASE COARSE SWITCH1
SW2 SC PHASE COARSE SWITCH 2
IC4(c) GATE
IC4(a) INVERTER
EXT OSC
IC5
VR4 H PHASE
Q21 BUFFER
L6 GEN-LOCK VCO
TP1 (EXT SC VCO)
SCP SW1
SCP SW2
W.HD
N/P
BGP
GL BSC
VSS 2
VDD 2
GL SYNC
FH P2
FH P1
HPCO
XFH OSC IN
XFH OSC OUT
+8.6V
+4.9V
+1.8V(l)
+4.9V(a)
+8.6V(a)
C
B
A
1
2
3

IC8
(0)
0
(4.4)
4.7
(4.6)
4.7
(0)
4.7
(0.2)
0
(4.4)
4.7
(0)
0
(4.7)
4.7
(0)
0
(4.7)
0
(0)
4.7
(0)
0
(4.7)
4.7
(4.7)
4.7

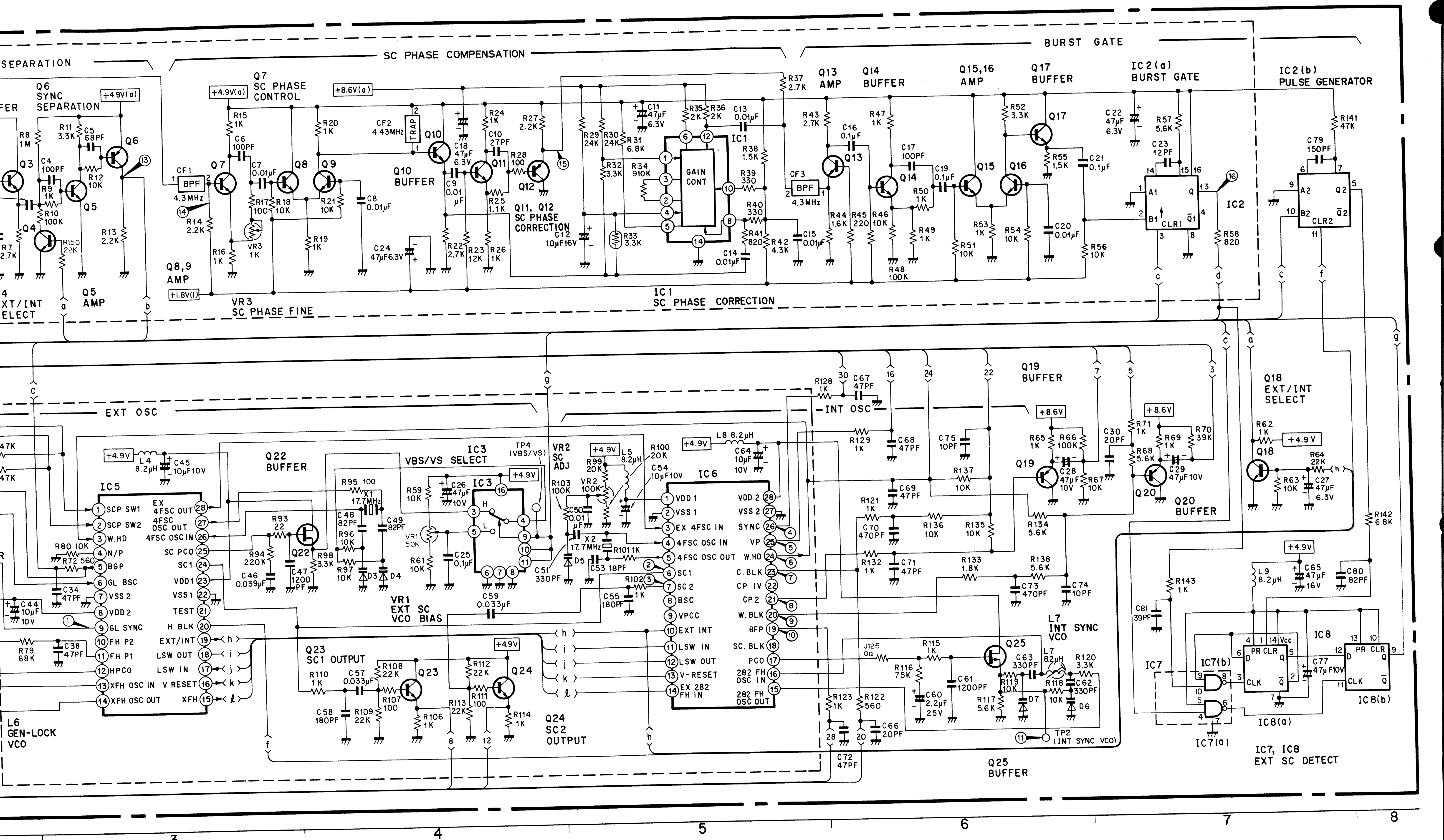
Q17
2.9
4.7
2.2

B6
B7
A2
B4
A4
A4
A6
A2
A2
A4
A4
A5
A6
A6

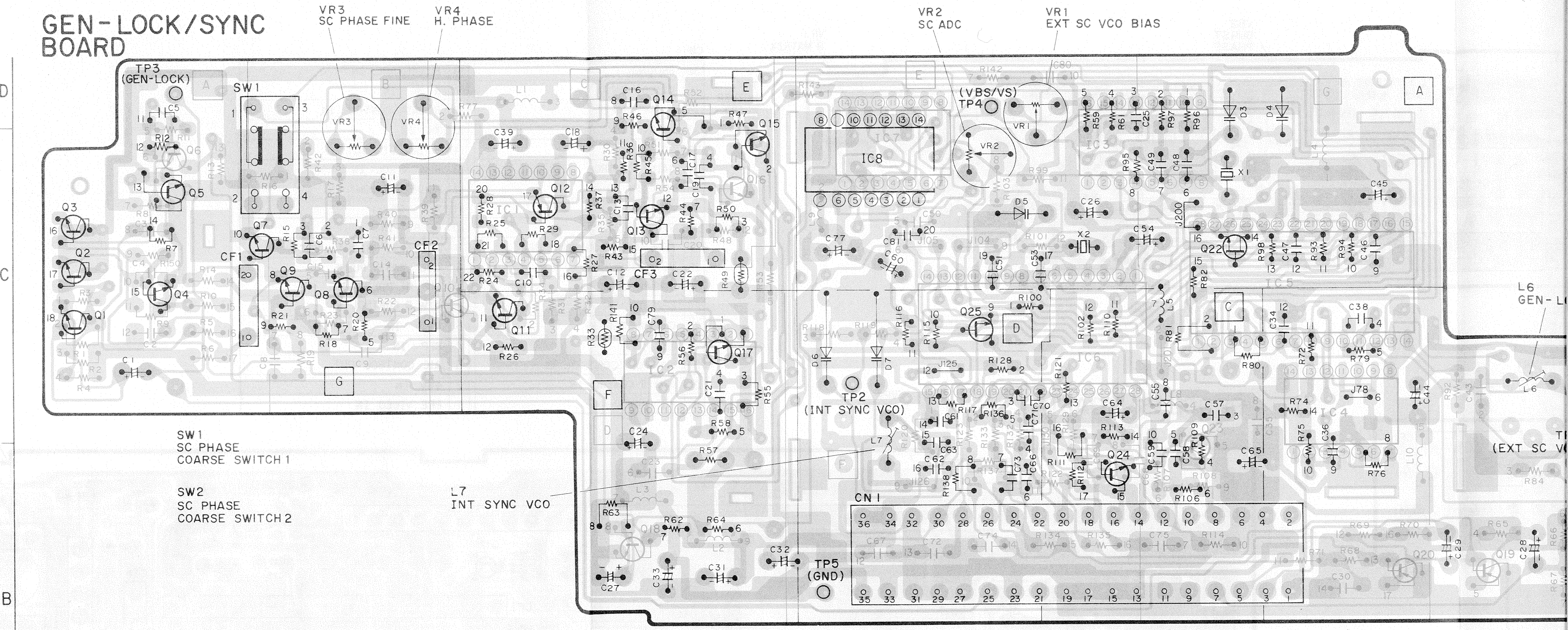
SCHEMATIC DIAGRAM OF GEN-LOCK BOARD


GEN LOCK/SYNC BOARD
TO MOTHER BOARD
CN1
+8.6V IN 1
NOT USED 2
WHD/WBL OUT 3
NOT USED 4
CP3 OUT 5
NOT USED 6
CP2 OUT 7
SC OUT 8
GROUND 9
NOT USED 10
GROUND 11
SC OUT 12
GROUND 13
+4.9V IN 14
GROUND 15
Vp OUT 16
GROUND 17
NOT USED 18
GROUND 19
BL OUT 20
NOT USED 21
CP2/WHD/BL OUT 22
GROUND 23
WHD OUT 24
GEN-LOCK IN 25
NOT USED 26
GROUND 27
BFP OUT 28
GROUND 29
SYNC OUT 30
GROUND 31
GROUND 32
+8.6V IN 33
+4.9V IN 34
+1.8V IN 35
+4.9V IN 36
TP3 (GEN-LOCK)
SYNC SEPARATION
Q1,2 AMP
Q3 BUFFER
Q6 SYNC SEPARATION
Q4 EXT/INT SELECT
Q5 AMP
SC PHASE COMPENSATION
Q7 SC PHASE CONTROL
Q8,9 AMP
Q10 BUFFER
Q11, Q12 SC PHASE CORRECTION
VR3 SC PHASE FINE
IC1 SC PHASE CORRECTION
GAIN CONT
Q13 AMP
CF1 BPF 4.3MHz
CF2 4.43MHz TRAP
CF3 BPF 4.3MHz
SW1 SC PHASE COARSE SWITCH 1
SW2 SC PHASE COARSE SWITCH 2
IC4(b) INVERTER
IC4(c) GATE
IC4(a) INVERTER
EXT OSC
INT OSC
IC5
SCP SW1
SCP SW2
W.HD
N/P
BGP
GL BSC
VSS 2
VDD 2
GL SYNC
FH P2
FH P1
HPCO
XFH OSC IN
XFH OSC OUT
EX 4FSC OUT
4FSC OSC OUT
4FSC OSC IN
SC PCO
SC1
VDD1
VSS1
TEST
H BLK
EXT/INT
LSW OUT
LSW IN
V RESET
XFH
Q22 BUFFER
IC3 VBS/VS SELECT
TP4 (VBS/VS)
VR1 EXT SC VCO BIAS
VR2 SC ADJ
IC6
VDD 1
VSS 1
EX 4FSC IN
4FSC OSC IN
4FSC OSC OUT
SC1
SC 2
BSC
VPCC
EXT INT
LSW IN
LSW OUT
V-RESET
EX 282 FH IN
VDD 2
VSS 2
SYNC
VP
W.HD
C.BLK
CP IV
CP 2
W.BLK
BFP
SC.BLK
PCO
282 FH OSC IN
282 FH OSC OUT
VR4 H PHASE
Q21 BUFFER
L6 GEN-LOCK VCO
TP1 (EXT SC VCO)
Q23 SC1 OUTPUT
Q24 SC2 OUTPUT
+8.6V
+4.9V
+8.6V(a)
+4.9V(a)
+1.8V(1)
1
2
3
4
5
A
B
C

SCHEMATIC DIAGRAM OF GEN-LOCK BOARD


SEPARATION
Q6 SYNC SEPARATION
+4.9V(a)
R8 1M
R11 3.3K
C5 68PF
Q6
Q3
C4 100PF
R9 1K
R12 10K
Q5
R10 100K
Q4
R150 22K
R13 2.2K
R7 2.7K
EXT/INT SELECT
Q5 AMP
SC PHASE COMPENSATION
Q7 SC PHASE CONTROL
+4.9V(a)
R15 1K
CF1 BPF 4.3MHz
Q7
R14 2.2K
R16 1K
C6 100PF
C7 0.01μF
R17 100
VR3 1K
Q8
R18 10K
Q9
R20 1K
R21 10K
R19 1K
C8 0.01μF
Q8,9 AMP
+1.8V(i)
VR3 SC PHASE FINE
+8.6V(a)
CF2 4.43MHz
TRAP
Q10
Q10 BUFFER
C18 47μF 6.3V
C9 0.01μF
C24 47μF6.3V
R22 2.7K
R24 1K
C10 27PF
Q11
R28 100
R25 1.1K
R23 12K
R26 1K
R27 2.2K
Q12
Q11, Q12 SC PHASE CORRECTION
C12 10μF16V
R29 24K
R30 24K
R31 6.8K
R32 3.3K
R34 910K
R33 3.3K
C11 47μF 6.3V
R35 2K
R36 2K
C13 0.01μF
IC1
GAIN CONT
R38 1.5K
R39 330
R40 330
R41 820
R42 4.3K
C14 0.01μF
C15 0.01μF
R37 2.7K
IC1 SC PHASE CORRECTION
BURST GATE
Q13 AMP
Q14 BUFFER
Q15,16 AMP
Q17 BUFFER
IC2(a) BURST GATE
IC2(b) PULSE GENERATOR
R43 2.7K
C16 0.1μF
Q13
CF3 BPF 4.3MHz
R44 1.6K
R45 220
R46 10K
R47 1K
C17 100PF
Q14
R48 100K
R50 1K
R49 1K
C19 0.1μF
Q15
Q16
R51 10K
R53 1K
R54 10K
R52 3.3K
Q17
R55 1.5K
C21 0.1μF
C20 0.01μF
R56 10K
C22 47μF 6.3V
R57 5.6K
C23 12PF
A1 B1 CLR1 Q Q̄1
IC2
R58 820
R141 47K
C79 150PF
A2 B2 CLR2 Q2 Q̄2
a b c d f g h
EXT OSC
IC5
+4.9V
L4 8.2μH
C45 10μF10V
SCP SW1
SCP SW2
W.HD
N/P
BGP
GL BSC
VSS 2
VDD 2
GL SYNC
FH P2
FH P1
HPCO
XFH OSC IN
XFH OSC OUT
EX 4FSC OUT
4FSC OSC OUT
4FSC OSC IN
SC PCO
SC1
VDD1
VSS1
TEST
H BLK
EXT/INT
LSW OUT
LSW IN
V RESET
XFH
R80 10K
R72 560
C34 47PF
C44 10μF 10V
R79 68K
C38 47PF
L6 GEN-LOCK VCO
Q22 BUFFER
R93 22
Q22
R94 220K
C46 0.039μF
C47 1200PF
R98 3.3K
R95 100
X1 17.7MHz
C48 82PF
C49 82PF
R96 10K
R97 10K
D3
D4
VBS/VS SELECT
IC3
R59 10K
C26 47μF 10V
VR1 50K
R61 10K
C25 0.1μF
+4.9V
H L
TP4 (VBS/VS)
VR1 EXT SC VCO BIAS
C59 0.033μF
Q23 SC1 OUTPUT
R110 1K
C57 0.033μF
R108 22K
Q23
R107 100
C58 180PF
R109 22K
R106 1K
+4.9V
R112 22K
Q24
R111 100
R113 22K
R114 1K
Q24 SC2 OUTPUT
VR2 SC ADJ
R103 100K
R99 20K
VR2 100K
L5 8.2μH
R100 20K
C54 10μF10V
C50 0.01μF
X2 17.7MHz
R101 1K
D5
C53 18PF
C51 330PF
R102 1K
C55 180PF
IC6
+4.9V
L8 8.2μH
C64 10μF 10V
VDD 1
VSS 1
EX 4FSC IN
4FSC OSC IN
4FSC OSC OUT
SC1
SC 2
BSC
VPCC
EXT INT
LSW IN
LSW OUT
V-RESET
EX 282 FH IN
VDD 2
VSS 2
SYNC
VP
W.HD
C.BLK
CP IV
CP 2
W.BLK
BFP
SC.BLK
PCO
282 FH OSC IN
282 FH OSC OUT
INT OSC
R128 1K
C67 47PF
R129 1K
C68 47PF
C75 10PF
R137 10K
C69 47PF
R121 1K
C70 470PF
R136 10K
R135 10K
R132 1K
C71 47PF
R133 1.8K
R134 5.6K
R138 5.6K
C73 470PF
C74 10PF
Q19 BUFFER
+8.6V
R65 1K
R66 100K
Q19
C28 47μF 10V
R67 10K
C30 20PF
R71 1K
+8.6V
R69 1K
R70 39K
R68 5.6K
C29 47μF 10V
Q20
Q20 BUFFER
J125 0Ω
R115 1K
R116 7.5K
C60 2.2μF 25V
C61 1200PF
Q25
C63 330PF
L7 82μH
R119 10K
D7
R117 5.6K
R118 10K
R120 3.3K
C62 330PF
D6
L7 INT SYNC VCO
TP2 (INT SYNC VCO)
R123 1K
R122 560
C66 20PF
C72 47PF
Q25 BUFFER
Q18 EXT/INT SELECT
R62 1K
+4.9V
Q18
R64 22K
R63 10K
C27 47μF 6.3V
R142 6.8K
R143 1K
C81 39PF
L9 8.2μH
+4.9V
C65 47μF 16V
C80 82PF
IC7(b)
PR CLR Vcc
D Q CLK Q̄
IC8
IC7
C77 47μF10V
IC8(a)
IC8(b)
IC7(a)
IC7, IC8 EXT SC DETECT
3
4
5
6
7
8

CONDUCTOR VIEW OF GEN-LOCK BOARD


GEN-LOCK/SYNC BOARD
VR3 SC PHASE FINE
VR4 H. PHASE
VR2 SC ADC
VR1 EXT SC VCO BIAS
TP3 (GEN-LOCK)
SW1
(VBS/VS) TP4
IC8
IC1
IC2
IC3
IC4
IC5
IC6
CF1
CF2
CF3
TP2 (INT SYNC VCO)
TP5 (GND)
CN1
L6 GEN-L
(EXT SC V
SW1 SC PHASE COARSE SWITCH 1
SW2 SC PHASE COARSE SWITCH 2
L7 INT SYNC VCO
D
C
B

COMPONENT SIDE
PATTERN SIDE

TO MOTHER BOARD

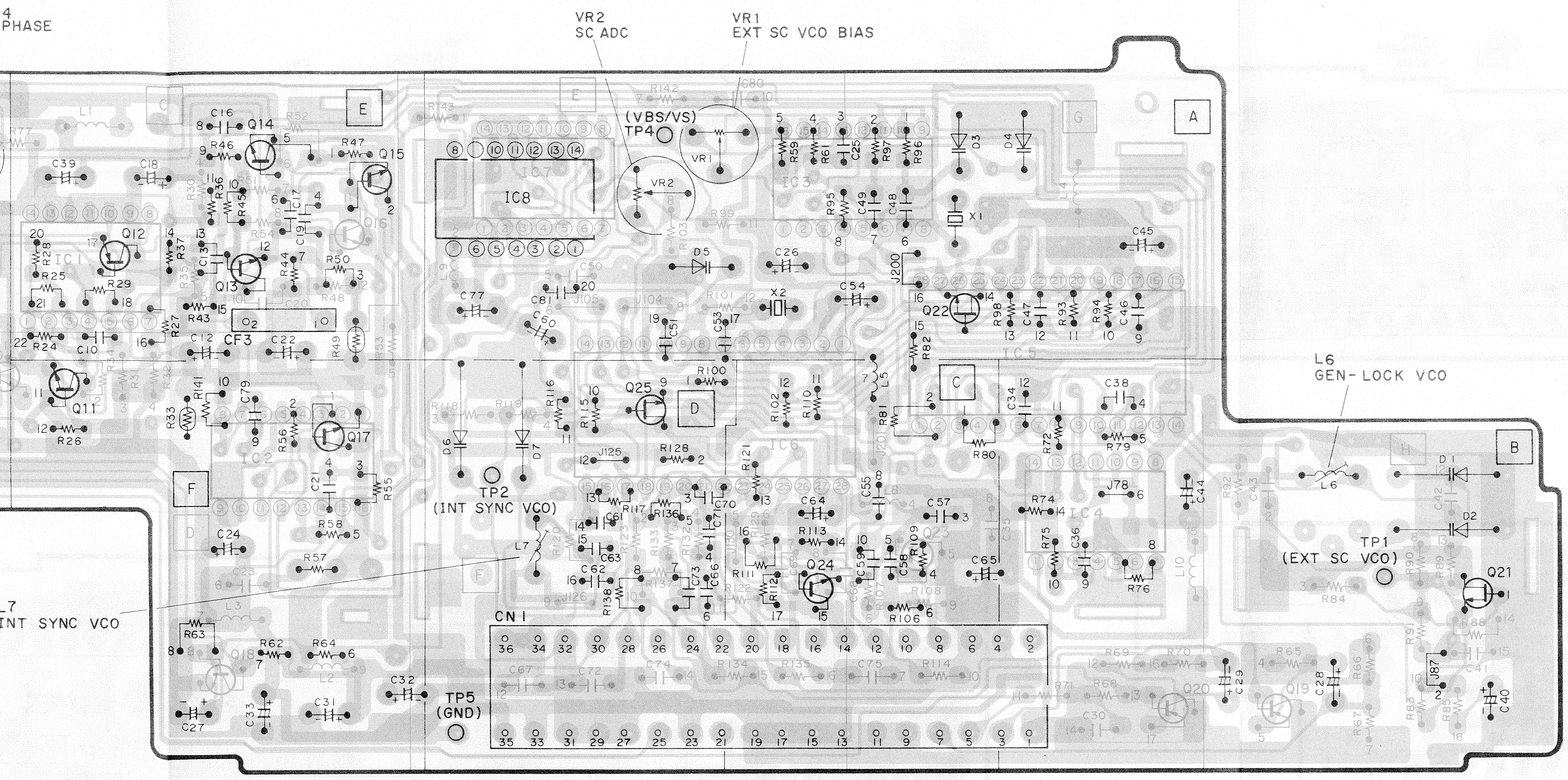
CN1

1	+8.6V IN	19	GROUND
2	NOT USED	20	BL OUT
3	WHD/WBL OUT	21	NOT USED
4	NOT USED	22	CP2/WHD/BL OUT
5	CP3 OUT	23	GROUND
6	NOT USED	24	WHD OUT
7	CP2 OUT	25	GEN-LOCK IN
8	SC OUT	26	NOT USED
9	GROUND	27	GROUND
10	NOT USED	28	BFP OUT
11	GROUND	29	GROUND
12	SC OUT	30	SYNC OUT
13	GROUND	31	GROUND
14	+4.9V IN	32	GROUND
15	GROUND	33	+8.6V IN
16	Vp OUT	34	+4.9V IN
17	GROUND	35	+1.8V IN
18	NOT USED	36	+4.9V IN

A

1 2 3 4 5

CONDUCTOR VIEW OF GEN-LOCK BOARD


4
PHASE
VR2
SC ADC
VR1
EXT SC VCO BIAS
(VBS/VS)
TP4
VR1
VR2
IC8
IC7
IC3
IC5
IC6
IC4
IC2
IC1
A
B
C
D
E
F
Q11
Q12
Q13
Q14
Q15
Q16
Q17
Q18
Q19
Q20
Q21
Q22
Q24
Q25
X1
X2
CF3
D1
D2
D3
D4
D5
D6
D7
TP2
(INT SYNC VCO)
L7
INT SYNC VCO
L6
GEN-LOCK VCO
TP1
(EXT SC VCO)
TP5
(GND)
CN1

COMPONENT SIDE
PATTERN SIDE

CN1

TO
MOTHER
BOARD

1	+8.6V IN	19	GROUND
2	NOT USED	20	BL OUT
3	WHD/WBL OUT	21	NOT USED
4	NOT USED	22	CP2/WHD/BL OUT
5	CP3 OUT	23	GROUND
6	NOT USED	24	WHD OUT
7	CP2 OUT	25	GEN-LOCK IN
8	SC OUT	26	NOT USED
9	GROUND	27	GROUND
10	NOT USED	28	BFP OUT
11	GROUND	29	GROUND
12	SC OUT	30	SYNC OUT
13	GROUND	31	GROUND
14	+4.9V IN	32	GROUND
15	GROUND	33	+8.6V IN
16	Vp OUT	34	+4.9V IN
17	GROUND	35	+1.8V IN
18	NOT USED	36	+4.9V IN

2 3 4 5 6

Gen-Lock/Sync Board

IC1	NJM1496M		C2
IC2	MN74HC221S		C3
IC3	MN4053BS		C4
IC4	MN74HC00S		C5
IC5	MN6761S		C5
IC6	MN67602PS		C4
IC7	MN74HC00S		C3
IC8	MN74HC74S		C3
Q1	2SC2404-CDTW	(U)	C1
Q2	2SD601-QR	(Y)	C1
Q3	2SD601-QR	(Y)	C1
Q2	2SD601-QR	(Y)	C1
Q5	2SC2404-CDTW	(U)	C1
Q6	2SB709-Q	(A)	C1
Q7	2SC2404-CDTW	(U)	C1
Q8	2SC2404-CDTW	(U)	C2
Q9	2SC2404-CDTW	(U)	C1
Q10	2SC2404-CDTW	(U)	C2
Q11	2SC2404-CDTW	(U)	C2
Q12	2SA1022-C	(E)	C3
Q13	2SC2404-CDTW	(U)	C2
Q14	2SC2404-CDTW	(U)	C3
Q15	2SC2404-CDTW	(U)	C3
Q16	2SC2404-CDTW	(U)	C3
Q17	2SC2404-CDTW	(U)	C3
Q18	2SD601-QR	(Y)	B2
Q19	2SB709-Q	(A)	B5
Q20	2SB709-Q	(A)	B5
Q21	2SK198-Q	(1Q)	B6
Q22	2SK198-Q	(1Q)	C4
Q23	2SC2404-CDTW	(U)	B4
Q24	2SC2404-CDTW	(U)	B4
Q25	2SK198-PQRTX	(1Q)	C4
D1	1SV153		B6
D2	1SV153		B6
D3	1SV153		C4
D4	1SV153		C5
D5	1SV153		C4
D6	1SV153		C3
D7	1SV153		C3

Process Board

IC1	MN3106S		A4
IC2	HB12		B7
IC3	HB18		B7
IC4	NJU4053BM		B5
IC5	AN2361S		B5
IC6	HB16		B4
IC7	AN2210SA		A3
Q1	2SD601-QR	(Y)	B6
Q2	2SD601-QR	(Y)	B6
Q3	2SD601-QR	(Y)	A6
Q4	2SC2404-CDTW	(U)	A6
Q5	2SC2404-CDTW	(U)	A6
Q6	2SD601-QR	(Y)	A5
Q7	2SB709-Q	(A)	A5
Q8	2SD601-QR	(Y)	A7
Q9	2SD601-QR	(Y)	B6
Q10	2SA1022-BCTX	(E)	B4
Q11	2SA1022-BCTX	(E)	B5
Q12	2SC2404-CDTX	(U)	B5
Q13	2SD601-QR	(Y)	B5
Q14	2SD601-QR	(Y)	A5
Q15	2SB709-Q	(A)	A3
Q16	2SK198-Q	(1Q)	B3
D1	MA151KTX	(MH)	A3

Process VIC-1 Board

IC1	MN3802A		A1
Q1	2SB709-Q	(A)	A1
Q2	2SD601-QR	(W)	A1
Q3	2SC2404-CDTW	(U)	A2
Q4	2SC2404-CDTW	(U)	A2
Q5	2SD601-QR	(W)	A2

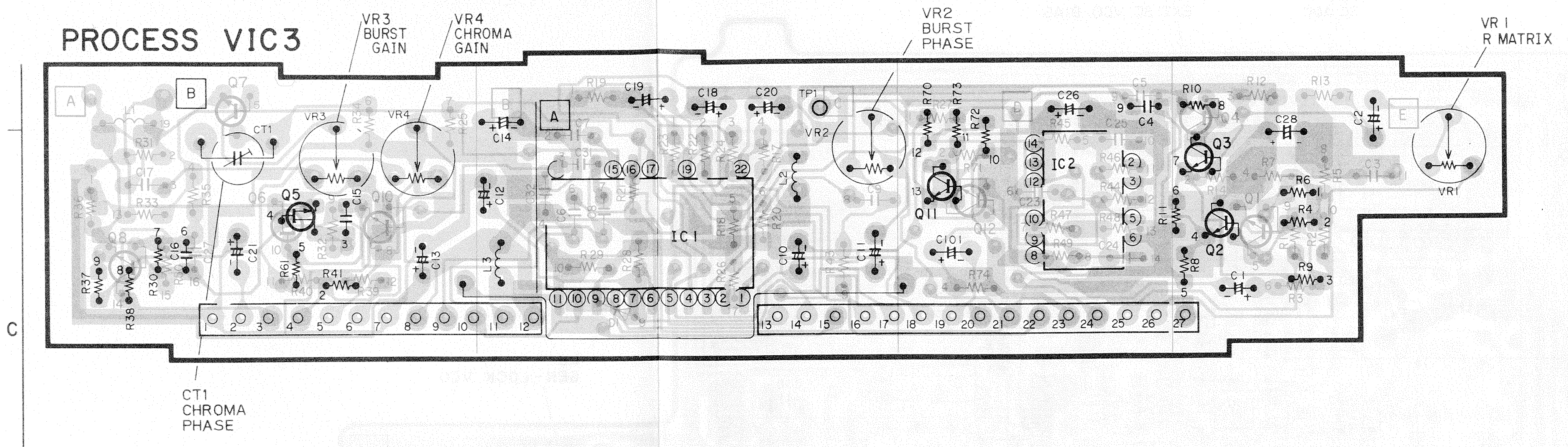
Process VIC-2 Board

IC1	AN608		B2
IC2	AN2020S		B1
Q1	2SC2404-CDTW	(U)	B2
Q2	2SD601-QR	(Y)	B2
D1	1SS108		B2
D2	1SS108		B2

Process VIC-3 Board

IC1	AN2410S		C2
IC2	NJM319M		C3
Q1	2SD601-QR	(Y)	C4
Q2	2SD601-QR	(Y)	C4
Q3	2SD601-QR	(Y)	C3
Q4	2SD601-QR	(Y)	C3
Q5	2SK198-Q	(1Q)	C1
Q6	2SK198-Q	(1Q)	C1
Q7	2SC2404-CDTW	(U)	C1
Q8	2SC2404-CDTW	(U)	C1
Q10	2SC2404-CDTW	(U)	C1
Q11	2SC2404-CDTW	(U)	C3
Q12	2SD601-QR	(Y)	C3
D1	MA151KTX	(MH)	C2

CONDUCTOR VIEW OF PROCESS BOARD

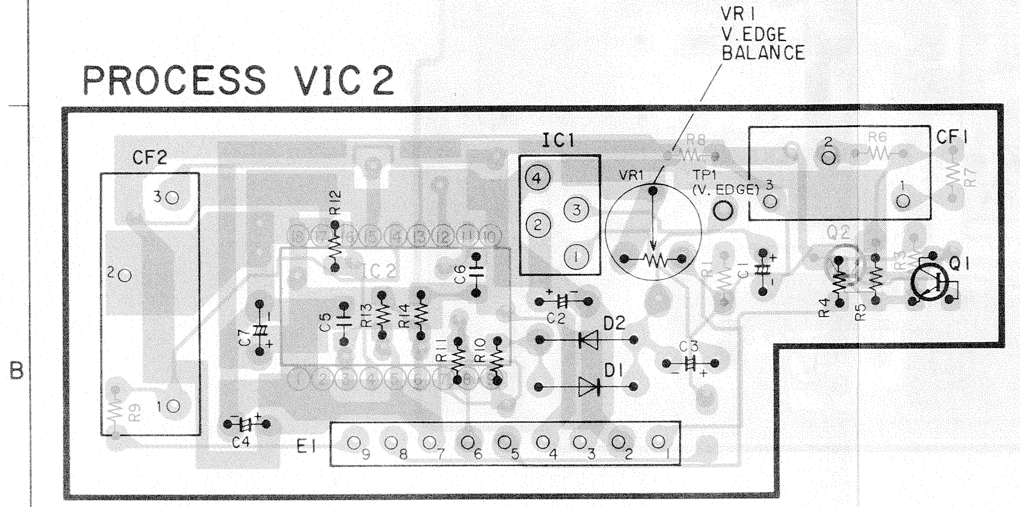

PROCESS VIC3

VR3 BURST GAIN
VR4 CHROMA GAIN
VR2 BURST PHASE
VR I R MATRIX
CT1 CHROMA PHASE
A
B
E
IC1
IC2
Q1
Q2
Q3
Q5
Q7
Q11
TP1
C

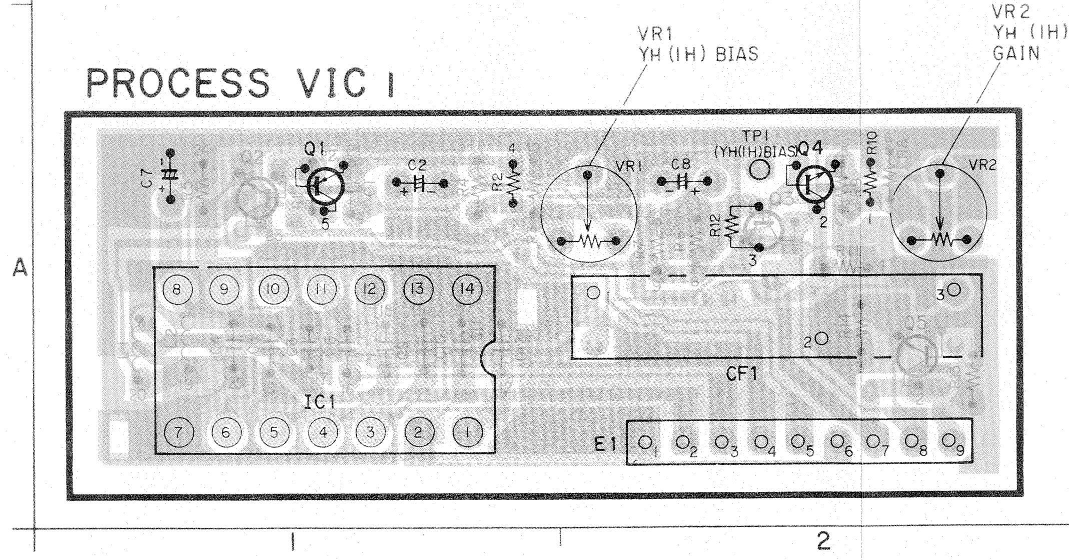
CN I

TO MOTHER BOARD

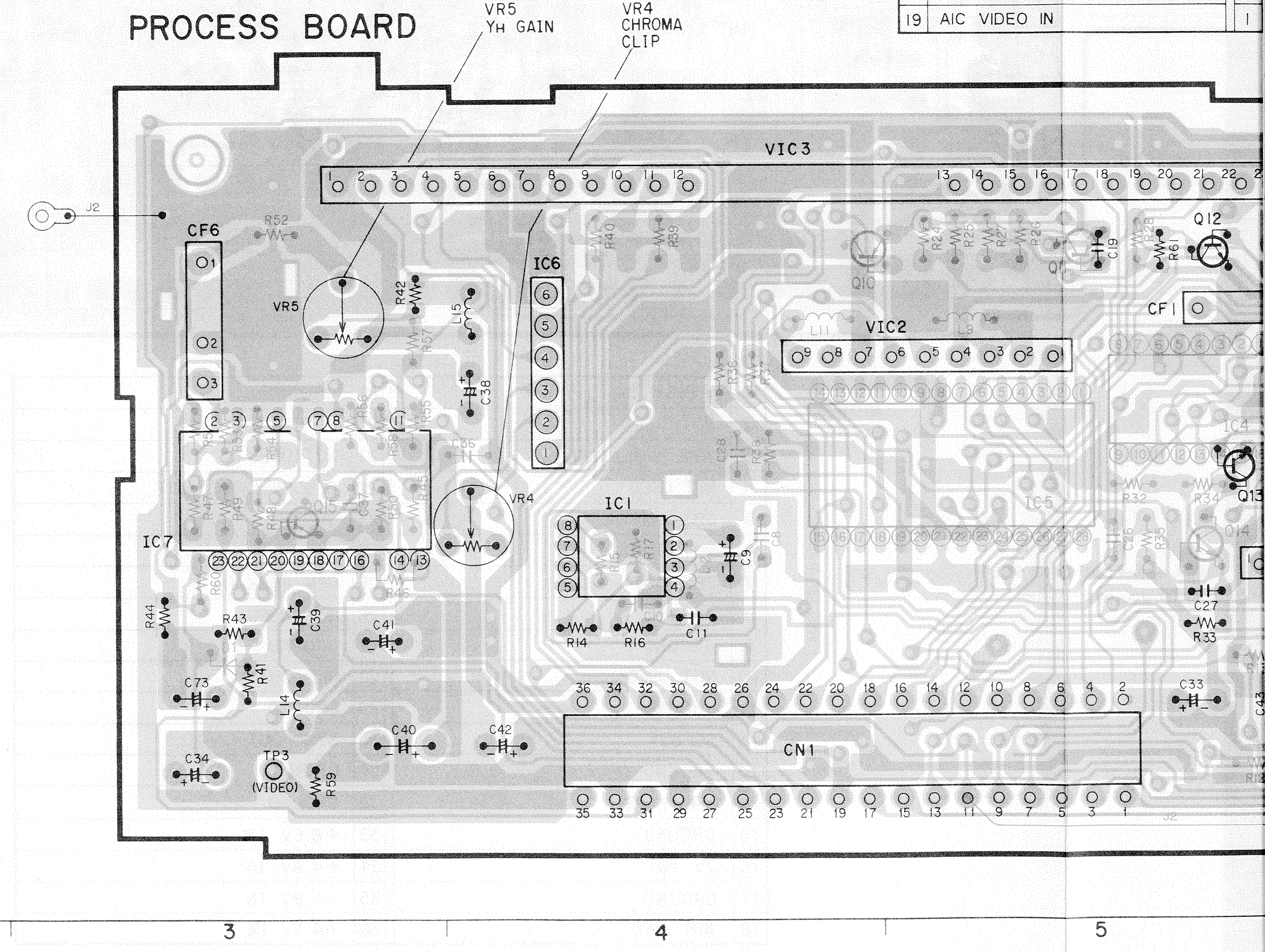
36	GROUND	18
35	VIDEO OUT	17
34	+1.8V IN	16
33	+4.9V IN	15
32	SC2 IN	14
31	GAIN CONTROL IN	13
30	WHD IN	12
29	GROUND	11
28	SYNC IN	10
27	PEDESTAL CONTOROL IN	9
26	NOT USED	8
25	CP2/WHD/BL IN	7
24	CP2 IN	6
23	CHARACTER IN	5
22	REF OUT	4
21	GAIN-UP CONTROL IN	3
20	ATW B CONTROL IN	2
19	AIC VIDEO IN	1


PROCESS VIC 2

VR I V.EDGE BALANCE
CF2
CF1
IC1
IC2
VR1
TP1 (V. EDGE)
Q1
Q2
D1
D2
E1
B

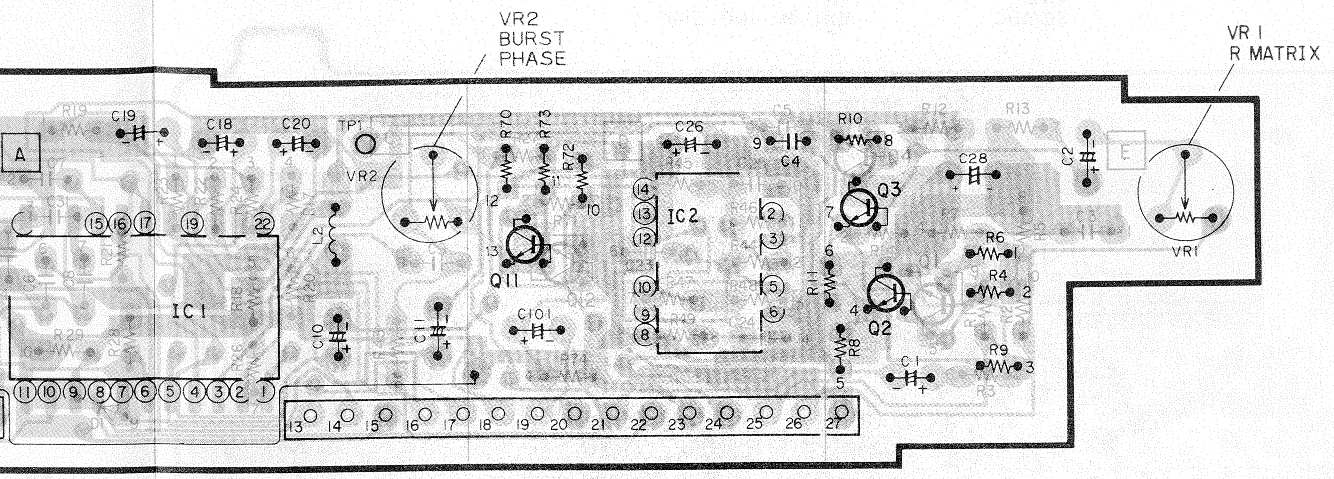

PROCESS VIC I

VR1 YH (IH) BIAS
VR2 YH (IH) GAIN
Q1
Q4
TP1 (YH(IH)BIAS)
IC1
CF1
E1
A
1
2


PROCESS BOARD

VR5 YH GAIN
VR4 CHROMA CLIP
VIC3
VIC2
CF6
CF1
IC1
IC6
IC7
J2
Q10
Q12
Q13
TP3 (VIDEO)
CN1
3
4
5

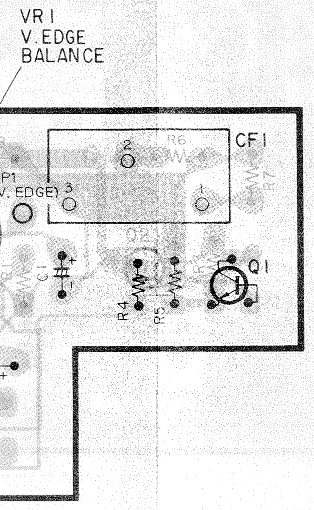
CONDUCTOR VIEW OF PROCESS BOARD

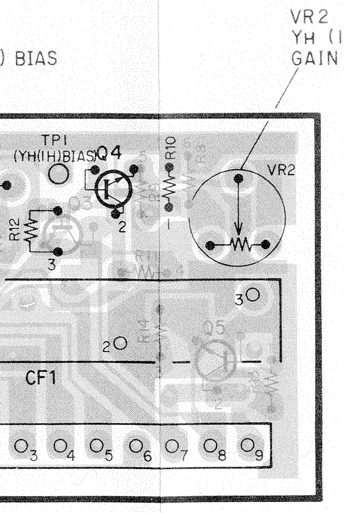

VR2
BURST
PHASE
VR1
R MATRIX
IC1
IC2
Q11
Q2
Q3
TP1
VR2
VR1
A
E

TO
MOTHER
BOARD

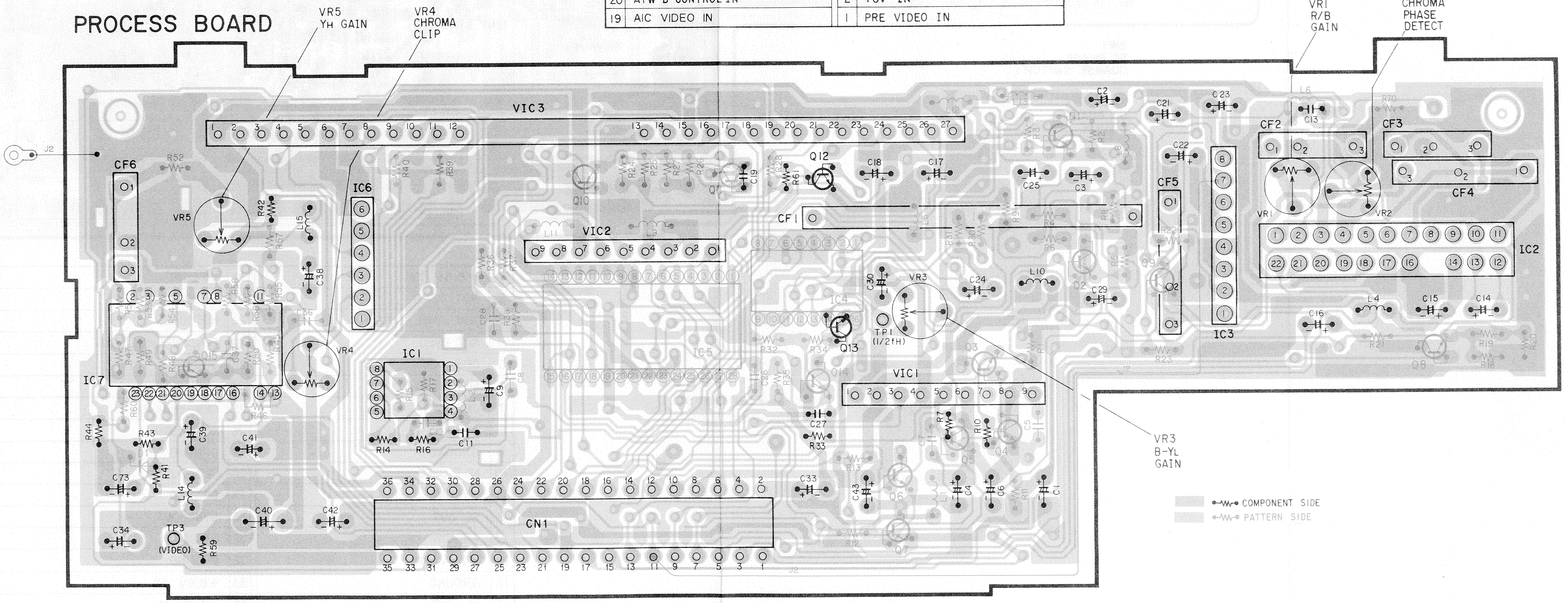
CN1

36	GROUND	18	B CONTROL IN
35	VIDEO OUT	17	R SC IN
34	+1.8V IN	16	R GAIN CONTROL IN
33	+4.9V IN	15	YH IN
32	SC2 IN	14	B DETECT OUT
31	GAIN CONTROL IN	13	R CONTROL IN
30	WHD IN	12	R DETECT OUT
29	GROUND	11	SC1 IN
28	SYNC IN	10	+8.6V IN
27	PEDESTAL CONTOROL IN	9	APERTURE SIGNAL OUT
26	NOT USED	8	ATW GATE PULSE IN
25	CP2/WHD/BL IN	7	1/2 fH PULSE IN
24	CP2 IN	6	ATW R CONTROL IN
23	CHARACTER IN	5	CP5 IN
22	REF OUT	4	BFP IN
21	GAIN-UP CONTROL IN	3	AIC CONTROL OUT
20	ATW B CONTROL IN	2	+6V IN
19	AIC VIDEO IN	1	PRE VIDEO IN


VR1
V.EDGE
BALANCE
CF1
TP1
(V.EDGE)
Q1
Q2

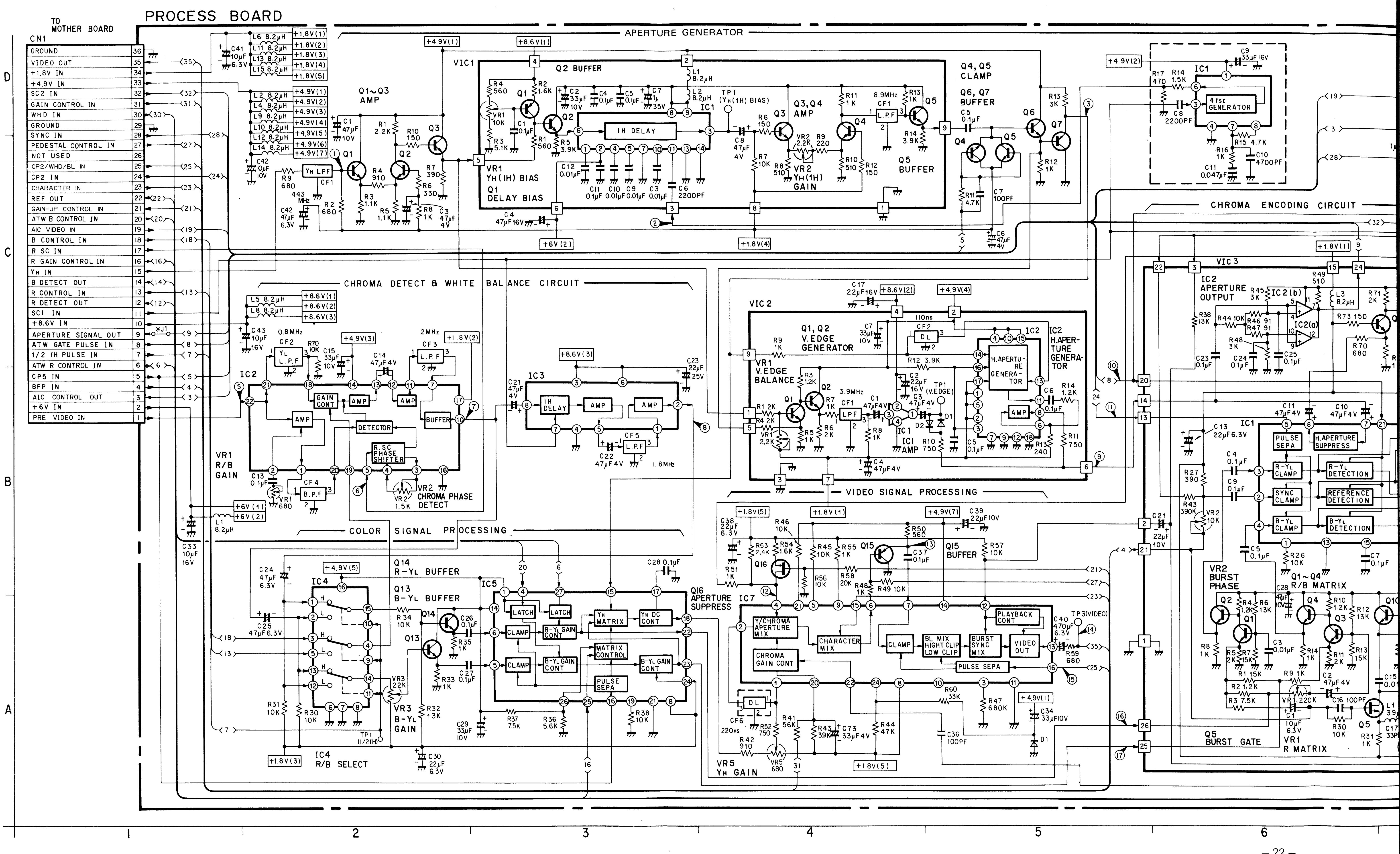

) BIAS
VR2
YH (IH)
GAIN
TP1
(YH(IH)BIAS)
Q4
Q5
VR2
CF1

PROCESS BOARD

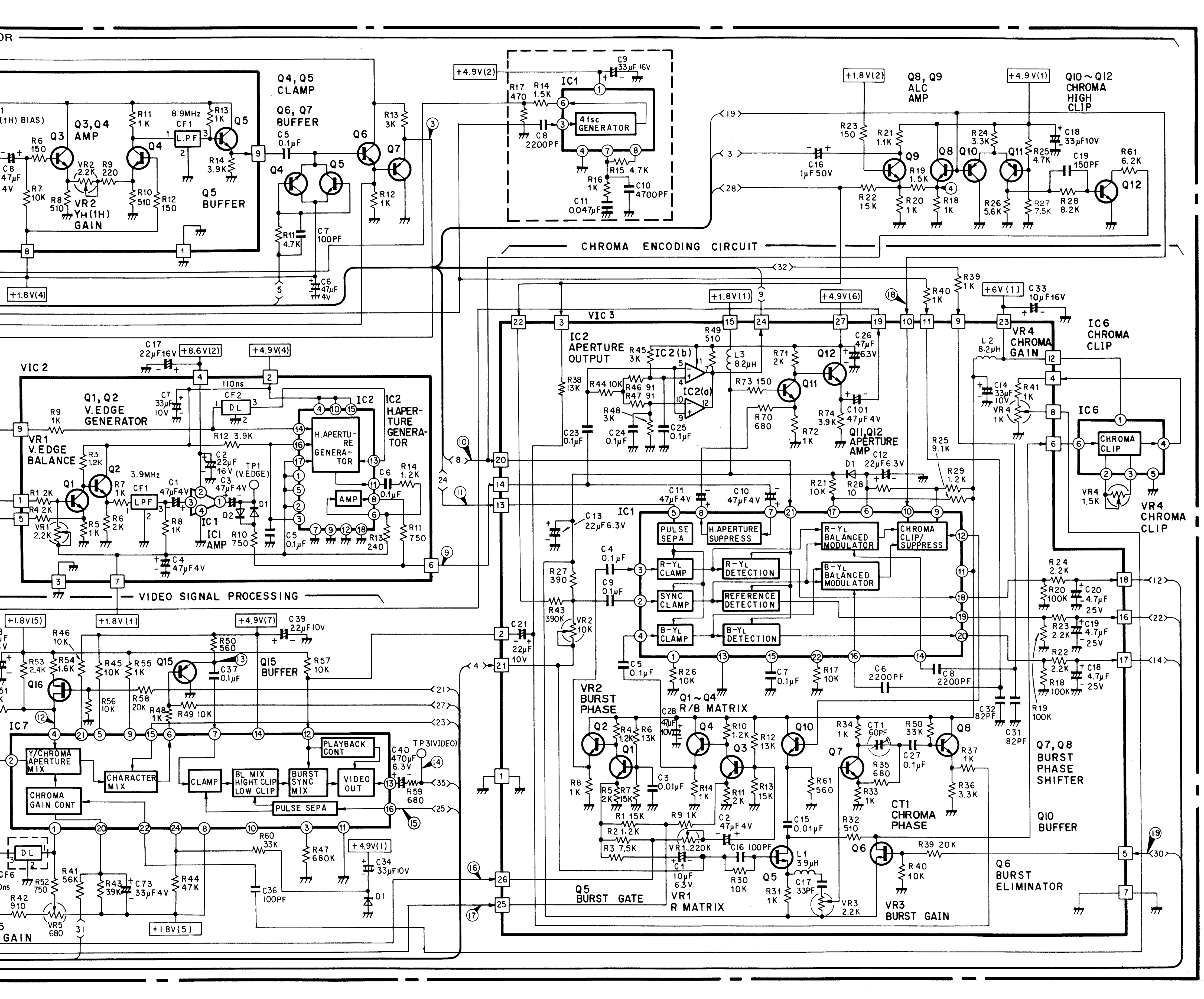

VR5
YH GAIN
VR4
CHROMA
CLIP
VR1
R/B
GAIN
VR2
CHROMA
PHASE
DETECT
VIC3
VIC2
VIC1
IC1
IC2
IC3
IC4
IC5
IC6
IC7
CF1
CF2
CF3
CF4
CF5
CF6
CN1
J2
Q12
Q13
TP1
(1/2fH)
TP3
(VIDEO)
VR3
B-YL
GAIN
COMPONENT SIDE
PATTERN SIDE

2 3 4 5 6 7

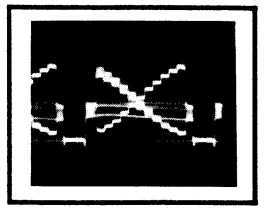
SCHEMATIC DIAGRAM OF PROC

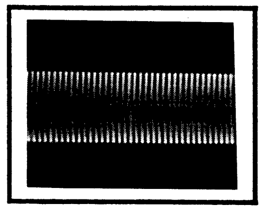

PROCESS BOARD
TO MOTHER BOARD
CN1
GROUND 36
VIDEO OUT 35
+1.8V IN 34
+4.9V IN 33
SC2 IN 32
GAIN CONTROL IN 31
WHD IN 30
GROUND 29
SYNC IN 28
PEDESTAL CONTROL IN 27
NOT USED 26
CP2/WHD/BL IN 25
CP2 IN 24
CHARACTER IN 23
REF OUT 22
GAIN-UP CONTROL IN 21
ATW B CONTROL IN 20
AIC VIDEO IN 19
B CONTROL IN 18
R SC IN 17
R GAIN CONTROL IN 16
YH IN 15
B DETECT OUT 14
R CONTROL IN 13
R DETECT OUT 12
SC1 IN 11
+8.6V IN 10
APERTURE SIGNAL OUT 9
ATW GATE PULSE IN 8
1/2 fH PULSE IN 7
ATW R CONTROL IN 6
CP5 IN 5
BFP IN 4
AIC CONTROL OUT 3
+6V IN 2
PRE VIDEO IN 1
APERTURE GENERATOR
VIC1
Q2 BUFFER
Q1~Q3 AMP
1H DELAY
Q3,Q4 AMP
Q4, Q5 CLAMP
Q6, Q7 BUFFER
Q5 BUFFER
VR1 YH (1H) BIAS
Q1 DELAY BIAS
VR2 YH (1H) GAIN
TP1 (YH (1H) BIAS)
YH LPF
CF1 443 MHz
8.9MHz CF1 L.P.F
IC1 4fsc GENERATOR
CHROMA DETECT & WHITE BALANCE CIRCUIT
IC2
GAIN CONT
AMP
DETECTOR
BUFFER
R.SC PHASE SHIFTER
VR1 R/B GAIN
VR2 CHROMA PHASE DETECT
CF2 YL L.P.F
CF3 L.P.F
CF4 B.P.F
0.8MHz
2MHz
IC3
1H DELAY
AMP
CF5 L.P.F
1.8MHz
VIC2
Q1, Q2 V.EDGE GENERATOR
VR1 V.EDGE BALANCE
IC2 H.APERTURE GENERATOR
H.APERTURE GENERATOR
IC1 AMP
CF2 DL 110ns
CF1 LPF 3.9MHz
TP1 (V.EDGE)
VIDEO SIGNAL PROCESSING
COLOR SIGNAL PROCESSING
IC4 R/B SELECT
Q14 R-YL BUFFER
Q13 B-YL BUFFER
VR3 B-YL GAIN
TP1 (1/2fH)
IC5
LATCH
CLAMP
R-YL GAIN CONT
B-YL GAIN CONT
YH MATRIX
YH DC CONT
MATRIX CONTROL
PULSE SEPA
Q16 APERTURE SUPPRESS
IC7
Y/CHROMA APERTURE MIX
CHROMA GAIN CONT
CHARACTER MIX
CLAMP
BL MIX HIGHT CLIP LOW CLIP
BURST SYNC MIX
VIDEO OUT
PLAYBACK CONT
PULSE SEPA
Q15 BUFFER
VR5 YH GAIN
TP3 (VIDEO)
CF6 DL 220ns
CHROMA ENCODING CIRCUIT
VIC3
IC2 APERTURE OUTPUT
IC1
PULSE SEPA
H.APERTURE SUPPRESS
R-YL CLAMP
R-YL DETECTION
SYNC CLAMP
REFERENCE DETECTION
B-YL CLAMP
B-YL DETECTION
VR2 BURST PHASE
Q1~Q4 R/B MATRIX
Q5 BURST GATE
VR1 R MATRIX
D
C
B
A
1
2
3
4
5
6

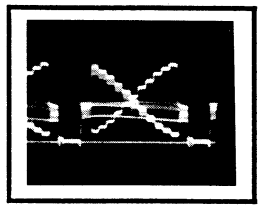
SCHEMATIC DIAGRAM OF PROCESS BOARD

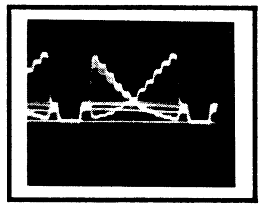


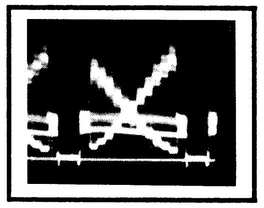
Q4, Q5 CLAMP
Q6, Q7 BUFFER
Q3, Q4 AMP
Q5 BUFFER
VR2 YH (1H) GAIN
IC1 4fsc GENERATOR
Q8, Q9 ALC AMP
Q10 ~ Q12 CHROMA HIGH CLIP
CHROMA ENCODING CIRCUIT
VIC 2
Q1, Q2 V.EDGE GENERATOR
VR1 V.EDGE BALANCE
IC2 H.APERTURE GENERATOR
IC1 AMP
VIDEO SIGNAL PROCESSING
VIC 3
IC2 APERTURE OUTPUT
Q11, Q12 APERTURE AMP
VR4 CHROMA GAIN
IC6 CHROMA CLIP
VR4 CHROMA CLIP
IC1
PULSE SEPA
H.APERTURE SUPPRESS
R-YL BALANCED MODULATOR
CHROMA CLIP/SUPPRESS
B-YL BALANCED MODULATOR
R-YL CLAMP
R-YL DETECTION
SYNC CLAMP
REFERENCE DETECTION
B-YL CLAMP
B-YL DETECTION
VR2 BURST PHASE
Q1 ~ Q4 R/B MATRIX
Q7, Q8 BURST PHASE SHIFTER
Q10 BUFFER
CT1 CHROMA PHASE
Q6 BURST ELIMINATOR
Q5 BURST GATE
VR1 R MATRIX
VR3 BURST GAIN
Q15 BUFFER
Q16
IC7
Y/CHROMA APERTURE MIX
CHARACTER MIX
CLAMP
BL MIX HIGHT CLIP LOW CLIP
BURST SYNC MIX
VIDEO OUT
PLAYBACK CONT
PULSE SEPA
CHROMA GAIN CONT
TP3 (VIDEO)
4
5
6
7
8

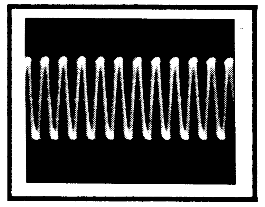

1. 50mV/DIV, 10μs/DIV

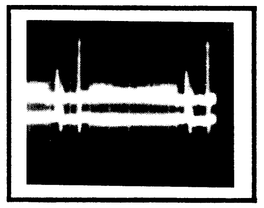

2. 0.2V/DIV, 0.2μs/DIV

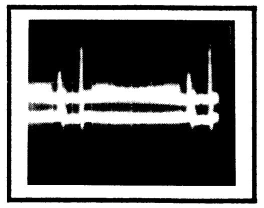

3. 0.1V/DIV, 10μs/DIV

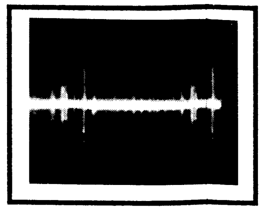

4. 0.2V/DIV 10μs/DIV

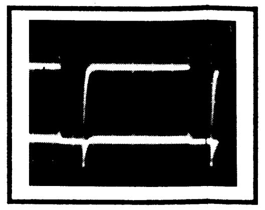

5. 0.1V/DIV, 10μs/DIV

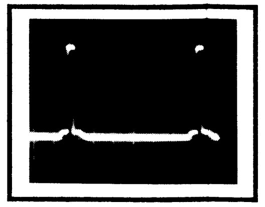

6. 0.2V/DIV, 0.2μs/DIV

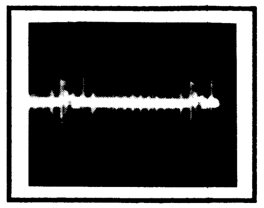

7. 50mV/DIV, 10μs/DIV

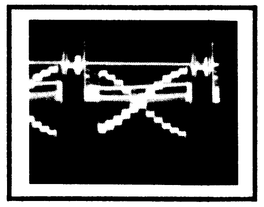

8. 50mV/DIV, 10μs/DIV

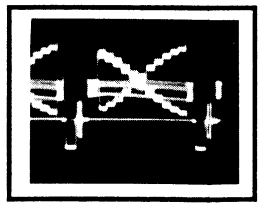

9. 0.1V/DIV, 10μs/DIV

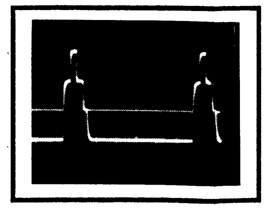

10. 0.2V/DIV, 10μs/lDIV

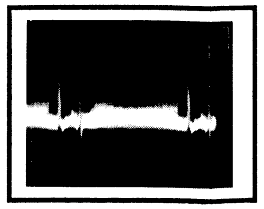

11. 1V/DIV, 10μs/DIV

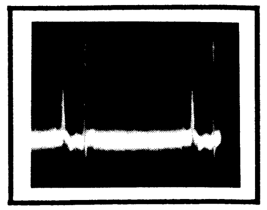

12. 50mV/DIV, 10μs/DIV

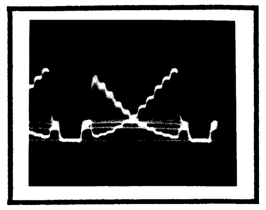

13. 0.2V/DIV, 10μs/DIV

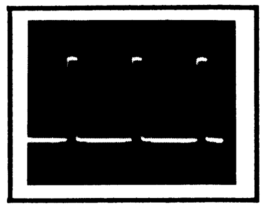

14. 0.2V/DIV, 10μs/DIV


15. 1V/DIV, 10μs/DIV


16. 20mV/DIV, 10μs/DIV


17. 20mV/DIV, 10μs/DIV


18. 0.2V/DIV, 10μs/DIV


19. 1V/DIV, 20μs/DIV

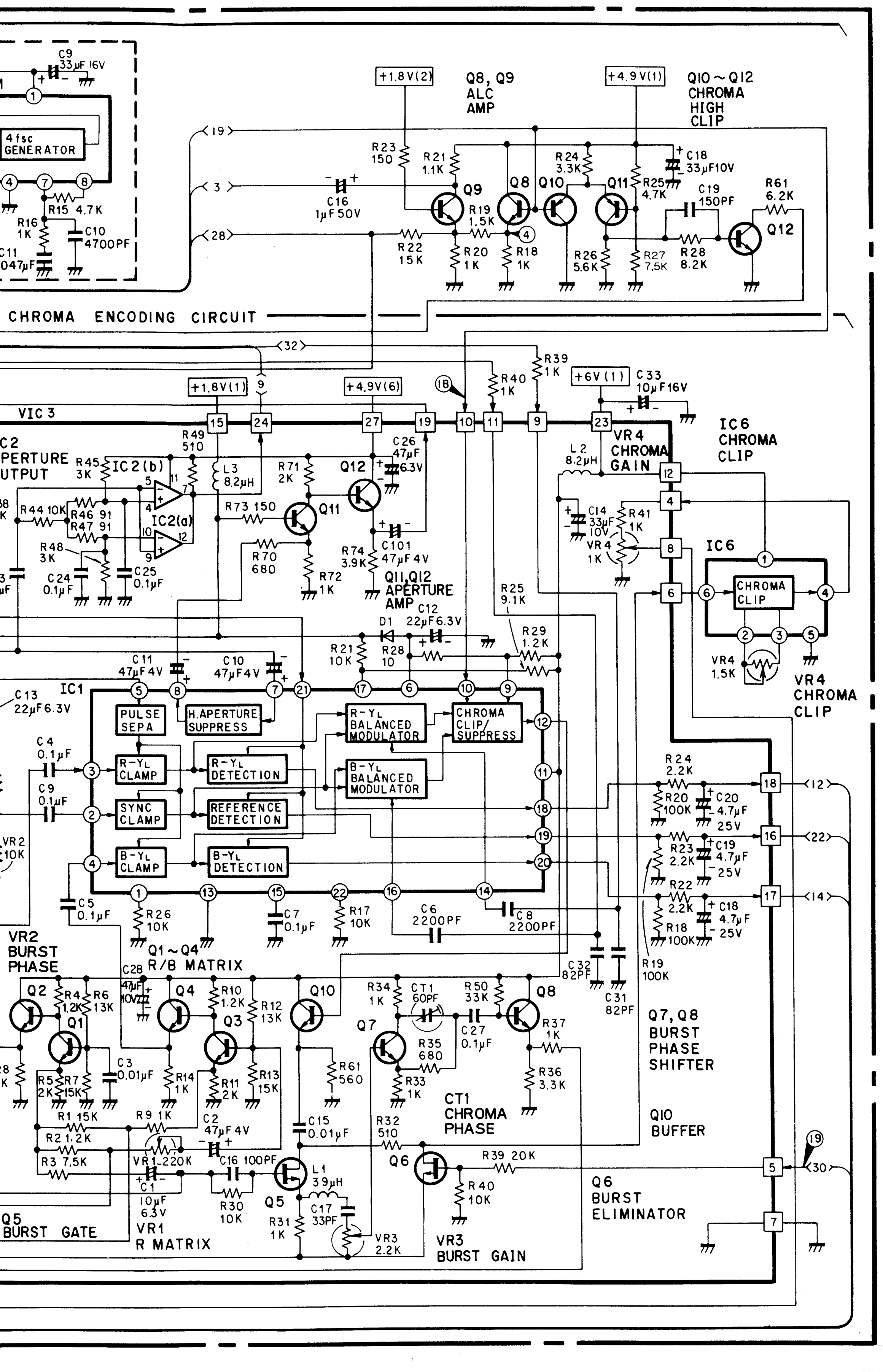
B
C
E

PROCESS VIC-3 BOARD

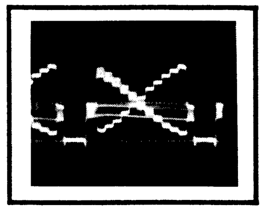
	IC1	IC2
Pin 1	----	0
2	1.6	0
3	3.1	0
4	3.1	2.4
5	0.7	2.3
6	2.4	0-
7	2.0	4.6
8	3.6	0
9	2.4	2.3
10	2.0	2.4
11	5.7	4.7
12	2.6	4.6
13	0	0
14	4.2	0
15	2.4	
16	4.2	
17	3.8	
18	2.3	
19	2.3	
20	2.3	
21	2.1	
22	0	

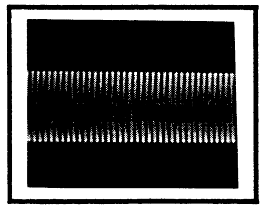
Q1
Q2
Q3
Q4
Q5
Q6
Q7
Q8
Q10
Q11
Q12

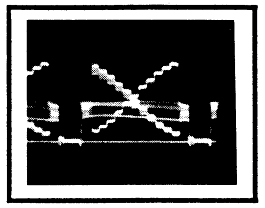
DIAGRAM OF PROCESS BOARD

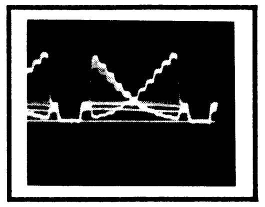


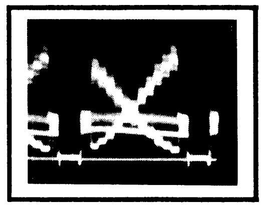
4fsc GENERATOR
CHROMA ENCODING CIRCUIT
Q8, Q9 ALC AMP
Q10 ~ Q12 CHROMA HIGH CLIP
+1.8V(2)
+4.9V(1)
VIC 3
+1.8V(1)
+4.9V(6)
+6V(1)
IC2 APERTURE OUTPUT
IC2(b)
IC2(a)
Q11,Q12 APERTURE AMP
VR4 CHROMA GAIN
IC6 CHROMA CLIP
IC6
CHROMA CLIP
VR4 CHROMA CLIP
IC1
PULSE SEPA
H.APERTURE SUPPRESS
R-YL BALANCED MODULATOR
CHROMA CLIP/ SUPPRESS
R-YL CLAMP
R-YL DETECTION
B-YL BALANCED MODULATOR
SYNC CLAMP
REFERENCE DETECTION
B-YL CLAMP
B-YL DETECTION
VR2 BURST PHASE
Q1 ~ Q4 R/B MATRIX
Q7, Q8 BURST PHASE SHIFTER
CT1 CHROMA PHASE
Q10 BUFFER
Q6 BURST ELIMINATOR
Q5 BURST GATE
VR1 R MATRIX
VR3 BURST GAIN
6
7
8

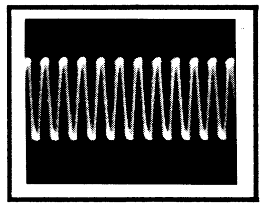

1. 50mV/DIV, 10μs/DIV

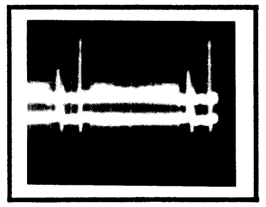

2. 0.2V/DIV, 0.2μs/DIV

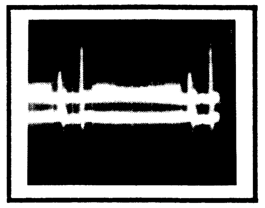

3. 0.1V/DIV, 10μs/DIV

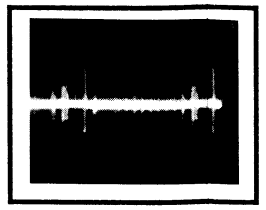

4. 0.2V/DIV 10μs/DIV

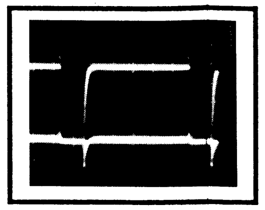

5. 0.1V/DIV, 10μs/DIV

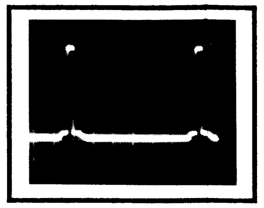

6. 0.2V/DIV, 0.2μs/DIV

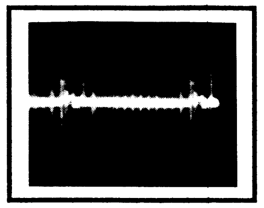

7. 50mV/DIV, 10μs/DIV

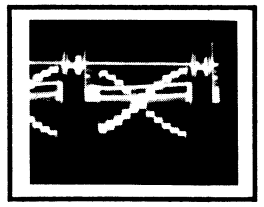

8. 50mV/DIV, 10μs/DIV

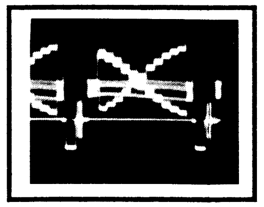

9. 0.1V/DIV, 10μs/DIV

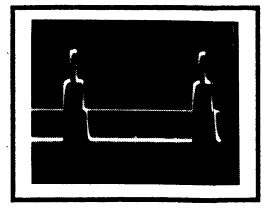

10. 0.2V/DIV, 10μs/IDIV

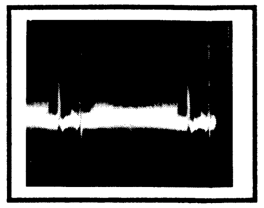

11. 1V/DIV, 10μs/DIV

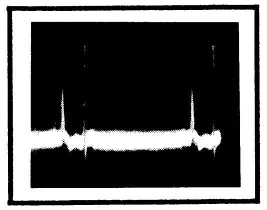

12. 50mV/DIV, 10μs/DIV

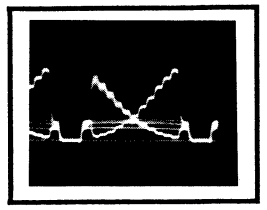

13. 0.2V/DIV, 10μs/DIV

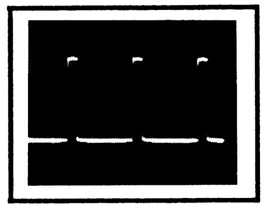

14. 0.2V/DIV, 10μs/DIV


15. 1V/DIV, 10μs/DIV


16. 20mV/DIV, 10μs/DIV


17. 20mV/DIV, 10μs/DIV


18. 0.2V/DIV, 10μs/DIV


19. 1V/DIV, 20μs/DIV

PROCESS BOARD

	IC1	IC2	IC3	IC4	IC5	IC6	IC7
Pin 1	4.7	0	1.3	1.7	4.6	5.7	1.8
2	2.3	1.1	0	1.7	1.8	1.2	1.8
3	0	1.0	8.2	1.7	2.5	3.5	1.8
4	0	0	0	1.7	1.8	2.2	0
5	7.9	1.5	3.7	1.7	1.7	0	0
6	15.8	0	8.2	0	1.7	1.8	2.6
7	1.5	3.5	1.5	0	3.1		1.5
8	1.5	0	2.5	0	1.7		0
9		2.9		2.3	3.1		1.7
10		2.3		2.3	2.5		2.3
11		3.5		2.3	0		0
12		1.1		1.7	0		1.7
13		1.4		1.7	0.7		2.4
14		4.6		1.6	4.6		4.6
15		---		1.7	1.9		0
16		0		4.9	0.1		0.7
17		1.7			2.9		0
18		0			2.0		0
19		1.7			0		0
20		1.7			2.4		1.7
21		1.8			0		4.6
22		2.0			2.4		1.7
23					2.3		2.7
24					1.7		1.7
25					1.7		
26					2.0		
27					1.7		
28					2.4		

	Q1	Q2	Q3	Q4	Q5	Q6	Q7	Q8	Q9	Q10	Q11	Q12	Q13	Q14	Q15	Q16
B	1.8	1.7	2.7	0	0.1	1.8	1.3	1.9	1.7	1.9	2.9	0	1.7	1.7	2.6	G 1.5
C	4.7	2.7	4.6	1.7	1.9	4.6	0	4.6	3.6	0	0	2.1	4.6	4.2	0	D 1.7
E	1.2	1.1	2.0	1.9	1.7	1.3	1.9	1.3	1.1	2.6	2.6	0	1.1	1.1	3.2	S 1.7

PROCESS VIC-1 BOARD

	IC1
Pin 1	---
2	6.0
3	1.2
4	---
5	---
6	2.2
7	0
8	4.9
9	4.9
10	3.5
11	1.4
12	0
13	---
14	0
15	
16	
17	

	Q1	Q2	Q3
B	5.4	2.8	1.6
C	2.7	4.6	4.6
E	6.0	2.2	0.9
	Q4	Q5	
B	1.7	3.5	
C	3.5	4.6	
E	1.0	2.9	

PROCESS VIC-2 BOARD

	IC1	IC2
Pin 1	4.8	1.8
2	8.6	1.8
3	2.1	2.5
4	0	4.9
5		1.8
6		1.8
7		0
8		2.0
9		0
10		4.9
11		2.0
12		0
13		1.9
14		1.9
15		4.9
16		2.5
17		1.8
18		0

	Q1	Q2
B	2.0	3.3
C	1.1	4.7
E	3.3	2.7

PROCESS VIC-3 BOARD

	IC1	IC2
Pin 1	----	0
2	1.6	0
3	3.1	0
4	3.1	2.4
5	0.7	2.3
6	2.4	0-
7	2.0	4.6
8	3.6	0
9	2.4	2.3
10	2.0	2.4
11	5.7	4.7
12	2.6	4.6
13	0	0
14	4.2	0
15	2.4	
16	4.2	
17	3.8	
18	2.3	
19	2.3	
20	2.3	
21	2.1	
22	0	

	B	C	E
Q1	3.0	4.3	2.4
Q2	4.3	5.7	3.6
Q3	3.0	4.2	2.4
Q4	4.2	5.7	3.6
Q5	G 1.7	D 1.8	S 1.7
Q6	G 0.2	D 1.8	S 0
Q7	1.7	4.7	1.0
Q8	5.4	5.7	4.7
Q10	2.6	5.7	1.9
Q11	1.7	2.6	1.0
Q12	2.6	4.7	1.9

Process Board

IC1	MN3106S		D6
IC2	HB12		B2
IC3	HB18		B3
IC4	NJU4053BM		A2
IC5	AN2361S		A3
IC6	HB16		B8
IC7	AN2210SA		A4
Q1	2SD601-QR	(Y)	C2
Q2	2SD601-QR	(Y)	C2
Q3	2SD601-QR	(Y)	C2
Q4	2SC2404-CDTW	(U)	C5
Q5	2SC2404-CDTW	(U)	C5
Q6	2SD601-QR	(Y)	D5
Q7	2SB709-Q	(A)	C5
Q8	2SD601-QR	(Y)	C7
Q9	2SD601-QR	(Y)	C7
Q10	2SA1022-BCTX	(E)	C7
Q11	2SA1022-BCTX	(E)	C7
Q12	2SC2404-CDTX	(U)	C8
Q13	2SD601-QR	(Y)	A2
Q14	2SD601-QR	(Y)	A2
Q15	2SB709-Q	(A)	B4
Q16	2SK198-Q	(1Q)	B4
D1	MA151KTX	(MH)	A5

Process VIC-1 Board

IC1	MN3802A		D3
Q1	2SB709-Q	(A)	D3
Q2	2SD601-QR	(W)	D3
Q3	2SC2404-CDTW	(U)	D4
Q4	2SC2404-CDTW	(U)	D4
Q5	2SD601-QR	(W)	D4

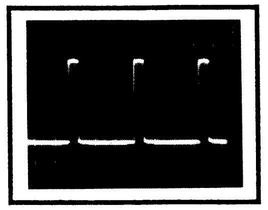
Process VIC-2 Board

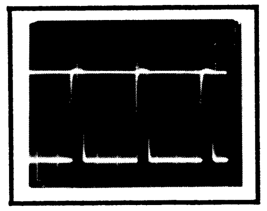
IC1	AN608		B4
IC2	AN2020S		B5
Q1	2SC2404-CDTW	(U)	B4
Q2	2SD601-QR	(Y)	B4
D1	1SS108		B5
D2	1SS108		B5

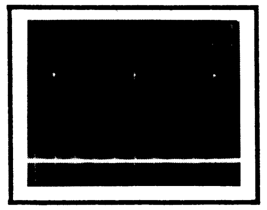
Process VIC-3 Board

IC1	AN2410S		B7
IC2	NJM319M		C6
Q1	2SD601-QR	(Y)	A6
Q2	2SD601-QR	(Y)	A6
Q3	2SD601-QR	(Y)	A6
Q4	2SD601-QR	(Y)	A6
Q5	2SK198-Q	(1Q)	A6
Q6	2SK198-Q	(1Q)	A7
Q7	2SC2404-CDTW	(U)	A7
Q8	2SC2404-CDTW	(U)	A7
Q10	2SC2404-CDTW	(U)	A7
Q11	2SC2404-CDTW	(U)	C7
Q12	2SD601-QR	(Y)	C7
D1	MA151KTX	(MH)	B7

	CCD image sensor without IC5 on Process board	CCD image sensor with IC5 on Process board
J1	Not Used	Used


1. 1V/DIV, 20µs/DIV


2. 1V/DIV, 20µs/DIV


3. 1V/DIV, 5ms/DIV

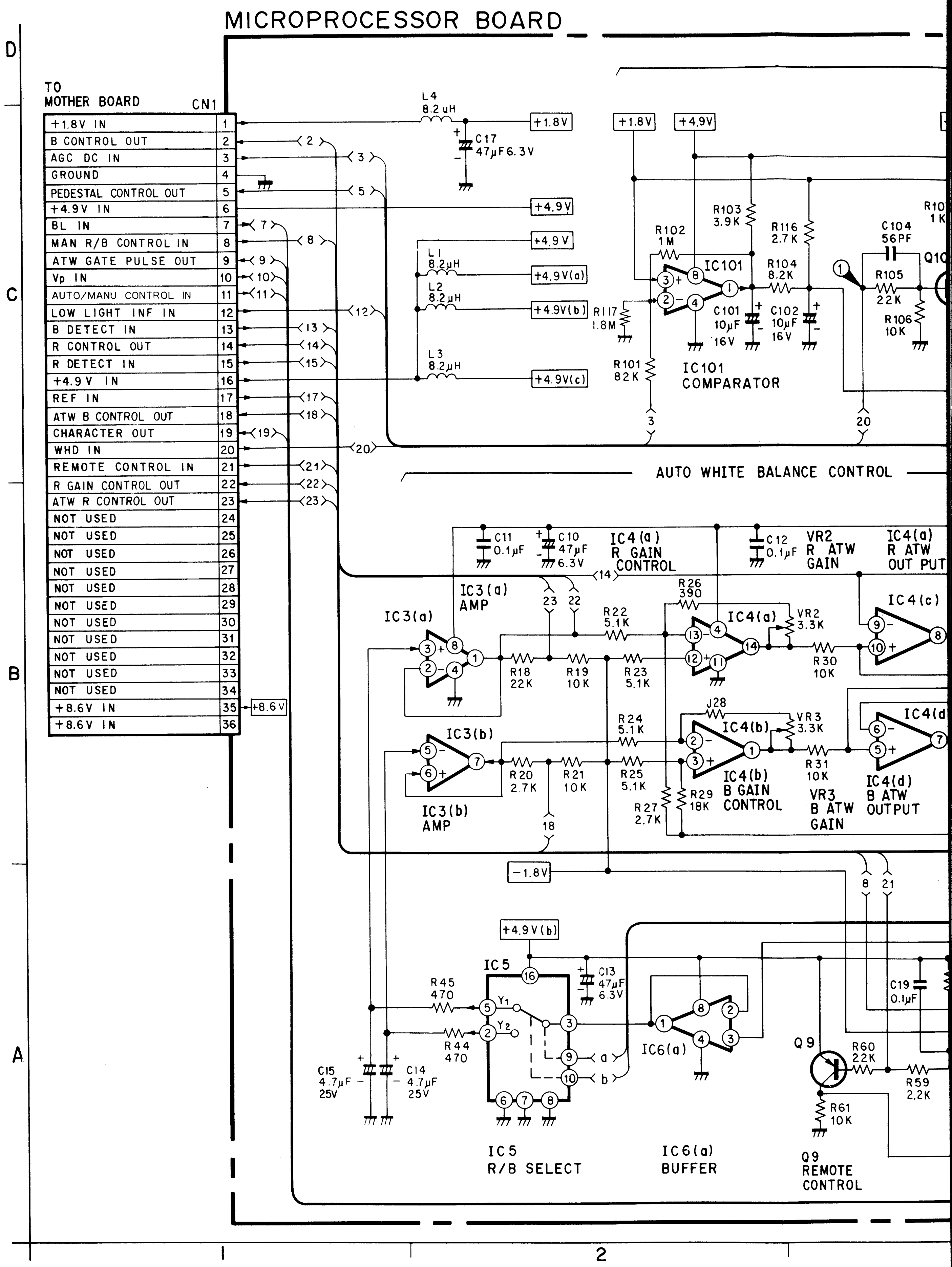
	IC1	IC2	IC3	IC4	IC5	IC6	IC7	IC8	IC9	IC10	IC11	IC101
Pin 1	4.7	4.7	1.6	1.7	1.7	1.8	3.8	0	4.3	5.0	0	0.1
2	4.7	4.7	1.6	1.7	1.8	1.8	2.3	0.1	0.1	0	4.6	1.7
3	4.7	4.7	1.6	1.7	1.8	1.8	2.3	0.1	4.1	5.0	0.3	1.7
4	4.7	4.7	0	5.0	0.1	0	0	0.1	4.5		0.1	0
5	4.7	4.7	1.8	1.7	1.7	1.7	2.3	0.1	0		0	0.3
6	0	0	1.8	1.7	0	1.7	2.3	0	0		0	0.3
7	0	0	0	1.7	0	1.7	2.1	0	4.6		0	0
8	0	0	5.0	1.7	0	5.0	5.0	0	0		0	4.9
9	4.2	4.2		1.7	0			0	4.6		0	
10	4.3	4.3		1.7	0			4.7	1.7		0	
11	4.7	4.7		0	0.1			0	4.3		0.7	
12	4.7	4.7		1.7	0.1			1.8	3.5		0	
13	4.7	4.7		1.7	0.1			1.7	4.6		0	
14	4.7	4.7		1.7	0.1			1.8	4.6		0	
15	4.7	4.7			4.7			4.7	3.9		4.6	
16	4.7	4.7			5.0			5.0	0		0	
17									4.7		4.1	
18									0		0	
19									2.0		0.3	
20									2.0		4.3	
21									4.6		4.2	
22									0.1		4.6	
23									4.6		4.3	
24									4.7		4.6	
25									4.6			
26									4.6			
27									4.6			
28									1.5			
29									1.5			
30									0			
31									0			
32									4.6			
33									4.5			
34									4.2			
35									4.6			
36									0			
37									4.6			
38									0			
39									0			
40									3.8			
41									2.1			
42									4.2			

	Q1	Q2	Q3	Q4	Q5	Q6	Q7	Q8	Q9	Q10	Q11	Q101	Q102	Q103	Q104
B	0	4.0	0	2.2	0	0	0	0	4.6	4.3	1.1	0.1	0.5	1.3	1.7
C	4.7	4.5	4.7	4.2	4.2	4.2	4.7	4.7	0	3.6	2.1	6.1	0.3	0	2.3
E	0	5.0	0	1.7	0.1	0	0.1	0	5.0	5.0	1.8	0	0	0.3	1.1

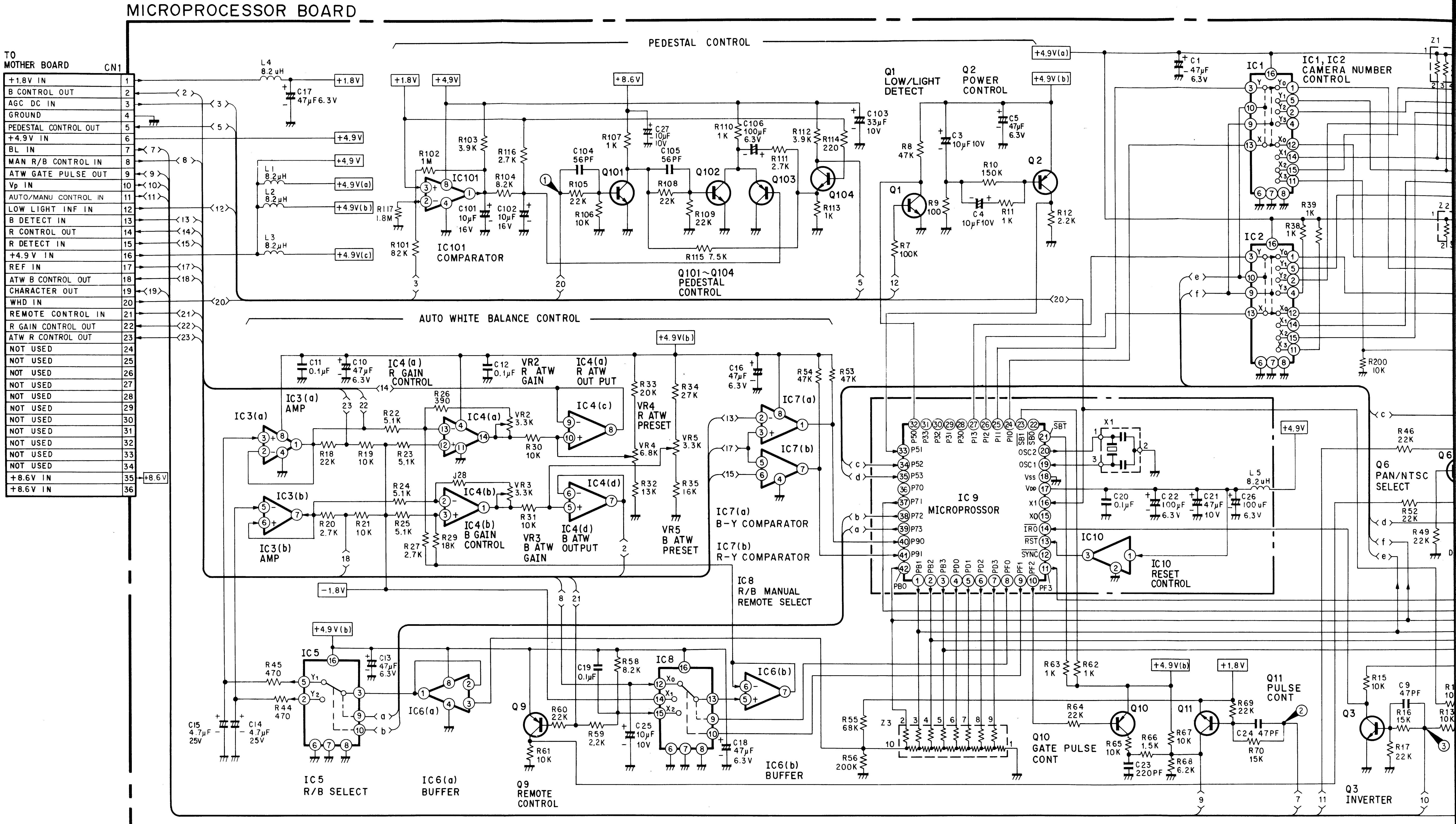
Microprocessor Board

IC1	NJU4052BM		C6
IC2	NJU4052BM		C6
IC3	NJM2904M		B2
IC4	NJM2902M		B2, B3
IC5	NJU4052BM		A2
IC6	NJM2904M		A2, A3
IC7	AN6914S		B4
IC8	NJU4052BM		A3
IC9	MN1552CVY		B4
IC10	M51951AMLW		B5
IC11	M50450FP		A7
IC101	NJM2903M		C2
Q1	2SC2405RSTTW	(S)	C4
Q2	2SB709-Q	(A)	C5
Q3	2SD601-QR	(Y)	A6
Q4	2SD601-QR	(Y)	B7
Q5	2SD601-QR	(Y)	B6
Q6	2SD601-QR	(Y)	B6
Q7	2SD601-QR	(Y)	B7
Q8	2SD601-QR	(Y)	B6
Q9	2SB709-Q	(A)	A3
Q10	2SB709-Q	(A)	A5
Q11	2SD601-QR	(Y)	A5
Q101	2SD601-QR	(Y)	C3
Q102	2SD601-QR	(Y)	C3
Q103	2SB709-Q	(A)	C3
Q104	2SD601-QR	(Y)	C4
D1	MA151WK	(MU)	B7
D2	MA151WK	(MU)	B6
D3	MA151K	(MH)	B6

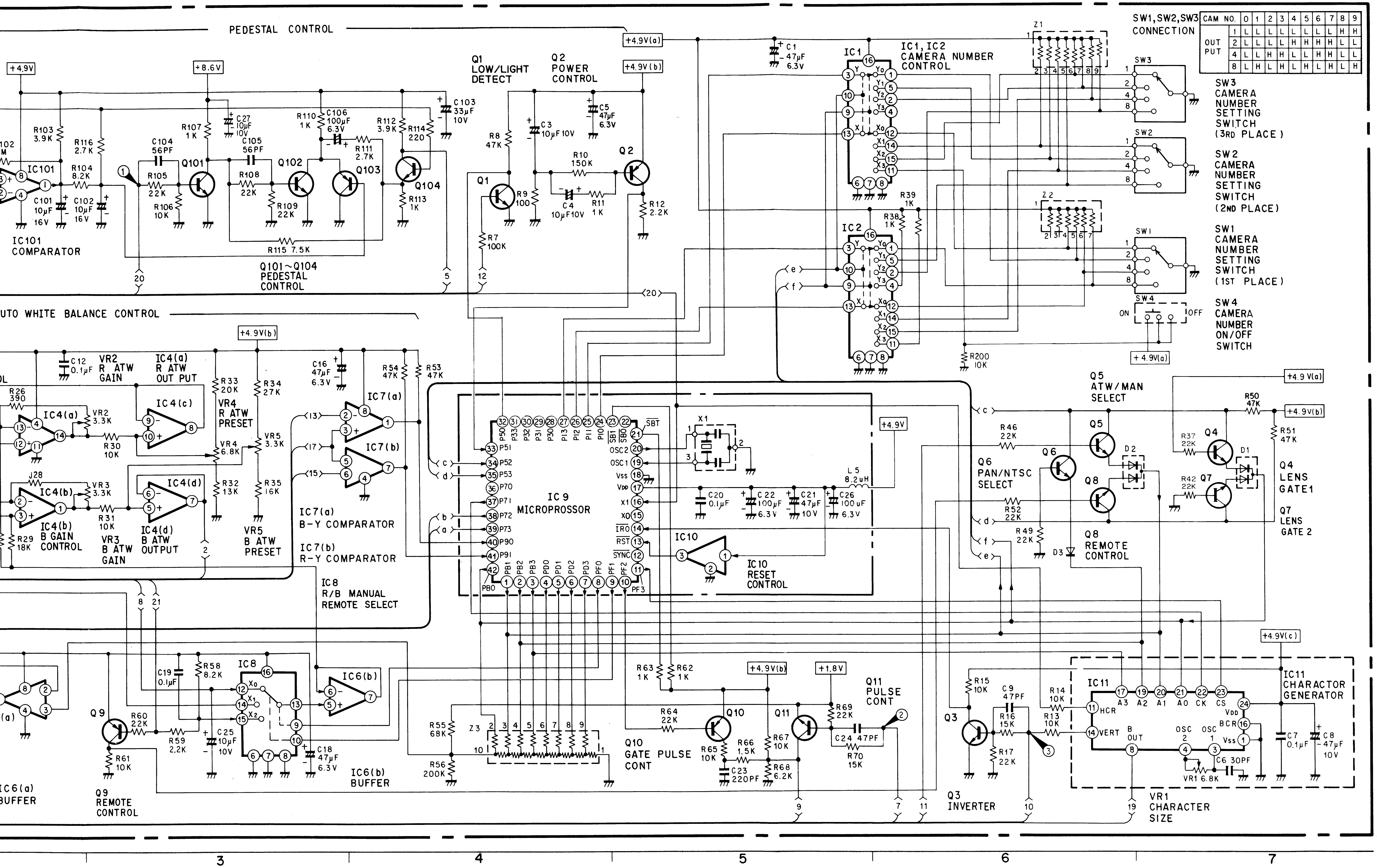
SCH


MICROPROCESSOR BOARD
TO MOTHER BOARD
CN1
+1.8V IN
B CONTROL OUT
AGC DC IN
GROUND
PEDESTAL CONTROL OUT
+4.9V IN
BL IN
MAN R/B CONTROL IN
ATW GATE PULSE OUT
Vp IN
AUTO/MANU CONTROL IN
LOW LIGHT INF IN
B DETECT IN
R CONTROL OUT
R DETECT IN
+4.9 V IN
REF IN
ATW B CONTROL OUT
CHARACTER OUT
WHD IN
REMOTE CONTROL IN
R GAIN CONTROL OUT
ATW R CONTROL OUT
NOT USED
+8.6V IN
+8.6V IN
+8.6V
L4 8.2µH
C17 47µF6.3V
+1.8V
+4.9V
+4.9V(a)
+4.9V(b)
+4.9V(c)
L1 8.2µH
L2 8.2µH
L3 8.2µH
R102 1M
R103 3.9K
R116 2.7K
R104 8.2K
R117 1.8M
R101 82K
C101 10µF 16V
C102 10µF 16V
C104 56PF
R105 22K
R106 10K
IC101
IC101 COMPARATOR
AUTO WHITE BALANCE CONTROL
C11 0.1µF
C10 47µF 6.3V
C12 0.1µF
IC4(a) R GAIN CONTROL
VR2 R ATW GAIN
IC4(a) R ATW OUT PUT
IC3(a) AMP
IC3(a)
R26 390
R22 5.1K
IC4(a)
VR2 3.3K
IC4(c)
R18 22K
R19 10K
R23 5.1K
R30 10K
J28
R24 5.1K
IC4(b)
VR3 3.3K
IC4(d)
IC3(b)
R20 2.7K
R21 10K
R25 5.1K
R31 10K
R27 2.7K
R29 18K
IC4(b) B GAIN CONTROL
VR3 B ATW GAIN
IC4(d) B ATW OUTPUT
IC3(b) AMP
-1.8V
+4.9V(b)
IC5
C13 47µF 6.3V
R45 470
R44 470
C15 4.7µF 25V
C14 4.7µF 25V
IC6(a)
C19 0.1µF
Q9
R60 22K
R59 2.2K
R61 10K
IC5 R/B SELECT
IC6(a) BUFFER
Q9 REMOTE CONTROL
D
C
B
A
1
2

SCHEMATIC DIAGRAM OF MICROPROCESSOR BOARD


MICROPROCESSOR BOARD
TO MOTHER BOARD
CN1
1 +1.8V IN
2 B CONTROL OUT
3 AGC DC IN
4 GROUND
5 PEDESTAL CONTROL OUT
6 +4.9V IN
7 BL IN
8 MAN R/B CONTROL IN
9 ATW GATE PULSE OUT
10 Vp IN
11 AUTO/MANU CONTROL IN
12 LOW LIGHT INF IN
13 B DETECT IN
14 R CONTROL OUT
15 R DETECT IN
16 +4.9V IN
17 REF IN
18 ATW B CONTROL OUT
19 CHARACTER OUT
20 WHD IN
21 REMOTE CONTROL IN
22 R GAIN CONTROL OUT
23 ATW R CONTROL OUT
24 NOT USED
25 NOT USED
26 NOT USED
27 NOT USED
28 NOT USED
29 NOT USED
30 NOT USED
31 NOT USED
32 NOT USED
33 NOT USED
34 NOT USED
35 +8.6V IN
36 +8.6V IN
PEDESTAL CONTROL
IC101 COMPARATOR
Q101~Q104 PEDESTAL CONTROL
Q1 LOW/LIGHT DETECT
Q2 POWER CONTROL
IC1, IC2 CAMERA NUMBER CONTROL
AUTO WHITE BALANCE CONTROL
IC3(a) AMP
IC3(b) AMP
IC4(a) R GAIN CONTROL
VR2 R ATW GAIN
IC4(a) R ATW OUT PUT
VR4 R ATW PRESET
IC4(b) B GAIN CONTROL
VR3 B ATW GAIN
IC4(d) B ATW OUTPUT
VR5 B ATW PRESET
IC7(a) B-Y COMPARATOR
IC7(b) R-Y COMPARATOR
IC8 R/B MANUAL REMOTE SELECT
IC9 MICROPROCESSOR
IC10 RESET CONTROL
Q6 PAN/NTSC SELECT
IC5 R/B SELECT
IC6(a) BUFFER
Q9 REMOTE CONTROL
IC6(b) BUFFER
Q10 GATE PULSE CONT
Q11 PULSE CONT
Q3 INVERTER
+1.8V
+4.9V
+8.6V
+4.9V(a)
+4.9V(b)
+4.9V(c)
-1.8V
1
2
3
4
5
6

SCHEMATIC DIAGRAM OF MICROPROCESSOR BOARD

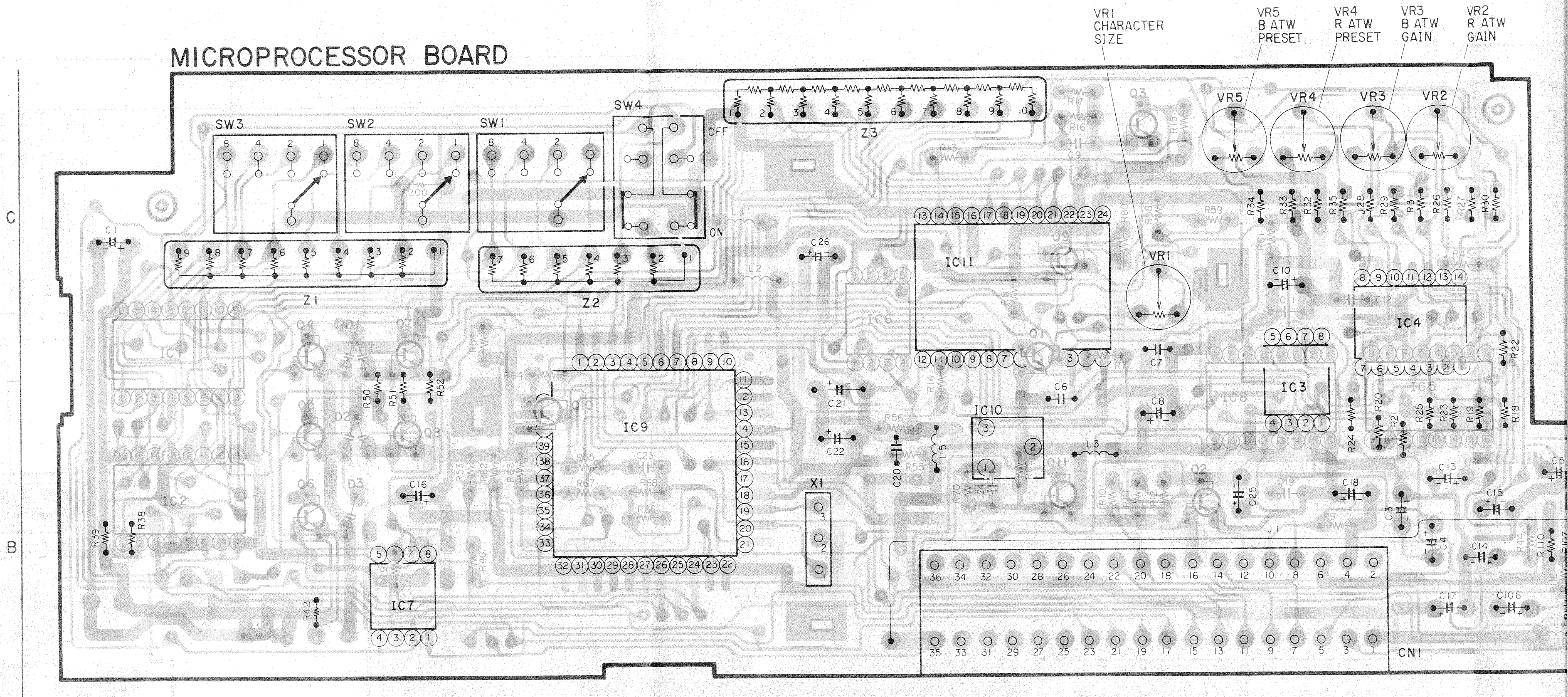


SW1, SW2, SW3 CONNECTION

CAM NO.		0	1	2	3	4	5	6	7	8	9
OUT PUT	1	L	L	L	L	L	L	L	L	H	H
	2	L	L	L	L	H	H	H	H	L	L
	4	L	L	H	H	L	L	H	H	L	L
	8	L	H	L	H	L	H	L	H	L	H

PEDESTAL CONTROL
+4.9V
+8.6V
+4.9V(a)
+4.9V(b)
+4.9V(c)
+4.9V
+1.8V
R103 3.9K
R116 2.7K
C104 56PF
R107 1K
C27 10μF 10V
C105 56PF
R110 1K
C106 100μF 6.3V
R111 2.7K
R112 3.9K
R114 220
C103 33μF 10V
R104 8.2K
IC101
C101 10μF 16V
C102 10μF 16V
R105 22K
R106 10K
Q101
R108 22K
R109 22K
Q102
Q103
Q104
R113 1K
R115 7.5K
IC101 COMPARATOR
Q101~Q104 PEDESTAL CONTROL
Q1 LOW/LIGHT DETECT
Q2 POWER CONTROL
R8 47K
C3 10μF 10V
C5 47μF 6.3V
R10 150K
R9 100
C4 10μF10V
R11 1K
R7 100K
R12 2.2K
C1 47μF 6.3V
IC1, IC2 CAMERA NUMBER CONTROL
IC1
IC2
Z1
Z2
R38 1K
R39 1K
R200 10K
SW3 CAMERA NUMBER SETTING SWITCH (3RD PLACE)
SW2 CAMERA NUMBER SETTING SWITCH (2ND PLACE)
SW1 CAMERA NUMBER SETTING SWITCH (1ST PLACE)
SW4 CAMERA NUMBER ON/OFF SWITCH
ON
OFF
AUTO WHITE BALANCE CONTROL
C12 0.1μF
VR2 R ATW GAIN
IC4(a) R ATW OUT PUT
IC4(c)
R26 390
VR2 3.3K
R30 10K
R33 20K
R34 27K
VR4 R ATW PRESET
VR4 6.8K
VR5 3.3K
J28
IC4(b)
VR3 3.3K
R31 10K
IC4(d)
R32 13K
R35 16K
R29 18K
IC4(b) B GAIN CONTROL
VR3 B ATW GAIN
IC4(d) B ATW OUTPUT
VR5 B ATW PRESET
C16 47μF 6.3V
R54 47K
R53 47K
IC7(a)
IC7(b)
IC7(a) B-Y COMPARATOR
IC7(b) R-Y COMPARATOR
IC8 R/B MANUAL REMOTE SELECT
IC9 MICROPROSSOR
P50 P33 P32 P31 P30 P13 P12 P11 P10 SB1 SB0 SBT
P51 P52 P53 P70 P71 P72 P73 P90 P91 PB0
PB1 PB2 PB3 PD0 PD1 PD2 PD3 PF0 PF1 PF2 PF3
OSC2 OSC1 Vss VDD X1 X0 IRQ RST SYNC
X1
L5 8.2μH
C20 0.1μF
C22 100μF 6.3V
C21 47μF 10V
C26 100μF 6.3V
IC10
IC10 RESET CONTROL
Q5 ATW/MAN SELECT
Q6 PAN/NTSC SELECT
Q8 REMOTE CONTROL
Q4 LENS GATE1
Q7 LENS GATE 2
R46 22K
R52 22K
R49 22K
D3
D2
D1
R37 22K
R42 22K
R50 47K
R51 47K
C19 0.1μF
R58 8.2K
IC8
IC6(b)
Q9
R60 22K
R59 2.2K
C25 10μF 10V
R61 10K
C18 47μF 6.3V
IC6(a) BUFFER
Q9 REMOTE CONTROL
IC6(b) BUFFER
R55 68K
R56 200K
Z3
R63 1K
R62 1K
R64 22K
Q10
R65 10K
R66 1.5K
R67 10K
R68 6.2K
C23 220PF
Q10 GATE PULSE CONT
Q11
Q11 PULSE CONT
R69 22K
C24 47PF
R70 15K
Q3
Q3 INVERTER
R15 10K
C9 47PF
R16 15K
R17 22K
R14 10K
R13 10K
IC11
IC11 CHARACTOR GENERATOR
HCR VERT B OUT OSC 2 OSC 1 Vss BCR VDD CS CK A0 A1 A2 A3
C6 30PF
VR1 6.8K
C7 0.1μF
C8 47μF 10V
VR1 CHARACTER SIZE
20 5 12 8 21 2 9 7 11 10 19
3
4
5
6
7

CONDUCTOR VIEW OF MICROPROCESSOR BOARD


MICROPROCESSOR BOARD
VR1 CHARACTER SIZE
VR5 B ATW PRESET
VR4 R ATW PRESET
VR3 B ATW GAIN
VR2 R ATW GAIN
SW1
SW2
SW3
SW4
OFF
ON
Z1
Z2
Z3
IC1
IC2
IC3
IC4
IC5
IC6
IC7
IC8
IC9
IC10
IC11
Q1
Q2
Q3
Q4
Q5
Q6
Q7
Q8
Q9
Q10
Q11
D1
D2
D3
X1
L1
L2
L3
L5
J1
CN1
C
B

SW1
CAMERA NUMBER SETTING SWITCH
(1ST PLACE)

SW2
CAMERA NUMBER SETTING SWITCH
(2ND PLACE)

SW3
CAMERA NUMBER SETTING SWITCH
(3RD PLACE)

SW4
CAMERA NUMBER ON/OFF SWITCH

TO MOTHER BOARD

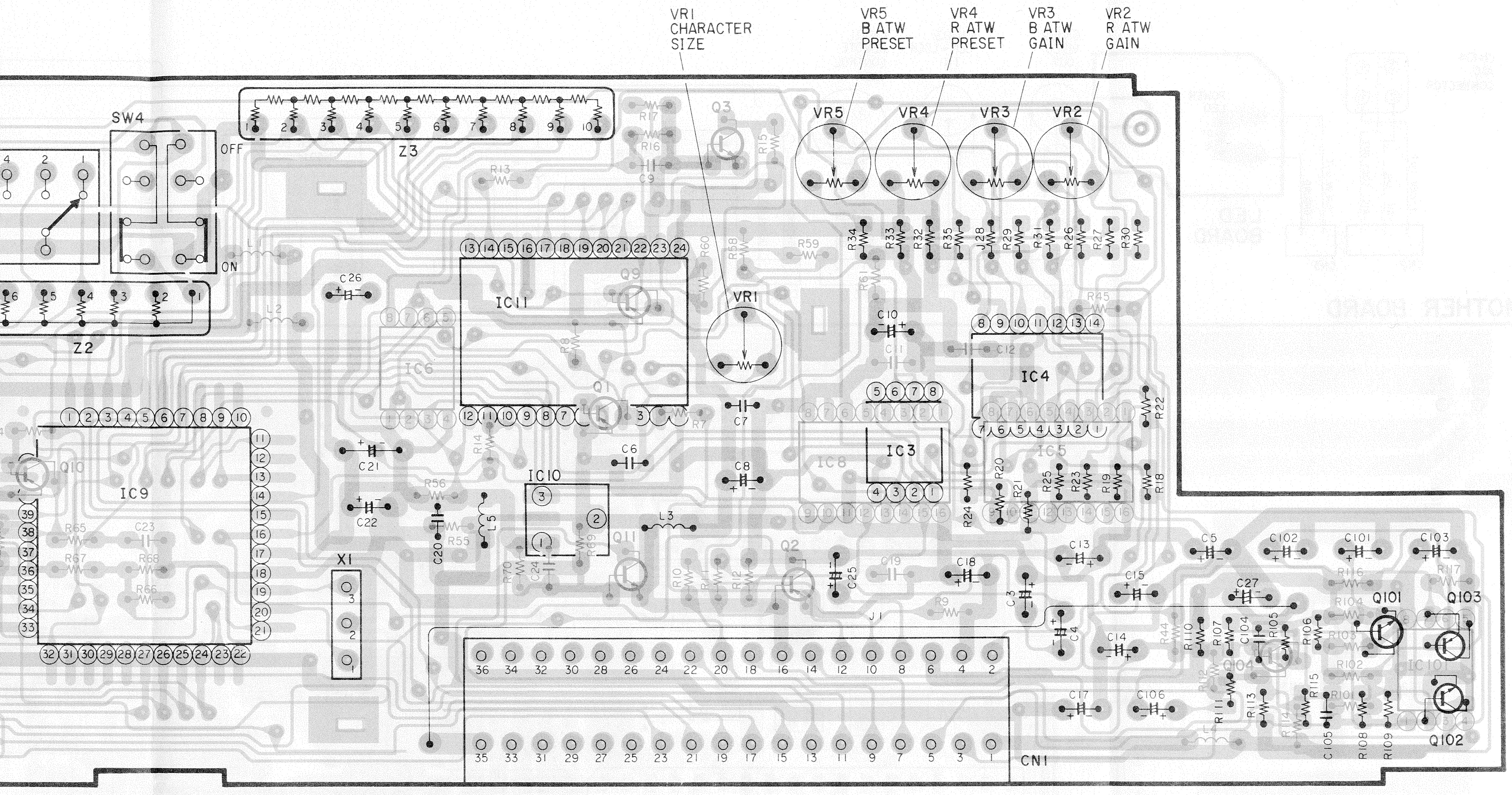
CN1

1	+1.8V IN	13	B DETECT IN	25	NOT USED
2	B CONTROL OUT	14	R CONTROL OUT	26	NOT USED
3	AGC DC IN	15	R DETECT IN	27	NOT USED
4	GROUND	16	+4.9V IN	28	NOT USED
5	PEDESTAL CONTROL OUT	17	REF IN	29	NOT USED
6	+4.9V IN	18	ATW B CONTROL OUT	30	NOT USED
7	BL IN	19	CHARACTER OUT	31	NOT USED
8	MAN R/B CONTROL IN	20	WHD IN	32	NOT USED
9	ATW GATE PULSE OUT	21	REMOTE CONTROL IN	33	NOT USED
10	Vp IN	22	R GAIN CONTROL OUT	34	NOT USED
11	AUTO/MANU CONTROL IN	23	ATW R CONTROL OUT	35	+8.6V IN
12	LOW LIGHT INF IN	24	NOT USED	36	+8.6V IN

A

1 2 3 4 5

CONDUCTOR VIEW OF MICROPROCESSOR BOARD


VR1 CHARACTER SIZE
VR5 B ATW PRESET
VR4 R ATW PRESET
VR3 B ATW GAIN
VR2 R ATW GAIN
SW4
OFF
ON
Z3
Z2
VR5
VR4
VR3
VR2
VR1
IC11
IC9
IC10
IC4
IC3
IC5
IC6
IC8
Q1
Q2
Q3
Q9
Q10
Q11
Q101
Q102
Q103
Q104
X1
L1
L2
L3
L4
L5
J1
CN1
R34 R33 R32 R35 J28 R29 R31 R26 R27 R30
R45
R22
R20 R21 R24 R25 R23 R19 R18
R13 R15 R16 R17
R55 R56 R58 R59 R60 R61
R65 R66 R67 R68 R69 R70
R7 R8 R9 R10 R11 R12 R14
R44 R101 R102 R103 R104 R105 R106 R107 R108 R109 R110 R111 R112 R113 R114 R115 R116 R117
C3 C4 C5 C6 C7 C8 C9 C10 C11 C12 C13 C14 C15 C17 C18 C19 C20 C21 C22 C23 C24 C25 C26 C27
C101 C102 C103 C104 C105 C106
36 34 32 30 28 26 24 22 20 18 16 14 12 10 8 6 4 2
35 33 31 29 27 25 23 21 19 17 15 13 11 9 7 5 3 1

Microprocessor Board

IC1	NJU4052BM		C1
IC2	NJU4052BM		B1
IC3	NJM2904M		B4
IC4	NJM2902M		C5
IC5	NJU4052BM		B5
IC6	NJM2904M		C3
IC7	AN6914S		B2
IC8	NJU4052BM		B4
IC9	MN1552CVY		B2
IC10	M51951AMLW		B4
IC11	M50450FP		C4
IC101	NJM2903M		B6
Q1	2SC2405RSTTW	(S)	C4
Q2	2SB709-Q	(A)	B4
Q3	2SD601-QR	(Y)	C4
Q4	2SD601-QR	(Y)	C1
Q5	2SD601-QR	(Y)	B1
Q6	2SD601-QR	(Y)	B1
Q7	2SD601-QR	(Y)	C2
Q8	2SD601-QR	(Y)	B2
Q9	2SB709-Q	(A)	C4
Q10	2SB709-Q	(A)	B2
Q11	2SD601-QR	(Y)	B4
Q101	2SD601-QR	(Y)	B6
Q102	2SD601-QR	(Y)	B6
Q103	2SB709-Q	(A)	B6
Q104	2SD601-QR	(Y)	B5
D1	MA151WK	(MU)	C2
D2	MA151WK	(MU)	B2
D3	MA151K	(MH)	B2

SWITCH

SWITCH

SWITCH

WITCH

TO
MOTHER
BOARD

CN1

1	+1.8V IN	13	B DETECT IN	25	NOT USED
2	B CONTROL OUT	14	R CONTROL OUT	26	NOT USED
3	AGC DC IN	15	R DETECT IN	27	NOT USED
4	GROUND	16	+4.9V IN	28	NOT USED
5	PEDESTAL CONTROL OUT	17	REF IN	29	NOT USED
6	+4.9V IN	18	ATW B CONTROL OUT	30	NOT USED
7	BL IN	19	CHARACTER OUT	31	NOT USED
8	MAN R/B CONTROL IN	20	WHD IN	32	NOT USED
9	ATW GATE PULSE OUT	21	REMOTE CONTROL IN	33	NOT USED
10	Vp IN	22	R GAIN CONTROL OUT	34	NOT USED
11	AUTO/MANU CONTROL IN	23	ATW R CONTROL OUT	35	+8.6V IN
12	LOW LIGHT INF IN	24	NOT USED	36	+8.6V IN

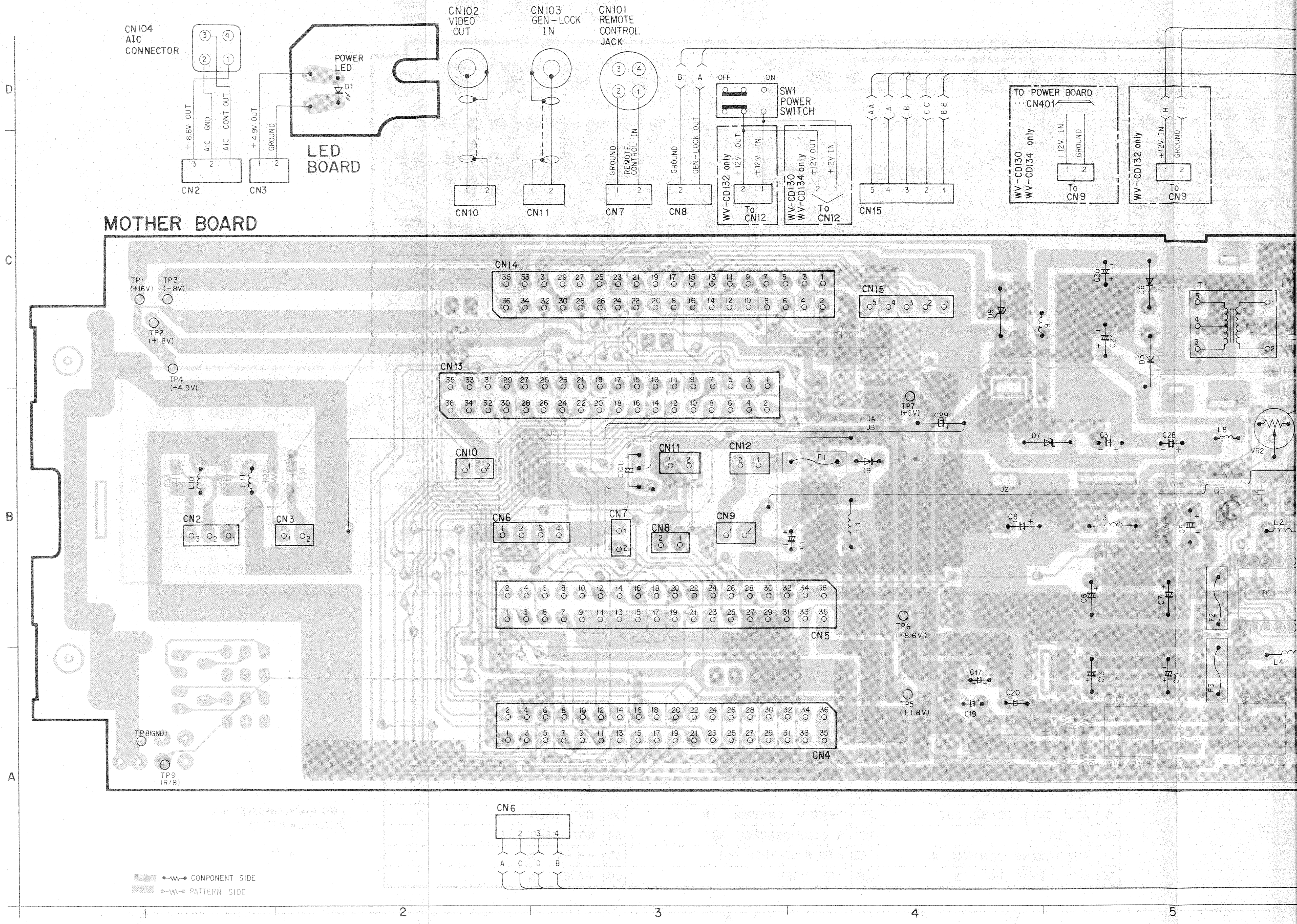
▨ COMPONENT SIDE
▧ PATTERN SIDE

3 4 5 6

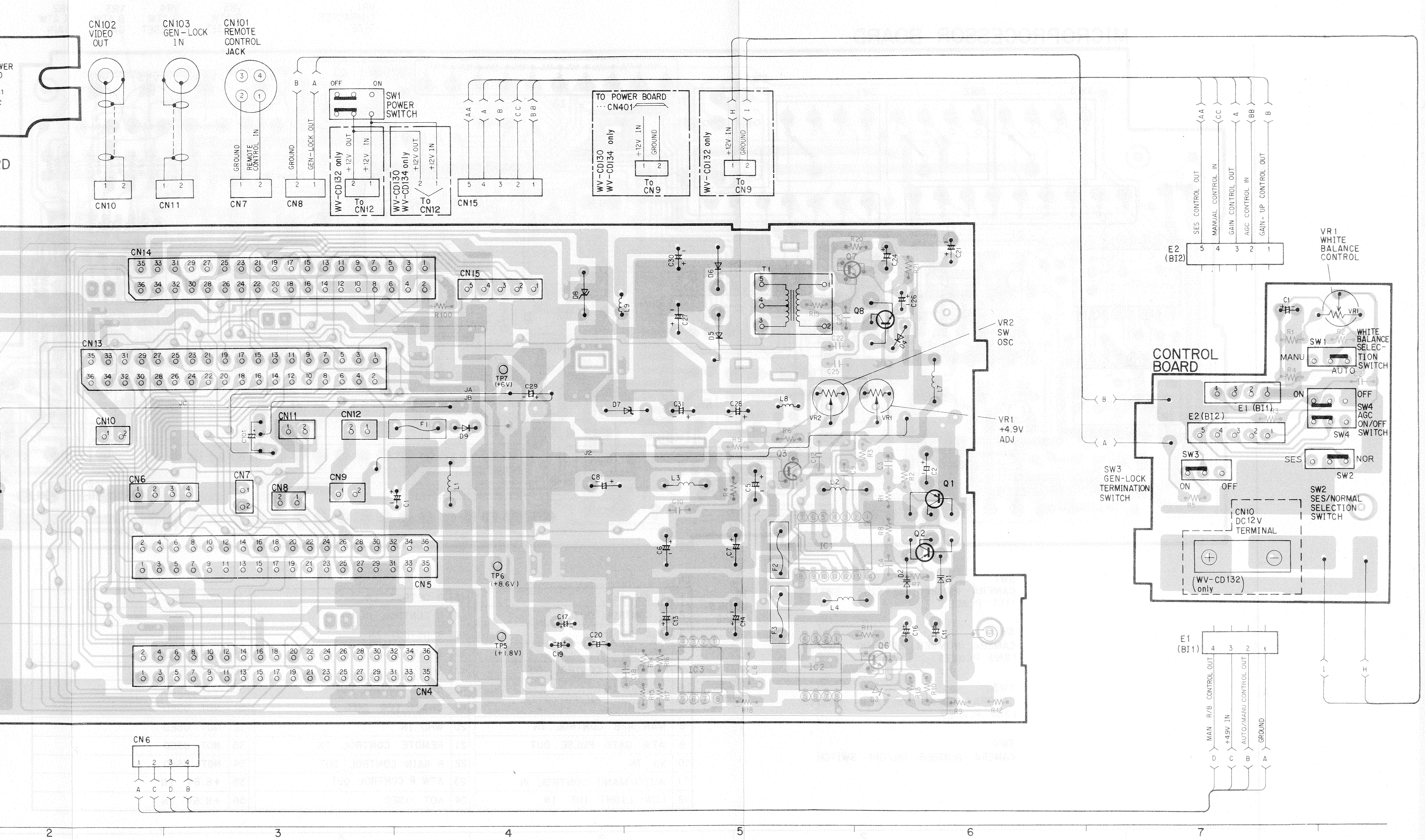
CONDUCTOR VIEW OF MOTHER BOARD

Mother Board

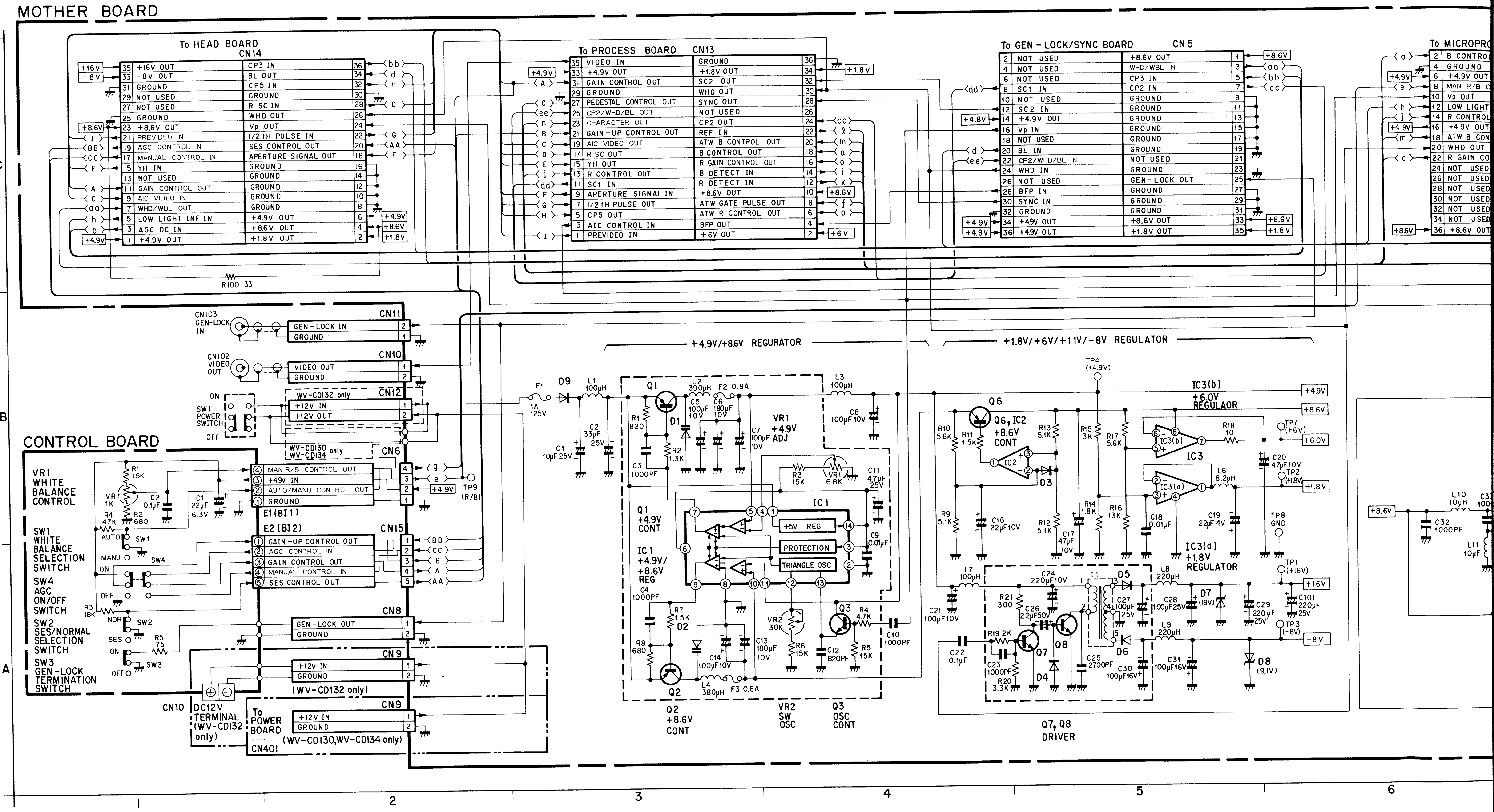
IC1	NJM2048MC		B5
IC2	M5218FP		A5
IC3	NJM3414M		A5
Q1	2SB837-CD		B6
Q2	2SB837-CD		B6
Q3	2SD601-QR	(Y)	B5
Q6	2SB970-QRS	(1R)	A6
Q7	2SD602-QRS	(W)	C6
Q8	2SC3303-0Y		C6
D1	EK14		B6
D2	EK14		B6
D3	MA151K	(MH)	A6
D4	MA170		C6
D5	EU01A		C5
D6	EU01A		C5
D7	RD18EB3		B5
D8	RD9.1EB3		C4
D9	SS1.5J4		B4


CN104
AIC
CONNECTOR
+8.6V OUT
AIC GND
AIC CONT OUT
CN2
+4.9V OUT
GROUND
CN3
POWER
LED
D1
LED
BOARD
CN102
VIDEO
OUT
CN10
CN103
GEN-LOCK
IN
CN11
CN101
REMOTE
CONTROL
JACK
GROUND
REMOTE CONTROL IN
CN7
GROUND
GEN-LOCK OUT
CN8
OFF
ON
SW1
POWER
SWITCH
WV-CD132 only
+12V OUT
+12V IN
To
CN12
WV-CD130
WV-CD134 only
+12V OUT
+12V IN
To
CN12
CN15
TO POWER BOARD
CN401
WV-CD130
WV-CD134 only
+12V IN
GROUND
To
CN9
WV-CD132 only
+12V IN
GROUND
To
CN9
MOTHER BOARD
TP1
(+16V)
TP3
(-8V)
TP2
(+1.8V)
TP4
(+4.9V)
CN14
CN13
CN15
R100
D8
L9
C30
D6
T1
C27
D5
R19
TP7
(+6V)
C29
JA
JB
JC
D7
C31
C28
L8
VR2
CN10
CN11
CN12
F1
D9
C101
R5
R6
J2
Q3
C12
C33
L10
C32
L11
R22
C34
CN2
CN3
CN6
CN7
CN8
CN9
C1
L1
C8
L3
R4
C5
L2
C10
C6
C7
IC1
F2
CN5
TP6
(+8.6V)
C17
C20
C13
C14
F3
L4
TP5
(+1.8V)
C19
C18
R14
R16
R15
R17
IC3
L6
IC2
R18
CN4
TP8(GND)
TP9
(R/B)
CN6
A
C
D
B
CONPONENT SIDE
PATTERN SIDE
A
B
C
D
1
2
3
4
5

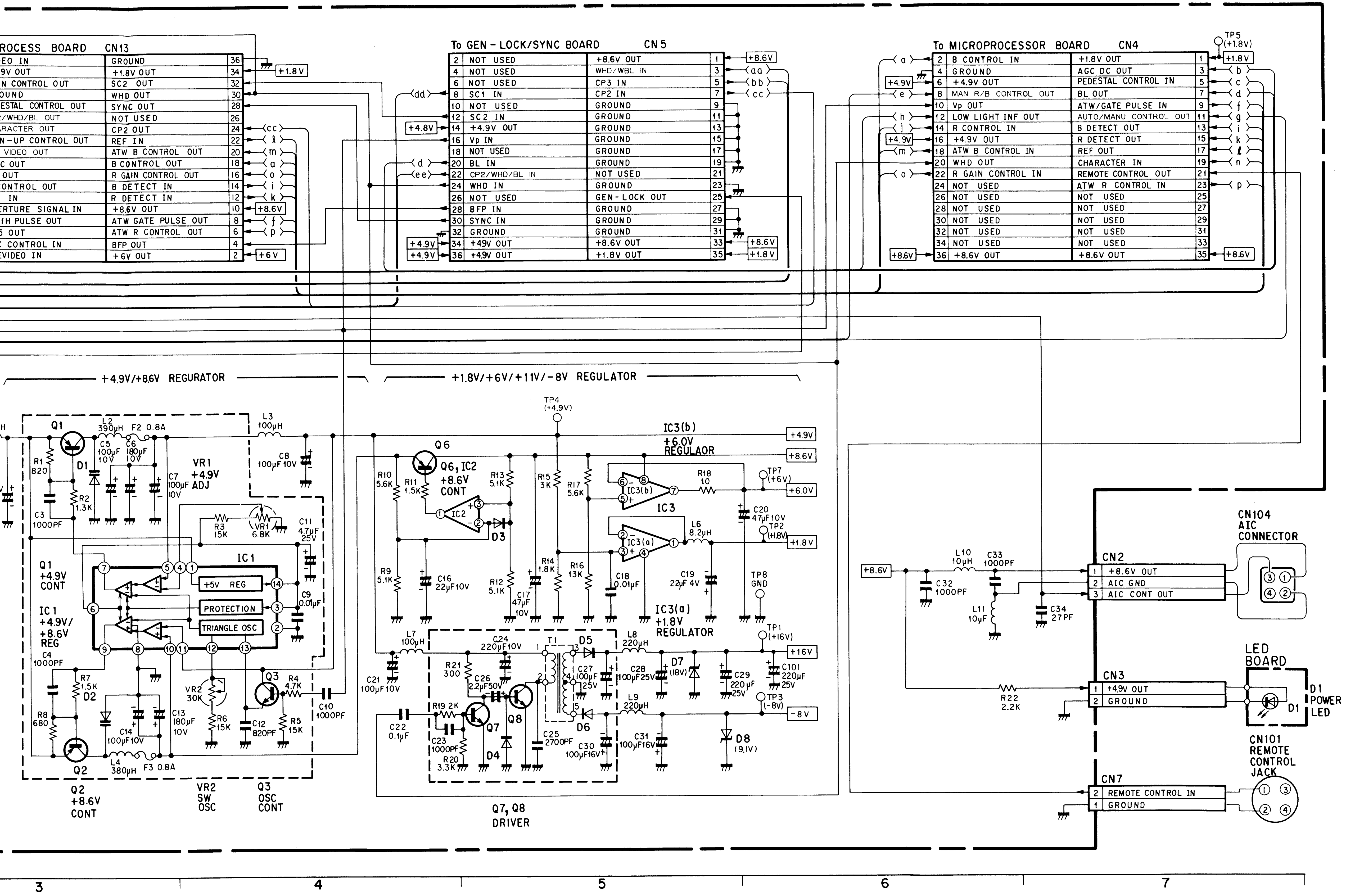
CONDUCTOR VIEW OF MOTHER BOARD


CN102 VIDEO OUT
CN103 GEN-LOCK IN
CN101 REMOTE CONTROL JACK
CN10
CN11
CN7
GROUND
REMOTE CONTROL IN
CN8
GROUND
GEN-LOCK OUT
SW1 POWER SWITCH
OFF
ON
WV-CD132 only
+12V OUT
+12V IN
To CN12
WV-CD130
WV-CD134 only
+12V OUT
+12V IN
To CN12
CN15
AA
A
B
CC
BB
TO POWER BOARD
···CN401
WV-CD130
WV-CD134 only
+12V IN
GROUND
To CN9
WV-CD132 only
+12V IN
GROUND
To CN9
H
I
SES CONTROL OUT
MANUAL CONTROL IN
GAIN CONTROL OUT
AGC CONTROL IN
GAIN-UP CONTROL OUT
E2 (BI2)
VR1 WHITE BALANCE CONTROL
CN14
CN13
CN15
R100
TP7 (+6V)
C29
JA
JB
JC
CN10
CN11
CN12
C101
F1
D9
CN6
CN7
CN8
CN9
CN5
CN4
TP6 (+8.6V)
TP5 (+1.8V)
C17
C19
C20
D8
L9
C30
C27
D6
D5
D7
C31
C28
L8
T1
R19
R20
Q7
C24
R21
C21
C26
Q8
C23
C22
C25
D4
L7
VR2
VR1
VR2 SW OSC
VR1 +4.9V ADJ
R5
R6
J2
Q3
C12
R3
C3
C8
L3
L1
C1
C10
R4
C5
L2
R1
R2
C2
Q1
IC1
R8
Q2
C6
C7
F2
C4
D2
R7
D1
L4
F3
C13
C14
C16
C11
R11
Q6
IC3
IC2
C18
R15
R17
R16
R14
L6
R18
D3
R13
R10
R9
R12
CONTROL BOARD
C1
VR1
R1
R2
WHITE BALANCE SELECTION SWITCH
SW1
MANU
AUTO
R4
E1 (BI1)
E2 (BI2)
ON
OFF
SW4 AGC ON/OFF SWITCH
R3
SW3
SES
NOR
SW2
ON
OFF
R5
SW3 GEN-LOCK TERMINATION SWITCH
SW2 SES/NORMAL SELECTION SWITCH
CN10 DC12V TERMINAL
(WV-CD132) only
E1 (BI1)
MAN R/B CONTROL OUT
+49V IN
AUTO/MANU CONTROL OUT
GROUND
D
C
B
A
CN6
A
C
D
B
2
3
4
5
6
7

SCHEMATIC DIAGRAM OF MOTHER BOARD


MOTHER BOARD
To HEAD BOARD CN14
35 +16V OUT
33 -8V OUT
31 GROUND
29 NOT USED
27 NOT USED
25 GROUND
23 +8.6V OUT
21 PREVIDEO IN
19 AGC CONTROL IN
17 MANUAL CONTROL IN
15 YH IN
13 NOT USED
11 GAIN CONTROL OUT
9 AIC VIDEO IN
7 WHD/WBL OUT
5 LOW LIGHT INF IN
3 AGC DC IN
1 +4.9V OUT
CP3 IN 36
BL OUT 34
CP5 IN 32
GROUND 30
R SC IN 28
WHD OUT 26
Vp OUT 24
1/2 fH PULSE IN 22
SES CONTROL OUT 20
APERTURE SIGNAL OUT 18
GROUND 16
GROUND 14
GROUND 12
GROUND 10
GROUND 8
+4.9V OUT 6
+8.6V OUT 4
+1.8V OUT 2
R100 33
To PROCESS BOARD CN13
35 VIDEO IN
33 +4.9V OUT
31 GAIN CONTROL OUT
29 GROUND
27 PEDESTAL CONTROL OUT
25 CP2/WHD/BL OUT
23 CHARACTER OUT
21 GAIN-UP CONTROL OUT
19 AIC VIDEO OUT
17 R SC OUT
15 YH OUT
13 R CONTROL OUT
11 SC1 IN
9 APERTURE SIGNAL IN
7 1/2 fH PULSE OUT
5 CP5 OUT
3 AIC CONTROL IN
1 PREVIDEO IN
GROUND 36
+1.8V OUT 34
SC2 OUT 32
WHD OUT 30
SYNC OUT 28
NOT USED 26
CP2 OUT 24
REF IN 22
ATW B CONTROL OUT 20
B CONTROL OUT 18
R GAIN CONTROL OUT 16
B DETECT IN 14
R DETECT IN 12
+8.6V OUT 10
ATW GATE PULSE OUT 8
ATW R CONTROL OUT 6
BFP OUT 4
+6V OUT 2
To GEN-LOCK/SYNC BOARD CN5
2 NOT USED
4 NOT USED
6 NOT USED
8 SC1 IN
10 NOT USED
12 SC2 IN
14 +4.9V OUT
16 Vp IN
18 NOT USED
20 BL IN
22 CP2/WHD/BL IN
24 WHD IN
26 NOT USED
28 BFP IN
30 SYNC IN
32 GROUND
34 +4.9V OUT
36 +4.9V OUT
+8.6V OUT 1
WHD/WBL IN 3
CP3 IN 5
CP2 IN 7
GROUND 9
GROUND 11
GROUND 13
GROUND 15
GROUND 17
GROUND 19
NOT USED 21
GROUND 23
GEN-LOCK OUT 25
GROUND 27
GROUND 29
GROUND 31
+8.6V OUT 33
+1.8V OUT 35
To MICROPRO
2 B CONTROL
4 GROUND
6 +4.9V OUT
8 MAN R/B C
10 Vp OUT
12 LOW LIGHT
14 R CONTROL
16 +4.9V OUT
18 ATW B CON
20 WHD OUT
22 R GAIN CO
24 NOT USED
26 NOT USED
28 NOT USED
30 NOT USED
32 NOT USED
34 NOT USED
36 +8.6V OUT
CN103 GEN-LOCK IN
CN11 GEN-LOCK IN GROUND
CN102 VIDEO OUT
CN10 VIDEO OUT GROUND
SW1 POWER SWITCH ON OFF
CN12 WV-CD132 only +12V IN +12V OUT
WV-CD130 WV-CD134 only
CN6
TP9 (R/B)
CONTROL BOARD
VR1 WHITE BALANCE CONTROL
SW1 WHITE BALANCE SELECTION SWITCH
SW4 AGC ON/OFF SWITCH
SW2 SES/NORMAL SELECTION SWITCH
SW3 GEN-LOCK TERMINATION SWITCH
AUTO MANU ON OFF NOR SES
E1(BI1)
4 MAN R/B CONTROL OUT
3 +4.9V IN
2 AUTO/MANU CONTROL OUT
1 GROUND
E2(BI2)
CN15
1 GAIN-UP CONTROL OUT
2 AGC CONTROL IN
3 GAIN CONTROL OUT
4 MANUAL CONTROL IN
5 SES CONTROL OUT
CN8 GEN-LOCK OUT GROUND
CN9 +12V IN GROUND (WV-CD132 only)
CN10 DC12V TERMINAL (WV-CD132 only)
To POWER BOARD CN401
CN9 +12V IN GROUND (WV-CD130,WV-CD134 only)
+4.9V/+8.6V REGURATOR
F1 1A 125V
D9
L1 100μH
Q1
L2 390μH
F2 0.8A
VR1 +4.9V ADJ
L3 100μH
IC1
+5V REG
PROTECTION
TRIANGLE OSC
Q1 +4.9V CONT
IC1 +4.9V/+8.6V REG
Q2 +8.6V CONT
VR2 SW OSC
Q3 OSC CONT
L4 380μH F3 0.8A
+1.8V/+6V/+11V/-8V REGULATOR
TP4 (+4.9V)
Q6
Q6,IC2 +8.6V CONT
IC2
IC3(b) +6.0V REGULAOR
IC3
IC3(a) +1.8V REGULATOR
TP7 (+6V)
TP2 (+1.8V)
TP8 GND
TP1 (+16V)
TP3 (-8V)
T1
D5 D6 D7 (18V) D8 (9.1V)
Q7 Q8
Q7, Q8 DRIVER
+4.9V +8.6V +6.0V +1.8V +16V -8V
1 2 3 4 5 6
A B C

SCHEMATIC DIAGRAM OF MOTHER BOARD


CN13
GROUND
+1.8V OUT
SC2 OUT
WHD OUT
SYNC OUT
NOT USED
CP2 OUT
REF IN
ATW B CONTROL OUT
B CONTROL OUT
R GAIN CONTROL OUT
B DETECT IN
R DETECT IN
+8.6V OUT
ATW GATE PULSE OUT
ATW R CONTROL OUT
BFP OUT
+6V OUT
To GEN-LOCK/SYNC BOARD CN5
NOT USED
SC1 IN
SC2 IN
+4.9V OUT
Vp IN
BL IN
CP2/WHD/BL IN
WHD IN
BFP IN
SYNC IN
GROUND
+4.9V OUT
+8.6V OUT
WHD/WBL IN
CP3 IN
CP2 IN
GEN-LOCK OUT
+1.8V OUT
To MICROPROCESSOR BOARD CN4
B CONTROL IN
GROUND
+4.9V OUT
MAN R/B CONTROL OUT
Vp OUT
LOW LIGHT INF OUT
R CONTROL IN
ATW B CONTROL IN
WHD OUT
R GAIN CONTROL IN
+8.6V OUT
+1.8V OUT
AGC DC OUT
PEDESTAL CONTROL IN
BL OUT
ATW/GATE PULSE IN
AUTO/MANU CONTROL OUT
B DETECT OUT
R DETECT OUT
REF OUT
CHARACTER IN
REMOTE CONTROL OUT
ATW R CONTROL IN
+4.9V/+8.6V REGURATOR
+1.8V/+6V/+11V/-8V REGULATOR
Q1 +4.9V CONT
IC1 +4.9V/+8.6V REG
+5V REG
PROTECTION
TRIANGLE OSC
VR1 +4.9V ADJ
Q2 +8.6V CONT
VR2 SW OSC
Q3 OSC CONT
Q6, IC2 +8.6V CONT
IC3(b) +6.0V REGULAOR
IC3(a) +1.8V REGULATOR
Q7, Q8 DRIVER
CN104 AIC CONNECTOR
CN2
+8.6V OUT
AIC GND
AIC CONT OUT
LED BOARD
D1 POWER LED
CN3
+4.9V OUT
GROUND
CN101 REMOTE CONTROL JACK
CN7
REMOTE CONTROL IN
GROUND
3
4
5
6
7

Mother Board

IC1	NJM2048MC		A4
IC2	M5218FP		B5
IC3	NJM3414M		B5
Q1	2SB837-CD		B3
Q2	2SB837-CD		B3
Q3	2SD601-QR	(Y)	A4
Q6	2SB970-QRS	(1R)	B4
Q7	2SD602-QRS	(W)	A5
Q8	2SC3303-0Y		A5
D1	EK14		B3
D2	EK14		A3
D3	MA151K	(MH)	B5
D4	MA170		A5
D5	EU01A		A5
D6	EU01A		A5
D7	RD18EB3		A5
D8	RD9.1EB3		A5
D9	SS1.5J4		B3

	IC1	IC2	IC3
Pin 1	12.0	6.6	1.7
2	0	4.1	1.7
3	5.0	4.1	1.7
4	0.5	0	0
5	5.0	2.8	5.7
6	5.0	2.8	5.8
7	6.3	11.1	6.2
8	0	11.6	8.2
9	3.2		
10	8.6		
11	5.0		
12	3.8		
13	2.6		
14	5.0		

	Q1	Q2	Q3	Q6	Q7	Q8
B	11.3	11.1	0	7.9	-0.4	15.6
C	5.0	8.6	4.7	8.2	1.5	6.1
E	12.0	12.0	2.6	8.6	0	0

REPLACEMENT PARTS LIST

Important Notice

1. Components identified by "⚠" mark have special characteristics important for safety. When replacing any of these components, use only manufacturer's specified parts.
2. Components identified by "o" mark are new parts used from this model.
3. Complete printed circuit board assembly is not available as replacement part.

SYMBOL NO.	ORIGINAL PART NO.	NEW PART NO.	DESCRIPTION
COLOR CCTV CAMERA WV-CD130			
MISCELLANEOUS			
CN101	YFHR10A7R4S	YWHR10A7R4S	4 pin Remoto Control Connector
CN110	YFCE030908FA	YWCO309-08F	AC Power Cord for WV-CD130/C,WV-CD130N/C
⚠	HBS308A	YWHBS-3-08	AC Power Cord for WV-CD130/B
⚠	YFVM030908A	YWVM0309-08	AC Power Cord for WV-CD130/G
⚠	YFKPGTSA25	YWKPGTSA-03	AC Power Cord for WV-CD130N/A
M36	YFV2PA0208A4	YFV2PA0208B4	Insulator
MICROPROCESSOR BOARD			
R32	YF2116133JT	YF2116133GT	Carbon 13K ohms 1/16W
R34	YF2116273JT	YF2116273GT	Carbon 27K ohms 1/16W
R55	YF2116683JT	YF2116683GT	Carbon 68K ohms 1/16W
R59	YF2116222JT	YF2116222GT	Carbon 2.2K ohms 1/16W
R111	YF2116272JT	YF2116272GT	Carbon 2.7K ohms 1/16W
R113	YF2116102JT	YF2116102GT	Carbon 1K ohms 1/16W
R115	YF2116912JT	YF2116912GT	Carbon 9.1K ohms 1/16W
R116	YF2116821JT	YF2116821GT	Carbon 820 ohms 1/16W
R200	---------------	ERJ8GCSJ103	Carbon 10K ohms 1/8W
C1	ECEA0JKS470	ECEA0JS470	Electrolytic 47µF 6.3V
C2	ECEA0JKS470	---------------	Deletion
C3,4	ECEA1AKK100	ECEA1EK100	Electrolytic 10µF 10V
C5	ECEA0JKS470	ECEA0JS470	Electrolytic 47µF 6.3V
C10	ECEA0JKS470	ECEA0JS470	Electrolytic 47µF 6.3V
C13	ECEA0JKS470	ECEA0JS470	Electrolytic 47µF 6.3V
C14,15	ECEA1EKS4R7	ECEA1ES4R7	Electrolytic 4.7µF 25V
C16-18	ECEA0JKS470	ECEA0JS470	Electrolytic 47µF 6.3V
C25	ECEA1AKK100	ECEA1EK100	Electrolytic 10µF 10V
C27	ECEA1AKK100	ECEA1EK100	Electrolytic 10µF 10V
C106	ECEA0JU101	ECEA0JKS101	Electrolytic 100µF 6.3V
GEN-LOCK/SYNC BOARD			
IC1	YFNJM1496M	NJM1496M	IC
Q12	2SA1022-CD	2SA1022-C	Transistor
R49	YFLP161KGT57	LP161KGT57	Carbon 1K ohms
R70	YF2116393JT	YF2116393GT	Carbon 39K ohms 1/16W
R79	YF2116683JT	YF2116683GT	Carbon 68K ohms 1/16W
R85	YF2116433JT	YF2116433GT	Carbon 43K ohms 1/16W
R150	---------------	YF2116223JT	Carbon 22K ohms 1/16W
VR1	YFH0522A47K	EVML3GA00B54	Variable Resistor 50K ohms
VR2	YFH0522A100K	oEVML3GA00B15	Variable Resistor 100K ohms
C1	ECEA0JKS470	ECEA0JS470	Electrolytic 47µF 6.3V
C3	YF400104XKT	YF400104FZT	Ceramic 0.1µF
C11	ECEA0JKS470	ECEA0JS470	Electrolytic 47µF 6.3V
C18	ECEA0JKS470	ECEA0JS470	Electrolytic 47µF 6.3V
C22	ECEA0JKS470	ECEA0JS470	Electrolytic 47µF 6.3V
C24	ECEA0JKS470	ECEA0JS470	Electrolytic 47µF 6.3V
C26	ECEA1AKS4R7	ECEA1AS470	Electrolytic 47µF 10V
C27	ECEA1AKS470	ECEA0JS470	Electrolytic 47µF 6.3V
C28,29	ECEA1AKS4R7	ECEA1AS470	Electrolytic 47µF 10V
C39	ECEA1AKS470	ECEA1AS470	Electrolytic 47µF 10V
J125	-------------	YF21160R00T	Jamper Resistor
J201	-------------	YF21160R00T	Jamper Resistor
CCD BOARD			
Q2	2SA1022-CTW	2SA1022-C	Transistor
Q5	2SA1022-CTW	2SA1022-C	Transistor
R4	YF2116514JT	YF2116514GT	Carbon 510K ohms 1/16W
R13	YF2116332JT	YF2116392JT	Carbon 3.9K ohms 1/16W
C1	ECSF1EE106	oECST1ED106ZR	Tantalum 10µF 25V
C21	ECSF1CE106	oECST1CD106ZR	Tantalum 10µF 16V
PROCESS VIC-1 BOARD			
Q2	2SD602-QRS	2SD601-QR	Transistor
Q5	2SD602-QRS	2SD601-QR	Transistor
M48	YFV2HA0466A4	oYFV2HA0466B4	Shield Cover-7
PROCESS VIC-2 BOARD			
R3	YF2116152JT	YF2116122JT	Carbon 1.2K ohms 1/16W
C2	ECEA1CKS220	ECEA1CK220	Electrolytic 22µF 16V
PROCESS VIC-3 BOARD			
Q9	2SB709-Q	---------------	Deletion
D1	---------------	MA151KTX	Diode
R6	YF2116133JT	YF2116133GT	Carbon 13K ohms 1/16W
R12	YF2116133JT	YF2116133GT	Carbon 13K ohms 1/16W
R25	-2116912JT	YF2116912GT	Carbon 9.1K ohms 1/16W
R38	YF2116133JT	YF2116133GT	Carbon 13K ohms 1/16W
R42	YF2116332JT	---------------	Deletion
R45	YF2116302JT	YF2116302GT	Carbon 3K ohms 1/16W
R48	YF2116302JT	YF2116302GT	Carbon 3K ohms 1/16W
C18-20	ECEA1EKS4R7	ECEA1ES4R7	Electrolytic 4.7µF 25V
C26	ECEA0JKS470	ECEA0JS470	Electrolytic 47µF 6.3V
C28	ECEA1AKS470	ECEA1AS470	Electrolytic 47µF 10V
C43	YF400103FZT	---------------	Deletion
C31,32	---------------	YF400820CHJT	Ceramic 82pF
CT1	ECRHA120N11	YFTZ03P600YR	Trimmer Capacitor 60pF
L1	EL05S1390K	oNL32T390K	Coil 39µH
HEAD BOARD			
IC8	YFNJM2930L05	NJM2930L05	IC
Q3,4	2SA1022-CTW	2SA1022-C	Transistor
R14	---------------	YF2116912GT	Carbon 9.1K ohms 1/16W

SYMBOL NO.	ORIGINAL PART NO.	NEW PART NO.	DESCRIPTION
R42,43	YF2116912JT	YF2116912GT	Carbon 9.1k ohms 1/16W
R51	YF2116303JT	YF2116303GT	Carbon 30k ohms 1/16W
R59	YF2116472JT	YF2116472GT	Carbon 4.7k ohms 1/16W
R103	YF2116303JT	YF2116303GT	Carbon 30k ohms 1/16W
R107	YF2116472JT	YF2116472GT	Carbon 4.7k ohms 1/16W
R113	YFLP16332J56	LP16332J56	Carbon 3.3k ohms
VR1	YFH0522A10K	oEVML1GA00B14	Variable Resistor 10K ohms
VR2	YFH0522A220K	EVML1GA00B25	Variable Resistor 200K ohms
VR3	YFH0522A100K	EVML1GA00B15	Variable Resistor 100K ohms
VR4,5	YFH0522A10K	oEVML1GA00B14	Variable Resistor 10K ohms
C16	ECEA1AKK100	ECEA1EK100	Electrolytic 10µF 10V
C25,26	ECEA1AKK100	ECEA1EK100	Electrolytic 10µF 10V
C28	ECEA1AKK100	ECEA1EK100	Electrolytic 10µF 10V
C32	ECEA1AKK100	ECEA1EK100	Electrolytic 10µF 10V
C37	ECEA1AKK100	ECEA1EK100	Electrolytic 10µF 10V
C40	ECEA1AKK100	ECEA1EK100	Electrolytic 10µF 10V
C74	ECEA0JKS220	ECEA0JK220	Electrolytic 22µF 6.3V
C81	ECEA1AKS470	ECEA1AS470	Electrolytic 47µF 10V
M54	YFV2HA0454A4	oYFV2HA0454B4	Shield Cover-7
M81	————	oYWV2PA0218A4	Insulator Paper
M82	————	oYWV2PA0219A4	Spacer
PROCESS BOARD			
Q10,11	2SA1022-C	2SA1022-BCTX	Transistor
Q12	2SC2404-CDTW	2SC2404-CDTX	Transistor
D1	MA151K	MA151KTX	Diode
R11	YF2116472JT	YF2116472GT	Carbon 4.7K ohms 1/16W
R13	YF2116302JT	YF2116302GT	Carbon 3K ohms 1/16W
R15	YF2116472JT	YF2116472GT	Carbon 4.7K ohms 1/16W
R25	YF2116472JT	YF2116472GT	Carbon 4.7K ohms 1/16W
R27	YF2116272JT	YF2116752JT	Carbon 7.5K ohms 1/16W
R32	YF2116133JT	YF2116133GT	Carbon 13K ohms 1/16W
R53	YF2116202JT	YF2116242GT	Carbon 2.4K ohms 1/16W
R62	YF2116103JT	————	Deletion
C17	ECEA1CKS220	ECEA1CK220	Electrolytic 22µF 16V
C24,25	ECEA0JKS470	ECEA0JS470	Electrolytic 47µF 6.3V
J29	YF21160R00T	————	Deletion
MOTHER BOARD			
R4	YF2116472JT	YF2116472GT	Carbon 4.7K ohms 1/16W
R16	YF2116133JT	YF2116133GT	Carbon 13K ohms 1/16W
C11	ECEA1EKS4R7	ECEA1ES4R7	Electrolytic 4.7µF 25V
C17	ECEA1AKS470	ECEA1AS470	Electrolytic 47µF 10V
C20	ECEA1AKS470	ECEA1AS470	Electrolytic 47µF 10V
C29	ECEA1EU471	ECEA1ES221	Electrolytic 220µF 25 V for WV-CD132
C101	ECEA1EU221	ECEA1ES221	Electrolytic 220µF 25 V
CN1-JM	YFB02DR	————	Deletion
CN2-JM	YFB03DR	YWB03DR	3 pin Jack Male
CN16,17-JM	YFB02DA-8M	YF02DA-8M	2 pin Jack Male
CONTROL BOARD			
R1	YF2116272JT	YF2116152JT	Carbon 1.5K ohms 1/16W
R2	YF2116132JT	YF2116681JT	Carbon 680 ohms 1/16W
R3	YF2116473GT	YF2116183JT	Carbon 18K ohms 1/16W

SYMBOL NO.	ORIGINAL PART NO.	NEW PART NO.	DESCRIPTION
C1	ECEA0JKS220	ECEA0JK220	Electrolytic 22µF 6.3V
COLOR CCTV CAMERA WV-CD132			
MISCELLANEOUS			
M36	YFV2PA0208A4	oYFV2PA0208B4	Insulator Paper
COLOR CCTV CAMERA WV-CD134			
MISCELLANEOUS			
M36	YFV2PA0208A4	oYFV2PA0208B4	Insulator Paper